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R8C Family

Creating Workspace with MR8C/4

Introduction

Creating workspace with MR8C/4 is a fundamental step towards writing an application that utilizes MR8C/4 RTOS. It is a critical step towards greater understanding and utilization of MR8C/4.

This document describes in detail the entire process of creating a workspace with MR8C/4. The step-by-step guide explanation strives to guide user from the installation of software required to downloading of program for debugging.

Target Device

Applicable MCU: R8C Family

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1. Guide in using this Document

This document aims to equip users with the fundamental knowledge of creating a workspace supporting MR8C/4 thereby providing them with greater ease of using MR8C/4.

With ample pictorial displays, users will find it with ease to follow the step-by-step guide.

Table 1 Explanation of Document Topics

Topic	Objective	Pre-requisite
Preparing the Software	Describe the installation steps for MR8C/4 and corresponding compiler package	None
Opening a Workspace	Guide users in creating a workspace that supports MR8C/4	Knowledge in High-performance Embedded Workshop
Configuring Workspace for MR8C/4	Elaboration on the configurations required based on hardware setups	R8C Family Devices
Downloading Program with E8a Emulator	Guide users in downloading of program using E8a Emulator	Knowledge in High-performance Embedded Workshop and E8a Emulator
Reference Documents	Listing of documents that equip users with knowledge in the pre-requisite requirements	None

2. Preparing the Software

To create a workspace supporting MR8C/4, both Renesas MR8C/4 and C Compiler Package for M16C Series and R8C Family [M3T-NC30WA] of version 5.45 and above must be installed.

2.1 Installing M3T-NC30WA

To begin, double click on the installer for M3T-NC30WA and follow the steps describe below.

Note: Ensure the package is version 5.45 and above.

The figure shows a sequence of five screenshots from the M3T-NC30WA installation process, with arrows pointing to the right where instructions are provided for each step.

- High-performance Embedded Workshop Install Manager:** The first screenshot shows the main installation manager window. The "Standard Install (Recommended)" option is selected. An arrow points to this option with the instruction: "Activate M3T-NC30WA installer package. Install Manager dialog will appeared. Select 'Standard Install' to proceed."
- Choose software:** The second screenshot shows a window where software to be installed is selected. The "M16C Toolchains V.5.45 Release 00" checkbox is checked. An arrow points to this checkbox with the instruction: "Check 'M16C Toolchains V.5.45 Release 00'.. Depress 'Install' button to proceed."
- M16C Series, R8C Family C Compiler V. 5.45 Release 00 License Agreement:** The third screenshot shows the license agreement window. The "Yes" button is highlighted. An arrow points to this button with the instruction: "Read 'License Agreement'. Depress 'Yes' upon agreement to proceed."
- M16C Series, R8C Family C Compiler V. 5.45 Release 00 Input License ID:** The fourth screenshot shows a window where the license ID is entered. The "Next" button is highlighted. An arrow points to this button with the instruction: "Input License ID. Depress 'Next' to proceed."
- M16C Series, R8C Family C Compiler V. 5.45 Release 00 Choose Destination Location:** The fifth screenshot shows a window where the installation destination is chosen. The "Next" button is highlighted. An arrow points to this button with the instruction: "Select installation directory. This will be the same directory to install M3T-MR8C/4. Depress 'Next' button to proceed."

The final screenshot shows a "HewInstMan" dialog box with a warning icon and the text: "The PC will be restarted to complete installation! Click on No button if you want to restart PC later." Below this text are "Yes" and "No" buttons. An arrow points to the "Yes" button with the instruction: "Depress 'Yes' to restart PC and complete installation."

Figure 1 Procedure in M3T-NC30WA Installation

2.2 Installing MR8C/4

To begin, double click on the installer for MR8C/4 and follow the steps describe below.

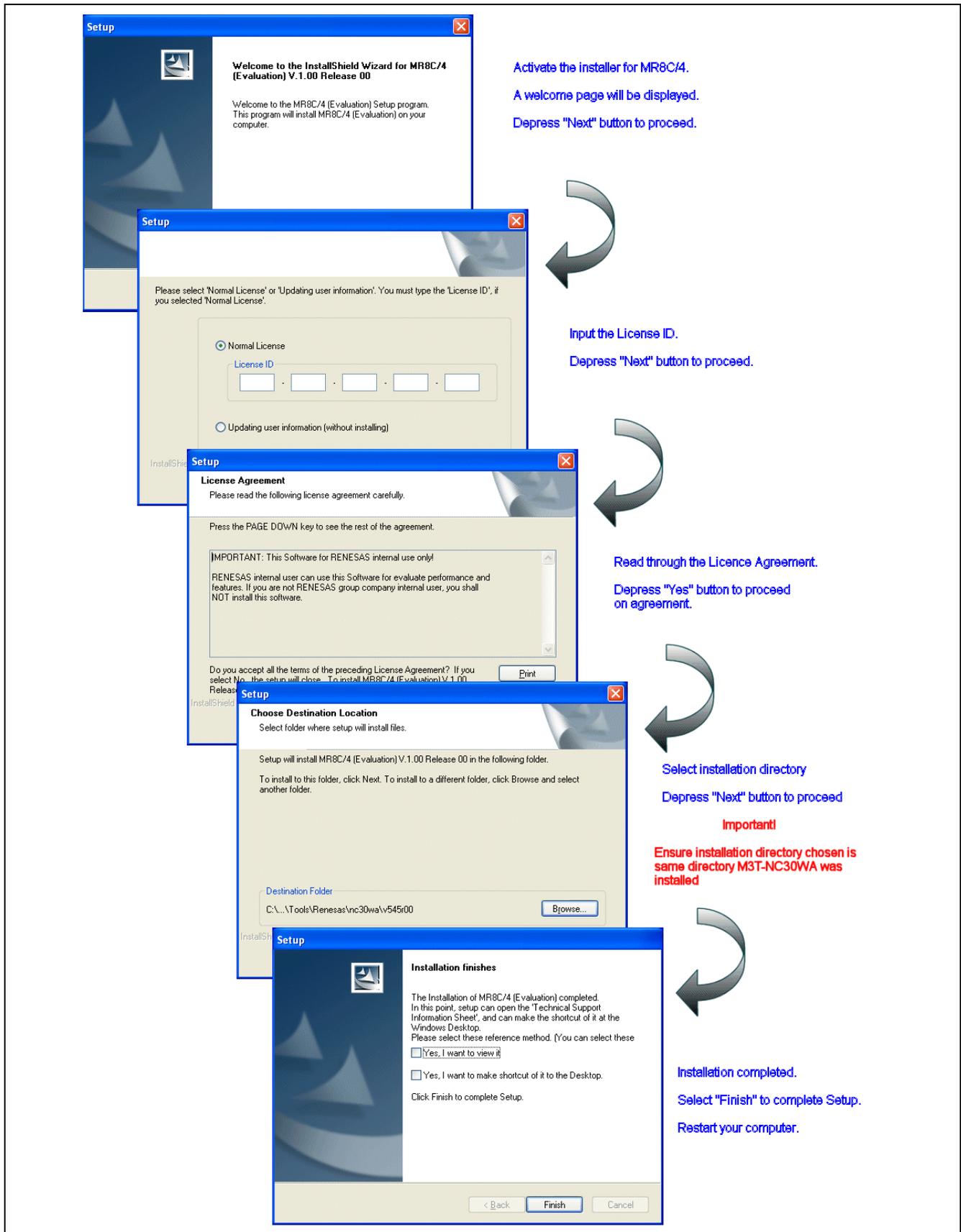


Figure 2 Procedures in MR8C/4 Installation

3. Opening a Workspace

Start High-performance Embedded Workshop and follow the creation procedure describe below.

Welcome!
 Create a new project workspace
 Open a recent project workspace:
 Browse to and open an existing project workspace

Select "Create a new project workspace"
Depress "OK"

New Project Workspace
 Project Types: Application, C source startup Application, Empty Application, Import Makefile, Library, RSKM16C26A, RSKM16C29, RSKM16C62P, RSKM16C94K, RSKR8C13, RSKR8C18, RSKR8C23, RSKR8C25, RSKR8C27
 Workspace Name: FIRST_M31MR8C4_PROG
 Project Name: FIRST_M31MR8C4_PROG
 Directory: C:\WorkSpace\FIRST_M31MR8C4_PROG
 CPU family: M16C
 Tool chain: Renesas M16C Standard

Input workspace name
Select "M16C for CPU family"
Select "Renesas M16C Standard" for Toolchain
Depress "OK" button

New Project-1/6 Select Target CPU.Toolchain version
 Toolchain version: 5.45.00
 Which CPU do you want to use for this project?
 CPU Series: M16C/30, M16C/20, M16C/10, M16C/Tiny, R8C/Tiny
 CPU Group: 23, 24, 25, 26, 27

Select "5.45.00" for Toolchain version
Select "R8C/Tiny" for CPU Series
Select your choice for CPU Group
Depress "Next" button

New Project-2/6 Select RTOS
 Target type: R8C/Tiny
 RTOS: R8C Family MR8C V1.00 Release
 Startup file type: Default C Linkege
 Startup files: Project, C source file, Assembly source file, crt0mx.a30, Assembly include file, c_sec.inc, C header file, Configuration file, template.cfg

Select "R8C/Tiny" for Target type
Select "R8C Family MR8C V1.00" for RTOS
Select "Default C Linkege" for Startup file type
Depress "Next" button

New Project-3/6 Setting the Contents of Files to be Generated
 What kind of initialization routine would you like to create?
 ROM: 32K
 Use Standard I/O Library (UART1)
 Use Heap Memory
 Heap Size: 080H
 Generate main() Function: C source file

Select your choice for ROM
Depress "Next" button

New Project-5/6-Setting the Target System for Debugging
 Targets:
 M16C R8C Simulator
 R8C E8 SYSTEM
 R8C E8a SYSTEM
 External Debugger: none, M16C Family PD30F V.2.20 Release 1, M16C Family PD30SIM V.5.20 Release 1
 Target type: R8C/Tiny
 Target CPU: 25

Place a "Check" against R8C E8a SYSTEM
Select "none" for External Debugger
Verify Target type chosen
Verify Target CPU chosen

Figure 3 Procedures in Creating Workspace with MR8C/4

Upon the creating of workspace, user will see the following file structure.

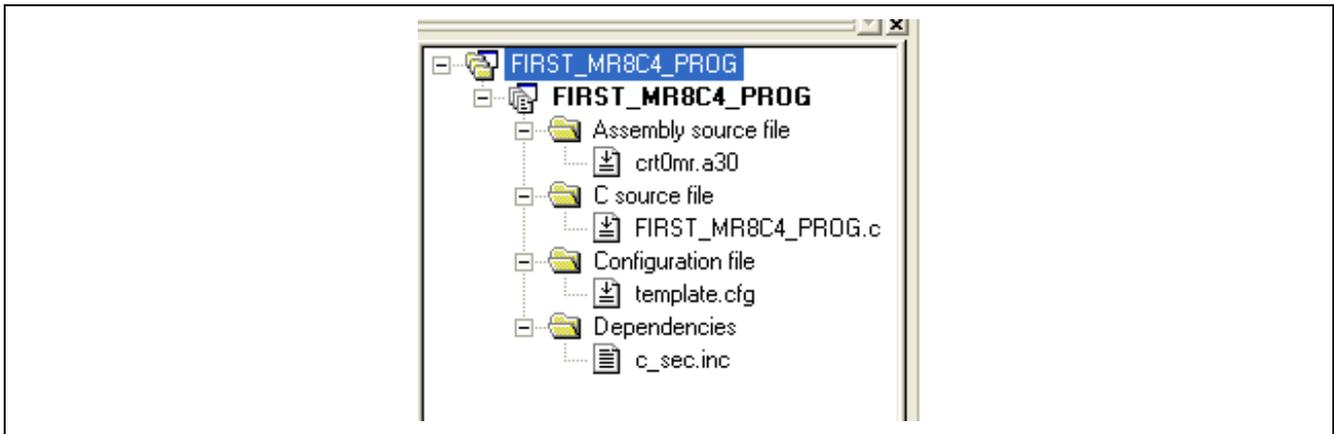


Figure 4 Workspace File Structure

Table 2: Description of Workspace Files

File	Description
crt0mr.a30	The MR8C/4 startup program for C language. Its purpose encompasses initializing RAM data used by MR8C, defining initial startup task, initializing parameters inherent in the application and act as a system clock interrupt handler.
FIRST_MR8C4_PROG.c	The source file for the application.
template.cfg	The configuration file for the definition of MR8C/4 RTOS resources.
c_sec.inc	Included from crt0mr.a30, this file map each section, sets starting addresses of sections, defines size of stack and heap sections, sets interrupt vector table and the fixed vector table.

4. Configuring Workspace for MR8C/4

Before starting off in writing an application and compiling it, user is required to make the following modifications for the proper assembling, compiling and linking of the workspace. It is important to note that not all mentioned modifications are necessary. It depends on the target system and the oscillator mode to be used.

- Defining Oscillator Mode
- Defining Target System
- Modifying toolchain setting
- Adding kernel header files
- Writing the first task

4.1 Defining Oscillator Mode

The *crt0mr.a30* program file does not define the selection of Oscillator. Therefore, a user is required to make the necessary modification to match the target system.

An example of changing the selection to using an external oscillator is illustrated in Figure 5.

```

after reset, this program will start
-----
ldc #(__Sys_Sp&OFFFFH),ISP ; set initial ISP

mov.b #2H, OAH
mov.b #00, PMOD ; Set Processor Mode Register
mov.b #0H, OAH
ldc #00H, FLG
ldc #(__Sys_Sp&OFFFFH),fb
ldc #__SB__,sb

.glob _ConfigureOperatingFrequency
JSR.W _ConfigureOperatingFrequency
    
```

Figure 5 Modification of Oscillator Mode selections in crt0mr.a30

The function “ConfigureOperatingFrequency” can be declared and defined in one of the source files. In this example, an external oscillator mode is selected.

```

/*****
Name:          ConfigureOperatingFrequency
Description:   Sets up operating speed
Parameters:   none
Returns:      none
*****/
void ConfigureOperatingFrequency(void)
{
    prcr = 1; /* Protect off */
    cm13 = 1; /* Xin Xout */
    cm15 = 1; /* XCIN-XCOUT drive capacity select bit : HIGH */
    cm05 = 0; /* Xin on */
    cm16 = 0; /* Main_Task clock = No division mode */
    cm17 = 0; /* Main_Task clock = No division mode */
    cm06 = 0; /* CM16 and CM17 enable */
    /* Waiting for stable of oscillation */
    asm("nop");
    asm("nop");
    asm("nop");
    asm("nop");
    ocd2 = 0; /* Main_Task clock change */
    prcr = 0; /* Protect on */
}

```

Figure 6 Selection of External Oscillator Mode

4.2 Defining Target System

The memory map for different target device varies. For example, R5F21256 and R5F21258 devices from R8C/25 Group have internal ROM that starts from 0x8000H and 0x4000H respectively. To be able to download and run the application successfully, memory section defined in *c_sec.inc* must match the target device.

The default setting of ROM data area in *c_sec.inc* is defined 0x4000H.

```

;-----
; Far ROM data area
;-----
        .section    rom_FE,ROMDATA
        .org        04000H
rom_FE_top:

```

Figure 7 Default setting of ROM Area

If the target device is one that has an internal ROM memory starting from 0x8000H, modifications will be as followed.

```

;-----
; Far ROM data area
;-----
        .section    rom_FE,ROMDATA
        .org        08000H
rom_FE_top:

```

Figure 8 Modified setting of ROM Area to 08000H

4.3 Modifying Toolchain Setting

It is necessary to define the RTOS specification and its version in the toolchain. The following steps provide the guide.

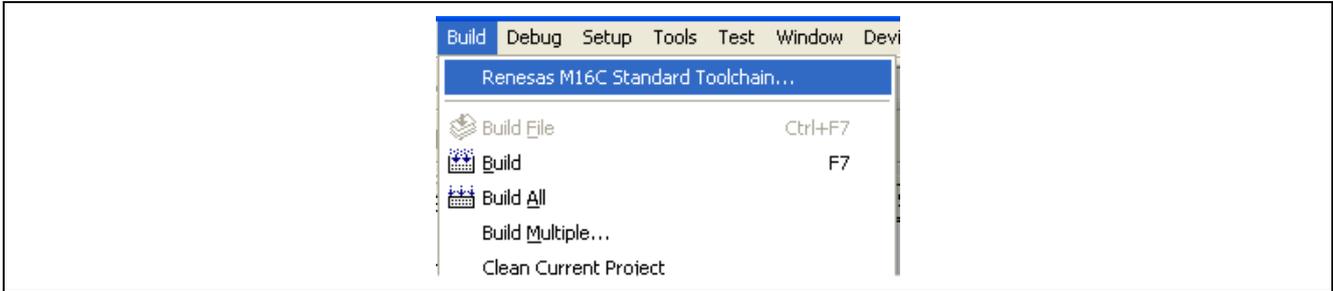


Figure 9 Opening Toolchain

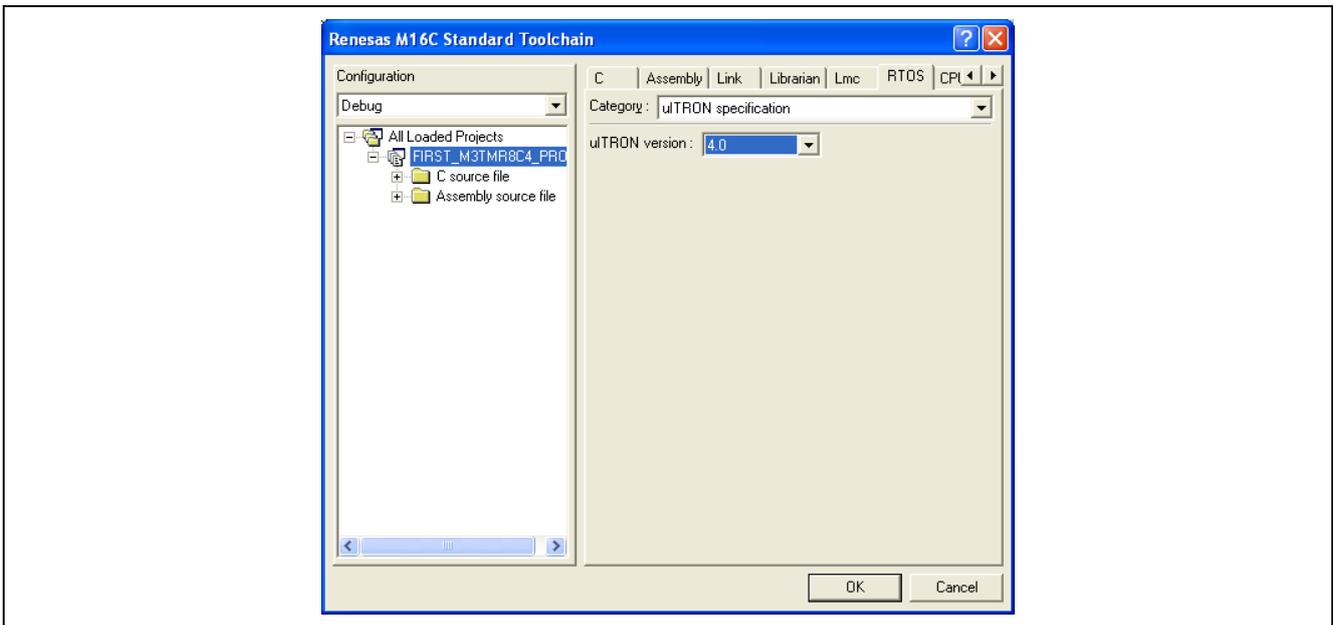


Figure 10 Defining RTOS Specification

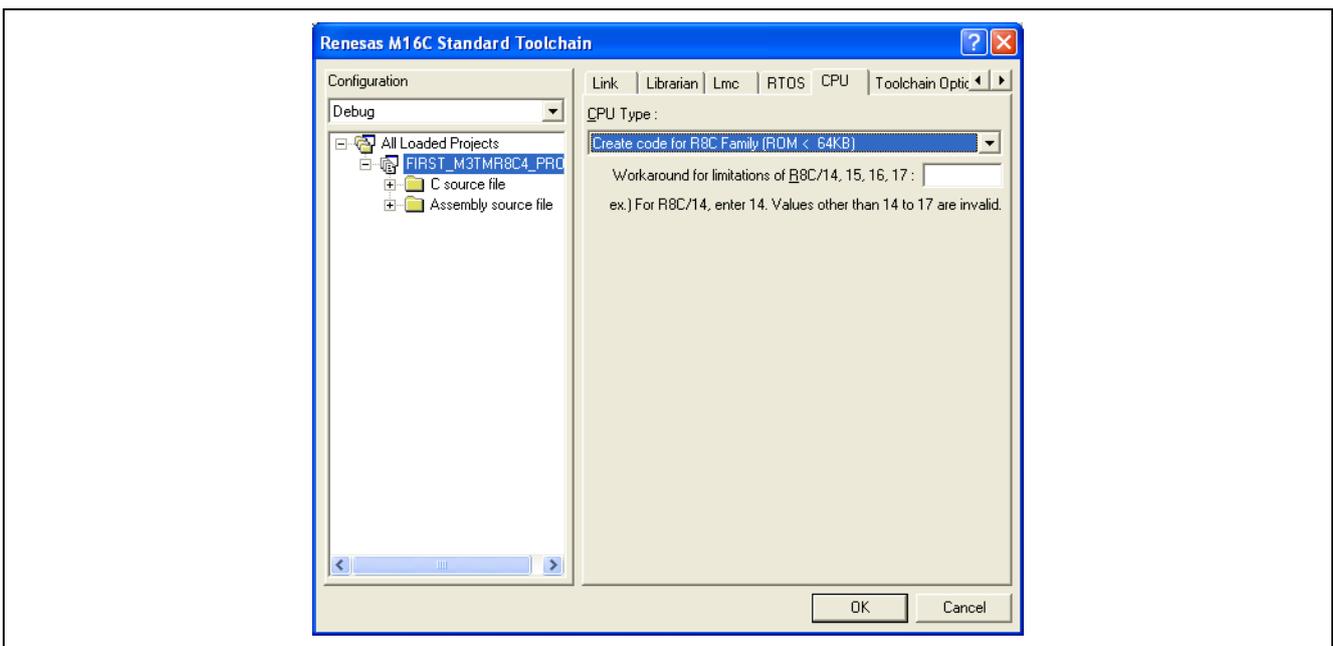


Figure 11 Verifying the CPU Type

4.4 Adding Kernel Header Files

Three kernel header files, namely, “itron.h”, “kernel.h” and “kernel_id.h” need to be included. Table 3 provides a discussion on the functionalities of each file.

Table 3: Explanation on Kernel Header Files

File	Description
itron.h	Contain definitions of data types, constants and macros, and other definitions specified in ITRON General Definitions section
kernel.h	Contain all service call declarations, data types, constants, and macro definitions specified in the kernel specification
kernel_id.h	Automatic assignment header file generated by kernel configurator

```

/*****
Includes <System Includes> , "Project Includes"
*****/
#include <itron.h>
#include <kernel.h>
#include "kernel_id.h"

```

Figure 12 Inclusions of Kernel Header Files

4.5 Writing the First Task

With all the configurations being done, the next step is to proceed in doing the coding. To start off in writing the first task, user will need to enlist a task resource in template.cfg and define the task context in the source file. Figure 13 provides the details on the enlistment of a task resource in template.cfg.

```

// System Definition
system{
    stack_size = 1024;
    priority   = 10;
    system_IPL = 4;
    tic_num    = 1;
    tic_deno   = 1;
};

// system clock definition
clock{
    mpu_clock = 20MHz;
    timer     = RB;
    IPL       = 3;
};

//Task Definition
task[1]{
    name           = ID_TASK1;
    entry_address  = main();
    stack_size     = 100;
    priority       = 1;
    initial_start  = ON;
    exinf          = 0x0;
};

```

Figure 13 Enlistment of RTOS Resource in Template.cfg

Inclusion of system clock definition if service calls from Time Management Module are to be used in this example, Timer RB is selected to be the system clock. User will have a selection of “RA”, “RB” and “OTHER” to choose from. If “OTHER” is selected, user will need to make additional modification in crt0mr.a30 (Figure 14) and assigned a system clock interrupt handler.

```

    .IF USE_TIMER To be replaced with '0'
;+-----+
;|      System timer interrupt setting      |
;+-----+
    mov.b  #stmr_mod_val,stmr_mod_reg  ;set timer mode
    mov.b  #stmr_int_IPL,stmr_int_reg  ;set timer IPL
    mov.b  #stmr_cnt_lower,stmr_ctr_reg  ;set interval
    mov.b  #stmr cnt upper,stmr pre reg  ;set interval
    
```

Figure 14 Disabling of System Timer Interrupt in crt0mr.a30

```

    clock{
        mpu_clock    = 20MHz;
        timer        = OTHER;
        IPL          = 3;
    };
    // Chosen TimerRE as System Clock
    interrupt_vector[10]{
        os_int       = YES;
        entry_address = _SYS_STMR_INH();
    };
    
```

Figure 15 Definition of System Timer Interrupt

Upon the definition of RTOS resource in template.cfg, next step is to declare the task function in the source file. The example above defined entry address of task [1] to be main (). As such, it is mandatory to declare the task function to be “void main (VP_INT stacd)”. User may proceed in the performing a “BUILD ALL” for the workspace.

```

    /*****
    Name:          main
    Description:   Main Fncion
    Parameters:    stacd
    Returns:       none
    *****/
    void main(VP_INT stacd)
    {
        /* Input your codes here */
    }
    
```

Figure 16 Declaration of Task “main ()” in FIRST_MR8C4_PROG.c

5. Downloading Program with E8a Emulator

The final step is to download the program after successfully created the workspace.

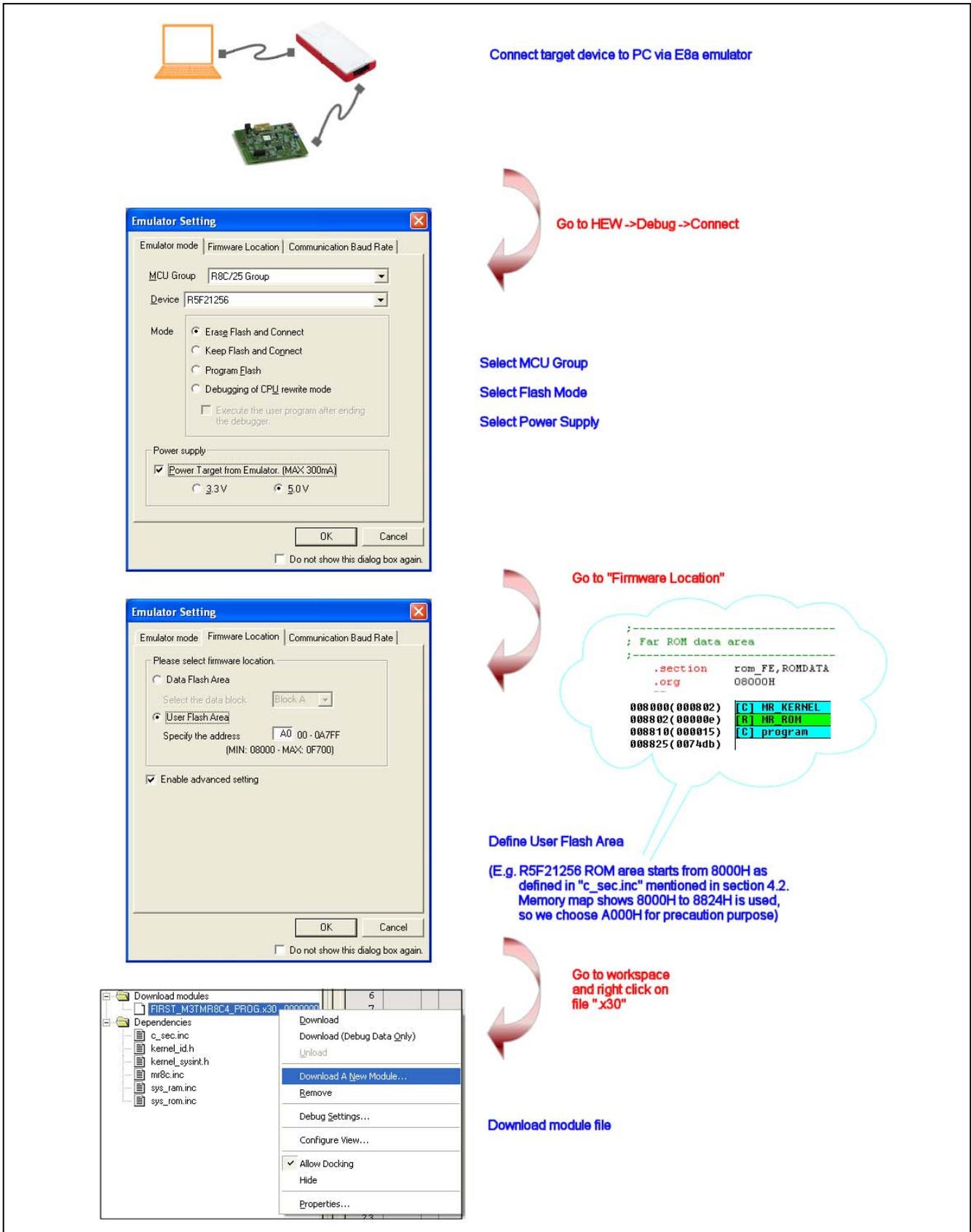


Figure 17 Procedures in Downloading Program with E8a Emulator

6. Reference Documents

User's Manual

- MR8C/4 V1.00 User's Manual
- E8a Emulator User's Manual
- High-performance Embedded Workshop V4.05 User's Manual
- R8C Family Hardware Manual

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