

To our customers,

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Note : Mitsubishi Electric will continue the business operations of high frequency & optical devices and power devices.

Renesas Technology Corp.
Customer Support Dept.
April 1, 2003

M16C/80 Series

Compressing BCD

1.0 Abstract

This program converts 2-digit unpacked BCD data into 1-digit packed BCD.

2.0 Introduction

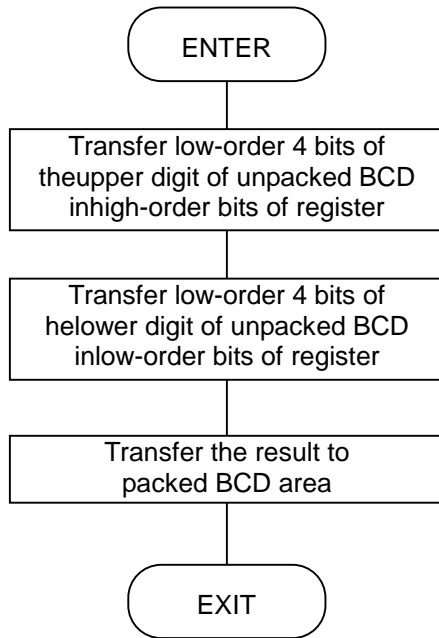
This program converts 2-digit unpacked BCD data into 1-digit packed BCD. Set the 2-digit unpacked BCD data in a variable area (UNPACK_BCDhi, UNPACK_BCDlow). When the program is executed, 1-digit packed BCD data is output to a variable area (PACK_BCD).

The program transfers the low-order 4 bits of the upper digit and the low-order 4 bits of the lower digit of the unpacked BCD in the high-order and the low-order bits of a data creation register by using a 4-bit manipulating instruction as it creates packed BCD.

Subroutine name :	-	ROM capacity :	10byte
Interrupt during execution:	Accepted	Number of stacks used :	None

Register/memory	Input	Output	Usage condition
R0L	-	Packed BCD	Used to create data
R0H	-	-	Unused
R1	-	-	Unused
R2	-	-	Unused
R3	-	-	Unused
A0	-	-	Unused
A1	-	-	Unused
UNPACK_BCDhi	Upper half of unpacked BCD	Does not change	←
UNPACK_BCDlow	Lower half of unpacked BCD	Does not change	←
PACK_BCD	-	Packed BCD	←
Usage precautions			

3.0 Flowchart



4.0 Programming Code

```

*****
;
; *
;   M16C Program Collection
;   CPU : M16C/80 series
; *
;
*****
VramTOP    .EQU    0000400H          ; Declares start address of RAM
VromTOP    .EQU    0FE0000H          ; Declares start address of ROM
Vsb        .EQU    0400H             ; Sets SB
           .SECTION    RAM,DATA
           .ORG        VramTOP        ; RAM area
UNPACK_BCDhi: .BLKB  1                ; Upper digit of unpacked BCD
UNPACK_BCDlow: .BLKB  1                ; Lower digit of unpacked BCD
PACK_BCD:    .BLKB  1                ; Packed BCD
;;
=====
;   Title: Compressing BCD
;   Outline: Converts 2-digit unpacked BCD to 1-digit packed BCD.
;   Input:  ----->
;           R0L()          R0L(Packed BCD)
;           R0H()          R0H(Unused)
;           R1()           R1(Unused)
;           R2()           R2(Unused)
;           R3()           R3(Unused)
;           A0()           A0(Unused)
;           A1()           A1(Unused)
;   Stack amount used: None
;   Notes:
=====
           .SECTION    PROGRAM,CODE
           .ORG        VromTOP        ; ROM area
           .SB        Vsb            ; Declares SB register value
           .SBSYM     UNPACK_BCDhi   ;
           .SBSYM     UNPACK_BCDlow  ;
           .SBSYM     PACK_BCD       ;
MOV LH     UNPACK_BCDhi,R0L          ;
MOV LL     UNPACK_BCDlow,R0L         ;
MOV.B     R0L,PACK_BCD              ;
RTS                                               ;
;
           .END ;

```

5.0 Reference

MCU Technical Information Homepage

<http://www.infocom.maec.co.jp/indexe.htm>

(or <http://www.mdece.com/> , <http://www.mitsubishichips.com/products/mcu/index.html> or your local Web Site.)

Technical Support

E-mail: support@apl.maec.co.jp

(or your local support E-mail address. A private e-mail address should NOT be used.)

Data Sheet

M16C/80 group

(Use the latest version on the Homepage: <http://www.infocom.maec.co.jp/indexe.htm>)

User's Manual

M16C/80 group

(Use the latest version on the Homepage: <http://www.infocom.maec.co.jp/indexe.htm>)

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