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April 1st, 2010
Renesas Electronics Corporation

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# 38D5 Group

## Interrupt (INT1 Interrupt)

## 1. Abstract

The following article introduces and shows an example of how to use the Interrupt (INT1 Interrupt) on the 38D5 Group device.

## 2. Introduction

The application explained in this document applies to the following MCU:
Applicable MCU: 38D5 Group

This sample program may include operations of unused bit functions for the convenience of the SFR bit layout. Set the values according to the operational conditions of the user system.

# 3. Contents

## 3.1 INT1 Interrupt

Outline: Interrupt via $INT_1$ pin.

Specifications:

- The interrupt routine is executed by the falling edge of the $INT_1$ pin.
- Multiple interrupts are disabled.
- Accumulator A, Index register X, and Index register Y are used within the interrupt routine.

Figure 3.1 shows the Connection Diagram, Figure 3.2 shows the Relevant Register Settings and Figure 3.3 shows the Control Procedure

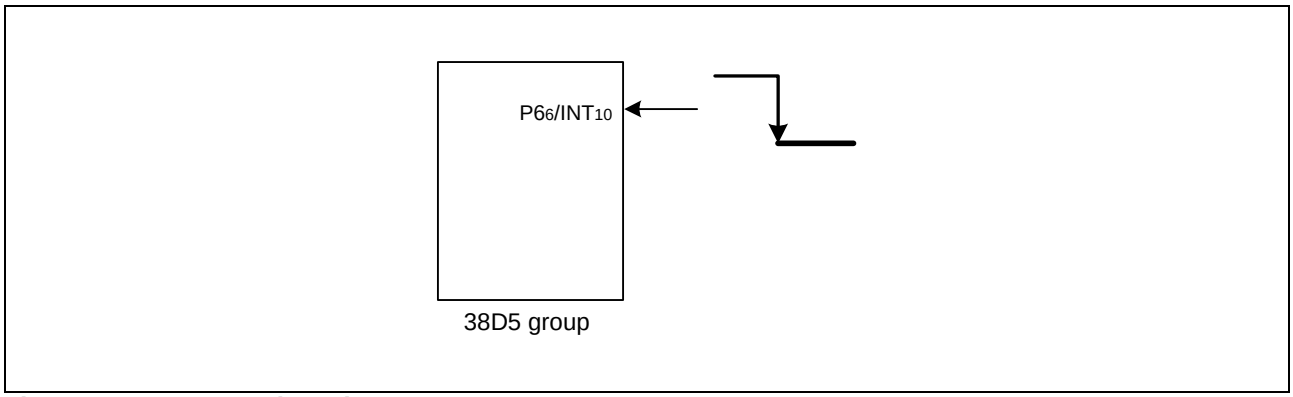


**Figure 3.1 Connection Diagram**

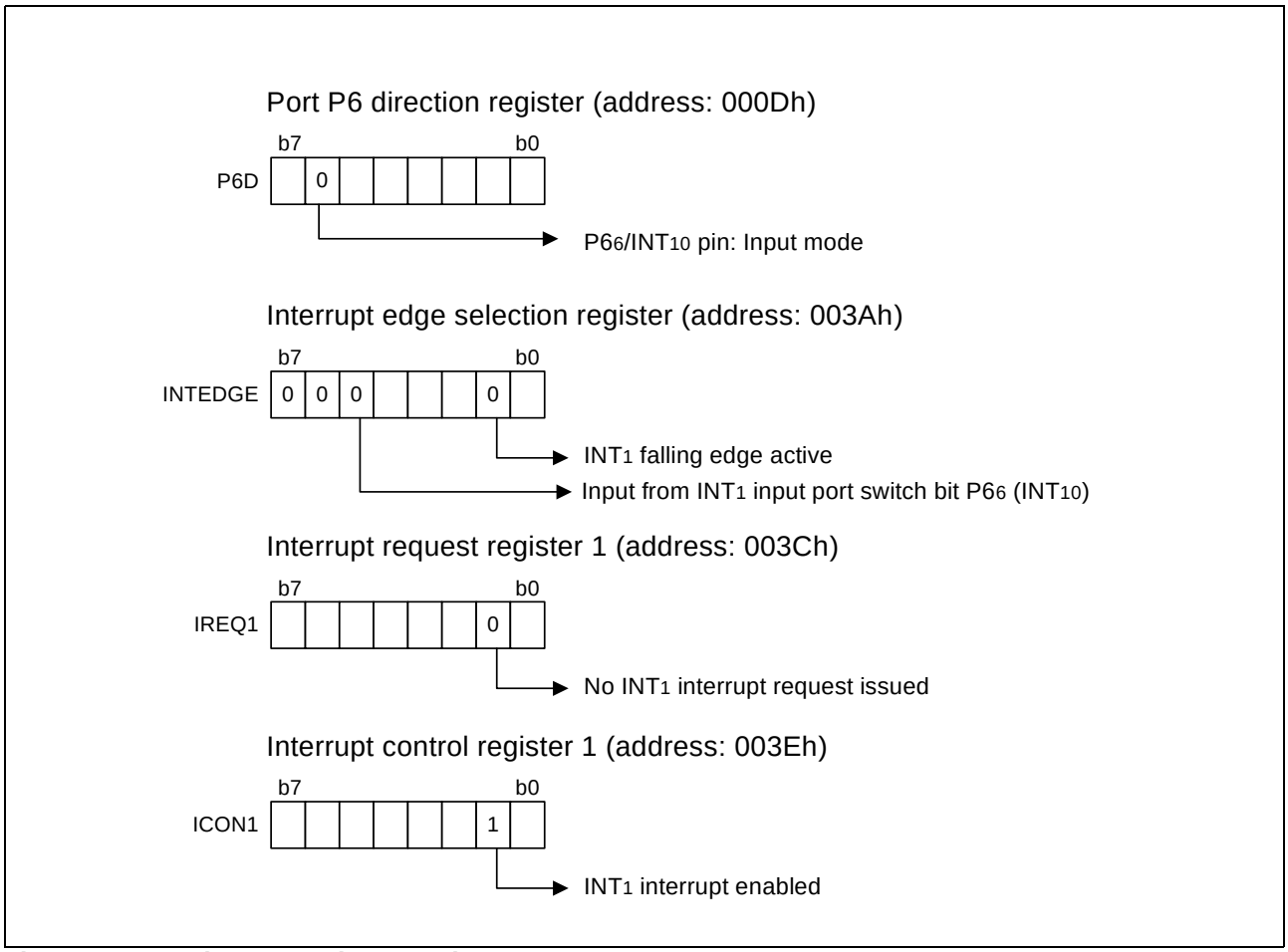


**Figure 3.2 Relevant Register Settings**

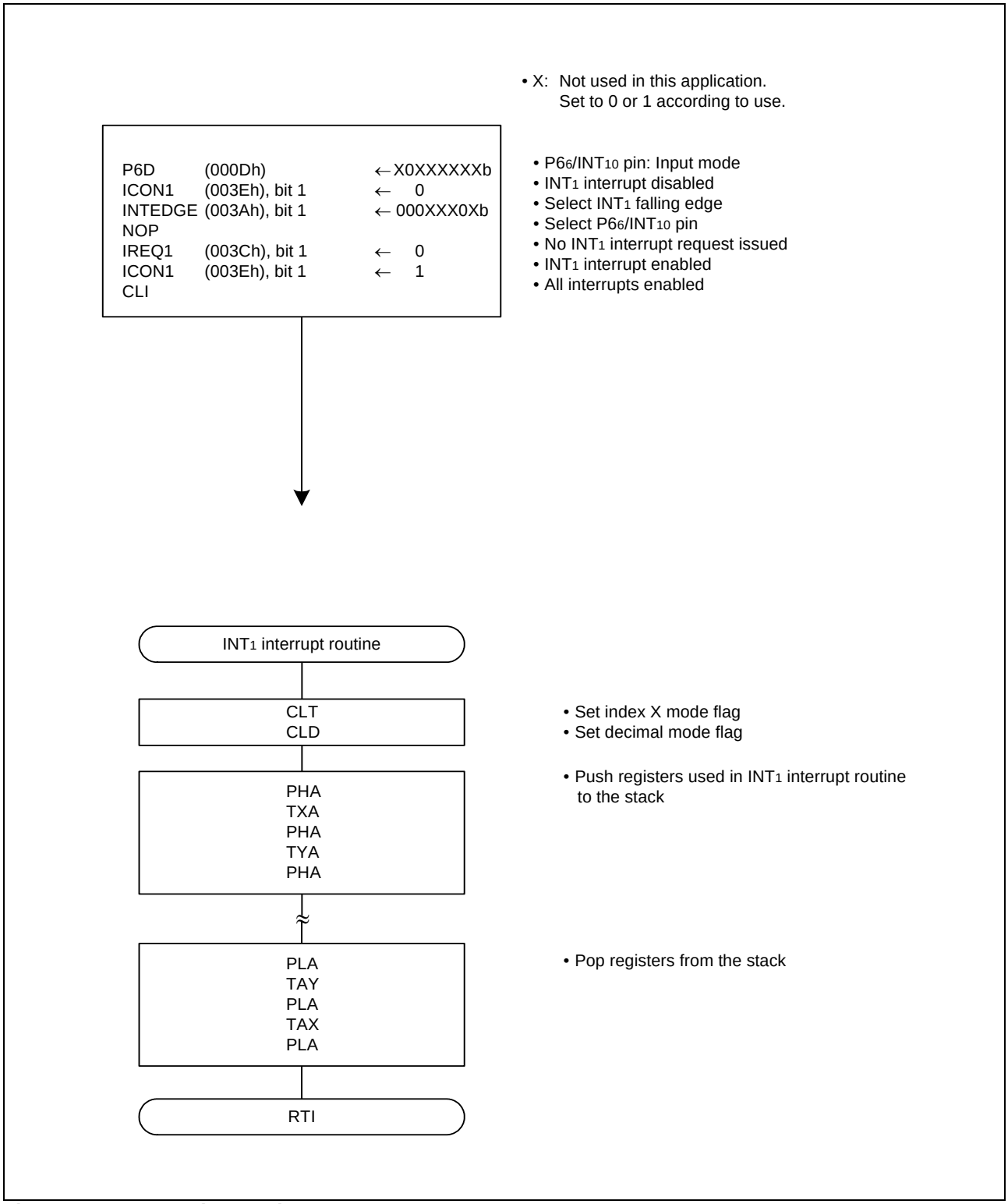


**Figure 3.3 Control Procedure**

## 4. Sample Programming Code

Download a sample program from the Renesas Technology website.
To download, click “Application Notes” in the left side menu on the page of the 38D5 Group.

## 5. Reference Document

Datasheet
38D5 Group Datasheet
Download the latest version from the Renesas Technology website.

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|---|---|

| Rev. | Date | Description | |
|---|---|---|---|
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| | | | |

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