RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU			Document No.	TN-SY*-A016A/E	Rev.	1.00
Title	Additional information for security MPU use in MPU chapter, S3A7 User's Manual Rev.1.20			Information Category	Technical Notification		
Applicable Product	Renesas Synergy™ S3 Series S3A7	Lot No.		Reference Document	S3A7 User's Manual: Microcontrollers, Rev.1.20		
		All lots					

We would inform you of corrections about S3A7 Microcontroller user's manual below.

<Details of corrections>

When using Security MPU, additional describes a note on setting of Security MPU Program Counter Start Address Register.

1. 16.6.1.3 Security MPU Region 0 Start Address Register (SECMPUS0)

   [Before]

   The SECMPUS0 and SECMPUE0 registers specify the security program and the flash data (0000 0000h to 000F FFFFh). These registers can only be accessed from the security program set up by SECMPUPCSn and SECMPUPCEn.

   [After]

   The SECMPUS0 and SECMPUE0 registers specify the security program and the flash data (0000 0000h to 000F FFFFh). These registers can only be accessed from the security program set up by SECMPUPCSn and SECMPUPCEn. Setting of the vector table area is prohibited.

2. 16.6.1.1 Security MPU Program Counter Start Address Register (SECMPUPCSn) (n = 0, 1)

   [Before]

   The secure program is executed in the memory space specified by these registers and can access the secure data specified in the SECMPUSn and SECMPUEn registers.

   [After]

   The secure program is executed in the memory space specified by these registers and can access the secure data specified in the SECMPUSn and SECMPUEn registers.

   A time interval of greater than 12 bytes is required between the last instruction of the non-secure program and the first instruction of the secure program.