

RENESAS TOOL NEWS on June 16, 2014: 140616/tn7

Revision to Device Driver for Enabling Clock-Synchronous Serial Communications Using SCI of RX Family MCUs

We have revised the device driver for enabling clock-synchronous serial communications using the SCI of the RX Family from V.2.01 Release 04 to V.2.01 Release 05.

We supply this device driver free of charge.

Click on the URL for the driver you require below and refer to the block diagram at the link for the software configuration. These pages are to be updated on June 20.

SPI Serial EEPROM Driver

https://www.renesas.com/driver/spi_serial_eeprom

SPI/QSPI Serial Flash Memory, QSPI Serial Phase Change Memory Driver

https://www.renesas.com/driver/spi_serial_flash

1. Description of Revision

The definitions of MCU pins to be used by the sample code have been modified in the file to provide for setting of the RX111 Group (R_SIO_sci.h.rx111).

2. Obtaining Device Driver

(1) Open the following web page for the driver you require below:

https://www.renesas.com/a_eeeprom_driver_app_notes

(page for the SPI serial EEPROM drivers)

https://www.renesas.com/a_serial_flash_driver_app_notes

(page for the SPI/QSPI serial flash memory and QSPI serial phase change memory drivers)

You can also see the list of titles of Application Notes.

(2) Select RX Family in the Select by Products box and click on Search by Products.

Only the application notes for the RX Family will be listed.

(3) In the list, find the following application note:

"RX210, RX21A, RX220, RX63N, RX63T, RX111 Group Clock Synchronous
Single Master Control Software Using the SCI"

This application note will be updated on June 20, 2014.

- (4) Click the link in the Project Files column to the right of the above title.
- (5) Read through the AGREEMENT; then click on Agree.
- (6) Download the sample program from the Download page.

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