

# RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RX*-A180A/E	Rev.	1.00
Title	Restriction for the PH7/XCIN Pin		Information Category	Technical Notification		
Applicable Product	RX110 Group RX111 Group RX113 Group	Lot No.	Reference Document	RX110 Group User's Manual: Hardware Rev.1.20 (R01UH0421EJ0120) RX111 Group User's Manual: Hardware Rev.1.30 (R01UH0365EJ0130) RX113 Group User's Manual: Hardware Rev.1.10 (R01UH0448EJ0110)		
		All				

This document describes a restriction for the PH7/XCIN pin function of the applicable products.

## 1. Note

The PH7/XCIN pin may not be used as a general input port even if the sub-clock generator is stopped by setting the SOSCCR.SOSTP bit to 1 and the RCR3.RTCEN bit to 0.

The pin number of PH7/XCIN pin for each package is listed below.

Package	Pin Number
100-pin LFQFP	18
100-pin TFLGA	G1
64-pin LFQFP and 64-pin LQFP	9
64-pin WFLGA	C1
48-pin LFQFP and 48-pin HWQFN	6

## 2. Restriction

Do not use the PH7/XCIN pin as a general input port.

When the sub-clock is not used, set the RCR3.RTCEN bit to 0 and the SOSCCR.SOSTP bit to 1 and connect the PH7/XCIN pin to VCC or VSS via a resistor.

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