

Report No. MCR-22-0502 July 15,2022

RENESAS SEMICONDUCTOR RELIABILITY REPORT

- GROUP : RL78/L12
- DEVICE : R5F10RXXX
- APPLICATION : Consumer / Industry

Quality Assurance Div. Renesas Electronics Corporation



MCR-22-0502

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Table. Reliability test results (QFP)

Test Items Reference		Test Conditions	Results Failure/Size	Comment
High Temperature Operating Life (HTOL)	JESD22-A108	Ta=125 ℃, Vccmax, 1000 hrs	0/22	
High Temperature Storage Life (HTSL)	JESD22-A103	Ta=150 ℃, 1000 hrs	0/22	
Temperature Humidity bias (THB) (*1)	JESD22-A101	Ta=85 ℃, RH=85 %, Vccmax, 1000 hrs	0/22	
Temperature Cycling (TC) (*1)	JESD22-A104	Ta=-65 ℃ to 150 ℃ , 300 cycles	0/22	
Latch-Up (LU)	JESD78	Pulse Current Injection, I=+/-150 mA	0/3	
Electrostatic discharge (ESD-HBM)	JS-001	1.5 kΩ, 100 pF, +/-2000 V, 1 time	0 pF, +/-2000 V, 1 time 0/3	
Electrostatic discharge (ESD-CDM)	JESD22-C101	+/-500V,1time	e 0/3	
Solderability (SD)	J-STD-002	245 °C, 5 s, Solder coverage ≥95 % $0/5$		
Resistance to Soldering Heat (PC)	JESD22-A113, J-STD-020	MSL3(Moisture Sensitivity Level 3)	0/22	

*1) With preconditioning per JESD22-A113, MSL 3 •It is tested to confirm that all the samples are satisfied with an individual product specification.

Note :

Basically qualification tests were performed using a representative product with the same wafer process and the same package structure .



Table. Reliability test results (QFN)

Test Items	Test Items Reference Test Conditions		Results Failure/Size	Comment
High Temperature Operating Life (HTOL)	JESD22-A108	Ta=125 ℃, Vccmax, 1000 hrs	0/22	
High Temperature Storage Life (HTSL)	JESD22-A103	Ta=150 ℃, 1000 hrs	0/22	
Temperature Humidity bias (THB) (*1)	JESD22-A101	Ta=85 ℃, RH=85 %, Vccmax, 1000 hrs	0/22	
Temperature Cycling (TC) (*1)	JESD22-A104	Ta=-65 ℃ to 150 ℃ , 300 cycles	0/22	
Latch-Up (LU)	JESD78	Pulse Current Injection, I=+/-150 mA	0/3	
Electrostatic discharge (ESD-HBM)	JS-001	1.5 kΩ, 100 pF, +/-2000 V, 1 time	pF, +/-2000 V, 1 time 0/3	
Electrostatic discharge (ESD-CDM)	JESD22-C101	+/-500V,1time	e 0/3	
Solderability (SD)	J-STD-002	245 °C, 5 s, Solder coverage ≥95 % 0/5		
Resistance to Soldering Heat (PC)	JESD22-A113, J-STD-020	MSL3(Moisture Sensitivity Level 3)	0/22	

*1) With preconditioning per JESD22-A113, MSL 3 •It is tested to confirm that all the samples are satisfied with an individual product specification.

Note :

Basically qualification tests were performed using a representative product with the same wafer process and the same package structure .



The failure rate of the device in an actual use condition can be estimated by the below procedure.

• Equation for the failure rate estimation (λ)

 $\lambda = \lambda b \times \pi T$ (FIT)

(1) Unique failure rate (λb)

λb= 3.8 FIT

Unique failure rate at Ta=55 $^{\circ}$ C using 60 $^{\circ}$ confidence level.

②Temperature term (π T)

 π T=exp{11600×Ea×(1/(273+55)-1/(273+Ta))}

Ea: Activation energy (eV)

Ta : Ambient temperature ($^{\circ}$ C)

π T simplified chart as Ea=0.7 eV												
Ta (℃)	40	50	55	60	65	70	75	80	85	90	100	110
πΤ	0.31	0.68	1	1.45	2.08	2.95	4.15	5.77	7.96	10.88	19.82	34.99

•MTTF (Mean Time To Failure)

 $MTTF = 1/\lambda$



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Reference about Renesas package code

Package type	Package code *1	
Lead type plastic package	QFP	PxQP
	SOP	PxSP
Non-lead type plastic package	QFN	PxQN
Grid array type plastic package	BGA	PxBG
	LGA	PxLG

*1. First four digit

Table. Product list

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No	Group	Product part number	Package code	No	Group	Product part number	Package code
1	RL78/L12	R5F10RB8AFP	PLQP0032G*	51			
2	RL78/L12	R5F10RB8GFP	PLQP0032G*	52			
3	RL78/L12	R5F10RBAAFP	PLQP0032G*	53			
4	RL78/L12	R5F10RBAGFP	PLQP0032G*	54			
5	RL78/L12	R5F10RBCAFP	PLQP0032G*	55			
6	RL78/L12	R5F10RBCGFP	PLQP0032G*	56			
7	RL78/L12	R5F10RF8AFP	PLQP0044G*	57			
8	RL78/L12	R5F10RF8GFP	PLQP0044G*	58			
9	RL78/L12	R5F10RFAAFP	PLQP0044G*	59			
10	RL78/L12	R5F10RFAGFP	PLQP0044G*	60			
11	RL78/L12	R5F10RFCAFP	PLQP0044G*	61			
12	RL78/L12	R5F10RFCGFP	PLQP0044G*	62			
13	RL78/L12	R5F10RG8AFB	PLQP0048K*	63			
14	RL78/L12	R5F10RG8GFB	PLQP0048K*	64			
15	RL78/L12	R5F10RGAAFB	PLQP0048K*	65			
16	RL78/L12	R5F10RGAGFB	PLQP0048K*	66			
17	RL78/L12	R5F10RGCAFB	PLQP0048K*	67			
18	RL78/L12	R5F10RGCGFB	PLQP0048K*	68			
19	RL78/L12	R5F10RJ8AFA	PLQP0052J*	69			
20	RL78/L12	R5F10RJ8GFA	PLQP0052J*	70			
21	RL78/L12	R5F10RJAAFA	PLQP0052J*	71			
22	RL78/L12	R5F10RJAGFA	PLQP0052J*	72			
23	RL78/L12	R5F10RJCAFA	PLQP0052J*	73			
24	RL78/L12	R5F10RJCGFA	PLQP0052J*	74			
25	RL78/L12	R5F10RLAAFA	PLQP0064J*	75			
26	RL78/L12	R5F10RLAAFB	PLQP0064K*	76			
27	RL78/L12	R5F10RLAANB	PWQN0064L*	77			
28	RL78/L12	R5F10RLAGFA	PLQP0064J*	78			
29	RL78/L12	R5F10RLAGFB	PLQP0064K*	79			
30	RL78/L12	R5F10RLAGNB	PWQN0064L*	80			
31	RL78/L12	R5F10RLCAFA	PLQP0064J*	81			
32	RL78/L12	R5F10RLCAFB	PLQP0064K*	82			
33	RL78/L12	R5F10RLCANB	PWQN0064L*	83			
34	RL78/L12	R5F10RLCGFA	PLQP0064J*	84			
35	RL78/L12	R5F10RLCGFB	PLQP0064K*	85			
36	RL78/L12	R5F10RLCGNB	PWQN0064L*	86			
37				87			
38			1	88			
39			1	89			
40		1	1	90			
41		1	1	91	1		1
42		1	1	92	1		
43			1	93			
44			1	94			
45				95			
45 46			1	96			
40 47			1	90			
47 48				97			
48 49		1	+	98	+		
49 50			+	100	-		+
50				100			