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M16C/26

Using Timer B for Pulse Period/Width Measurements

1.0 Abstract

The following article introduces and shows how to use Timer B of the M16C/26 (M30262) Flash microcontroller (MCU) for pulse period/width measurements. A sample program written for the MSV30262-SKP, with an accelerometer, shows how to use this Timer B function.

2.0 Introduction

The Renesas M16C/26 is a 16-bit MCU based on the M16C/60 CPU core and has multiple peripheral functions. It has 8 16-bit timers which can be broken into 5 Timer A's and 3 Timer B's. Only Timer B can be used for pulse period/width measurements.

Pulse period/width measurement can be used in applications where in pulse or duty cycles need to be measured. It can be used to measure sensor outputs or verify time periods and duty cycle.

For the demo, pulse outputs from the ADI ADXL210 accelerometer were measured. Using these measurements, acceleration, tilt, or vibration can also be calculated. The demo was written to run on the MSV30262-SKP board with an ADXL210EB evaluation board connected to Timer B0, B1 input pins.

3.0 Timer B

The M16C/26 has three Timer B's, which are all 16-bit timers. The three modes of operations of Timer B are: Timer Mode, Event Counter Mode, and Pulse Period/Pulse Width Measurement Mode. The first two modes are standard timer operations and also exist in the 5 Timer A's. These two modes are discussed in other application notes and are not mentioned in this document. The focus of this document is how to use the third mode, Pulse Period/Pulse Width Measurement Mode. A block diagram of Timer B is shown in Figure 1. Register used in pulse period/width measurement is shown in Figure 2.



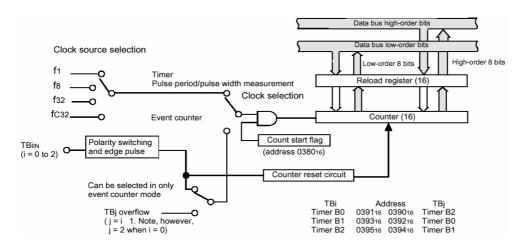
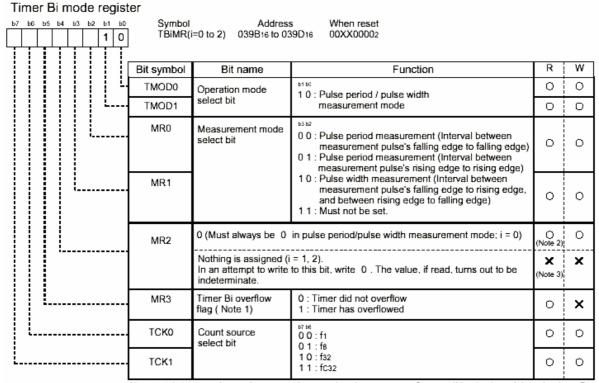


Figure 1 M16C/26 Timer B Block Diagram



Note 1: It is indeterminate when reset. Assume that the count start flag condition is 1 and then the timer Bi overflow flag becomes 1. If the timer Bi mode register has a write access after next count cycle of the timer from the above condition, the timer Bi overflow flag becomes 0. This flag cannot be set to 1 by software.

Note 2: Timer B0. Note 3: Timer B1, timer B2.

Figure 2 Timer B Mode Register in Pulse Period/Width Measurement Mode



3.1 Measurement Modes

There are three modes in which a pulse can be measured: falling edge-to-falling edge, rising edge-to-rising edge, or rising edge-to-falling edge-to-rising edge. The mode can be selected by setting bits 2 and 3 (MR0, MR1) of the Timer B Mode register.

In the demo, two modes are used: rising edge-to-rising edge and rising edge-to-falling edge/falling edge-to-rising edge. The rising edge-to-rising edge mode is used to measure one total cycle (period), t2. The rising edge-to-falling edge/falling edge-to-rising edge mode is used to measure the 'On' time, t1, of the accelerometer. When measuring the 'On' time of the accelerometer, the port pin where the pulse is connected is polled to determine if the measurement should be used or discarded. The port pin is polled after the measurement is taken (i.e. a Timer B interrupt has occurred). If the port pin is low, then the rising edge-to-falling edge ('On' time) is measured is used. If the port pin is high (i.e. falling edge-to-rising edge), the measurement is discarded.

3.2 Count Source

There are four count sources to choose from: f1, f8, f32, or fc32. f1, f8, and f32 are clocks derived from the main clock input, Xin. f1 is the frequency of the main clock, f8 is f1 divided by 8, and f32 is f1 divided 32. On the other hand, fc32 is a clocked derived from the subclock input. fc32 is the frequency of the subclock, fc, divided by 32. On the MSV30262 Board, f1 is 20MHz (f8=2.5MHz, f32=625KHz) and fc is 32.768KHz (fc32=1024Hz).

The importance of the count source is that, it determines the sampling rate of the measurement. For example, if the pulse you need to measure is 1.5ms and you sample it every 1ms, you will miss the width by 0.5ms. If we sample the pulse with every 0.1ms (100us), the pulse measurement will be close to 1.5ms. Figure 3 shows how the sampling clock affects the accuracy of the pulse measurement. As can be seen on the figure, using a faster sampling clock rate makes a more accurate pulse width measurement.

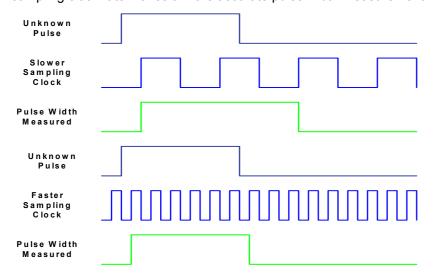


Figure 3 Sampling Clock & Measurement Accuracy



You might be thinking that we should use the fastest sampling clock so we can get the most accurate measurement. We have to remember that the timer must be able to handle the count without overflowing when we used a faster sampling clock. For example, in the demo, the total cycle of the pulse is about 4ms. f1, f8, and f32 can be used as the sampling clock (but not fc32). Timer B is a 16-bit counter, if we used f1, the total count will be 20MHz * 4ms which will be equal to 80,000. This count is beyond the 16-bit counter range (65,536). And so, f8 was used for the demo.

List 1 Timer B Mode Register Initialization for Pulse Period Measurement

List 2 Timer B Mode Register Initialization for Pulse Width Measurement

3.3 Interrupts

When taking pulse period/width measurements, interrupts occur after the period or pulse has been measured and is dependent on the mode selected. If using rising edge-to-rising edge mode, the interrupt is generated during the first rising edge of the count source (sampling clock) after the second rising edge of the pulse is detected. If using rising edge-to-falling edge/falling edge-to-rising edge mode, the interrupt is generated on the next rising edge of the count source for every rising or falling edge of the pulse.

4.0 Pulse Period/Width Measurement Demo Program

The demo program was written to run on the MSV30262-SKP board with Analog Device's ADXL210EB Evaluation Board connected to the M16C/26's Timer B input pins. The 'On' time period of the dual axis accelerometer is shown on the LCD. The LCD display is updated every 2 seconds. X-axis PM, in milliseconds (ms), is shown on the upper line while the Y-axis PM is displayed on the lower line of the LCD. Tilting the board sideways, X- or Y-axis, will change the pulse period displayed. Further modifications of the program will allow acceleration, tilt, vibration, or duty cycle measurements.

If you have an MSV30262-SKP, a copy of the source files can be found under the C:\MTOOL\MSV30262-SKP\Sample_Code\PulsMsmt folder after MSV30262-SKP software installation. The program was compiled using the KNC30 Compiler, which also came with the MSV30262-SKP. It can be modified to suit a user application.



5.0 Conclusion

Pulse Period/Width Measurement mode on the M16C/26 MCU allows a simple method of measuring pulse periods or duty cycles. It can be used in applications where time periods, duty cycles, etc., needs to be measured.

6.0 Reference

Renesas Technology Corporation Semiconductor Home Page

http://www.renesas.com

E-mail Support

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Data Sheets

M16C/26 datasheets, M30262eds.pdf

User's Manual

- M16C/20/60 C Language Programming Manual, 6020c.pdf
- M16C/20/60 Software Manual, 6020software.pdf
- Interrupt Handler App Note, M16C26 Interrupt Handlers in C.doc
- MSV30262-SKP Users Manual, Users_Manual_MSV30262.pdf

7.0 Software Code

The pulse period/width measurement demo routines (in pulsmsmt.c) are shown below. The complete project, written in C, can be compiled/linked using the KNC30 compiler and will be provided upon request. Please contact your Renesas representative for details.



```
/* Timer B0 & B1 irq routines used - vectors modified in sect30 pm.inc */
                 tb0 int
#pragma INTERRUPT
#pragma INTERRUPT
                   tb1 int
unsigned int t2; // t2 period (one total cycle - rising edge to rising edge) unsigned int t1_tb0, t1_tb1; // t1 pulse ('ON' portion of the total cycle (rising edge to
falling edge)
char tb0 flag, tb1 flag = 0; // indicator if Timer B0 or B1 interrupt has occurred
extern char brd mode;
                           // Mode:
                            // INIT : PWM Initialization
                               RUNNING : App running
                           //
                               HOLD : measurements are on-hold
                           //
/*******************************
            init_pm
Parameters:
            none
            none
Returns:
Description: PM initialization routine.
                     For this demo, two timers are used since the accelerometer is
                     dual axis (X & Y). Timer B1 is used for the X-axis and Timer B0
                    is used for the Y-axis.
void init pm(void){
 /* set timer B0 (P9_0) and B1 (P9_1) GPIO ports to inputs, P93 for self test to output */
             // P9 is write protected - disable protection before writing to P9
  prc2 = 1;
                    // set to timer B0 input
  pd9 0 = 0;
  pd9_1 = 0;
                   // set to timer B1 input
  pd9 2 = 1;
                   // unused - set to output
  p9 \ 3 = 0;
                   // accelerometer self test - off
  p_{3_{3}} pd9_3 = 1;
                    // set to output - accelerometer self test
                    // Write protect P9
  prc2 = 0;
  /* Initialize brd mode to PWM initialization */
  brd mode = INIT;
  /* measure t2, the width of one cycle, on both timers. t2 is used for calculation */
  get t2();
  /* set our brd mode to running */
  brd mode = RUNNING;
}
Name:
            get_t2
            none
Parameters:
Returns:
Description: Gets the width of one cycle, t2, and stores it in memory to be used
           for calculation later.
```



asm("FSET I");

```
t2 is measured using timer B0 as it is the same for both X and Y
             channels (see ADXL210 datasheet for details). Timer B0 is initialized
             to measure rising edge to rising edge. By doing this, we will be able
             to measure one cycle (T2), which will be used for later calculation.
             f8 is used because it provides high resolution for measuring our
             pulses (about 5ms cycles). fl is too high that our 16-bit counter
             will always time out. f32/fc32 can be used but may not provide
             enough resolution.
             After measuring t2, the timers are reconfigured for measuring t1,
             i.e. measure rising edge to falling edge.
             While the app is running, it is called to compensate for error due
             to temperature.
*****************************
void get t2(void){
       char second_t2 = 0;
                                     // indicator for second measurement of t2.
                                     \ensuremath{//} 1st irq is indeterminate so we have to wait
                                     // for the second one
   /* disable interrupts before we change timer configuration */
       asm("FCLR I");
                                           // disable interrupts
       tb0ic = 0;
                                            // disable timer B0 irq
       tb1ic = 0;
                                            // disable timer B1 irg
       asm("FSET I");
                                             // enable interrupts
       tb0s = 0;
                                             // stop timer B0
       /* Initialize Timer B0 for t2 measurement */
       tb0mr = 0x46;
                                     // f8, measure rising-to-rising edges,
                                             // pulse width measurement mode
       /* enable timer B0 irq only (timer B1 irq remain disabled) for t2 measurement */
                                            // disable interrupts
       asm("FCLR I");
       tb0ic = 6;
                                             // enable timer B0 irq only
       asm("FSET I");
                                             // enable interrupts
       tb0s = 1;
                                             // start timer B0
       brd mode = INIT;
                                             // change brd mode to INIT for T2 measurement
       /* measure t2 for timer B0 */
       while (second_t2 < 2) { // make two measurements and use the second one
              while (tb0_flag == 0); // wait for the interrupt and t2_tb0 measurement
              ++second_t2;
                                    // increment our second measurement indicator
              tb0 flag = 0;
                                    // initialize flag and wait for second interrupt
       tb0s = 0;
                                             // stop timer B0
   /* disable interrupts before we change timer configuration */
       asm("FCLR I");
                                           // disable interrupts
       tb0ic = 0;
                                            // disable timer B0 irq
```

// enable interrupts



```
/* now that we have t2, let's set switch both timers to measure t1, rising-
      -to-falling edges (or falling-to-rising edges) */
       tb0mr = 0x4A;
                                            // f8, measure rising-to-rising edge, PWM mode
       tb1mr = 0x4A;
                                            // f8, measure rising-to-rising edge, PWM mode
   /* completed PWM initialization, we are ready for our app - enable interrupts*/
       asm("FCLR I");
                                           // disable interrupts
       tb0ic = 6;
                                            // enable timer B0 irq
       tb1ic = 5;
                                            // enable timer B1 irg
       asm("FSET I");
                                            // enable interrupts
       brd mode = RUNNING; // change brd mode back to RUNNING after T2 measurement
   /* start both timers */
       tb0s = 1;
       tb1s = 1;
}
/***********************************
             tb0_int
Parameters:
             None
             None
Returns:
Description: This is the Timer BO, Y-axis, interrupt routine. During PWM
              initialization, it is used to measure t2, period of one total cycle.
              It is called every rising edge.
              When the app is already running, it is used to measure t1 ('ON'
              portion of the total cycle for the Y-axis). It is called every
              rising or falling edge.
              According to ADXL210 datasheet, 'ON' time t1 is time the pulse is
              in a high/Vcc state. We added a condition (p9 0 == 0) so that only
              measurements between rising-and-falling edges are taken but not
              falling-and-rising edges.
              No need to initialize timer B0 count register, tb0, because it is
              re-initialized after an effective edge.
******************************
void tb0 int(void)
{
       if (brd_mode == RUNNING && p9_0 == 0) // are we in RUNNING brd_mode?
              t1 tb0 = tb0;
                                            // read current tb0 count and do an average
       if (brd_mode == INIT) {
                                            // are we in INIT brd mode?
              t2 = tb0;
                                           // yes, read t2 and store it
              tb0 flag = 1;
                                            // set our timer B0 irq flag
       }
}
```



```
/*******************************
            tb1 int
Parameters: None
            None
Returns:
Description: This is the Timer B1, X-axis, interrupt routine.
             When the app is already running, it is used to measure t1 ('ON'
             portion of the total cycle for the X-axis). It is called every
             rising or falling edge.
             According to ADXL210 datasheet, 'ON' time t1 is time the pulse is
             in a high/Vcc state. We added a condition (p9_1 == 0) so that only
             measurements between rising-and-falling edges are taken but not
             falling-and-rising edges.
             No need to initialize timer B1 count register, tb1, because it is
             re-initialized after an effective edge.
***************************
void tb1_int(void)
      if (brd_mode == RUNNING && p9_1 == 0) // are we in RUNNING brd_mode?
             t1 tb1 = tb1;
                                         // read current tb1 count and do an average
}
```

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