

# PG-FP6

Usage of Communications Commands

# Introduction

This document introduces how to use communications commands via a serial interface for programming by the PG-FP6. Using these commands also enables automated control of programming.

# **Target Devices**

RA family RL78 family RX family RH850 family Renesas Synergy<sup>™</sup> microcontrollers RE family Power-management ICs Renesas USB Power Delivery family (C30 group) ICs for driving motors and actuators (ICs for motor control) SuperH family V850 family 78K family

R8C family

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# 1. Overview

The PG-FP6 flash programmer can be run from a host PC by using serial communications commands. Using communications software to manipulate the PG-FP6 also enables automated programming. For details on the PG-FP6, refer to the user's manual.

https://www.renesas.com/pg-fp6

# 2. Basic Procedure up to the Use of Communications Commands

This chapter helps you to understand the sequence of the basic procedures to follow up to the execution of communications commands for the PG-FP6 via communications software, taking the RL78/G13 as an example of the target MCU.

• The descriptions in this chapter apply under the following conditions.

| · · ·                       | 11 2 3                                 |
|-----------------------------|--|
| Host PC interface:          | USB                                    |
| Programming area:           | Area 0                                 |
| Target MCU:                 | R5F100LE (RL78/G13)                    |
| Programming GUI:            | FP6 Terminal V1.04.01                  |
| Communications software:    | Tera Term Version 4.105*               |
| Power supply:               | 5 V supplied from the PG-FP6           |
| Clock supply:               | 32 MHz (on-chip clock oscillator)      |
| Communications interface:   | Single-wire UART (1,000,000 bps)       |
| Security setting:           | Disabled                               |
| Operations of flash memory: | Erasure, programming, and verification |
| Operating mode:             | Chip mode                              |
| Flash options:              | None                                   |
|                             |  |

\*Official Web site of Tera Term: http://ttssh2.osdn.jp

- This chapter describes the procedures listed below. Steps 1 to 4 refer to standard operations assuming the use of the FP6 Terminal. If you already understand these steps, start from step 5.
  - 1. Connecting the host PC and PG-FP6
  - 2. Connecting a target system
  - 3. Creating a setting file
  - 4. Executing commands
  - 5. Executing commands via communications software
  - 6. Creating a macro file
  - 7. Executing the macro file



#### 2.1 Connecting the Host PC

Connect the USB port of the host PC to the PG-FP6 via a USB cable. Also connect the PG-FP6 to the power adapter that comes with the PG-FP6. After that, turn on the power supply switch on the PG-FP6.

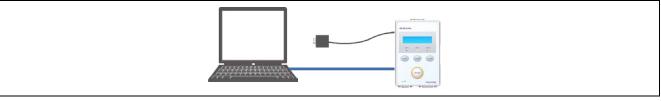


Figure 2.1 Connecting the Host PC

# 2.2 Connecting a Target System

Connect the target cable of the PG-FP6 to the target system.



Figure 2.2 Connecting a Target System



# 2.3 Creating a Setting File

Starting up the FP6 Terminal opens the main window.

| 👼 FP6 Terminal V1.04.01  |   | – 🗆 X   |
|--|---|---|
| <u>File Programmer Target H</u> elp  |   |   |
| 🖪 🗛 🚥 🗅 🖉 🖊 🛛 🌧 🕫  |   |   |
| <pre>&gt;ver<br/>Firmware Version V1.04.00<br/>Board H/W V2<br/>Serial No.: 123456789<br/>Standard mode unsecured<br/>&gt;</pre> | <ul> <li>Programmer</li> <li>Port</li> <li>Version</li> <li>Serial Number</li> <li>Programming Area</li> <li>FP6 Mode</li> <li>Project</li> <li>Setting Name</li> <li>Target Info.</li> <li>Interface</li> <li>Speed</li> <li>Power Suppy</li> <li>Program File</li> <li>File Name</li> <li>Date</li> </ul> | COM4<br>V1.04.00<br>123456789<br>1<br>Standard mode unsecured |
|  |   | Pass  |
|  | ↓ Clear stat  | us and message  |





Select [New Setting File...] from the [File] menu to open the [Create New Setting] dialog box.

| Create New Setting  |                          |    |        | $\times$ |
|---------------------|--------------------------|----|--------|----------|
| Setting Information |                          |    |        |          |
|                     | (Search microcontroller) |    |        |          |
| Family:             | RL78                     |    | ~      |          |
| Group:              | RL78/G13                 |    | ~      |          |
| Target Device:      | R5F100LE                 | ~  | Browse |          |
| Setting             | sample                   |    |        |          |
| Setting Folder:     | C:¥fp6                   |    | Browse |          |
|                     |                          | OK | Gancel |          |
|                     |                          | ОК | Cancel |          |

Figure 2.4 [Create New Setting] Dialog Box

Make the following settings.

[Family]:RL78[Group]:RL78/G13[Target Device]: R5F100LE[Setting]:sample[Setting Folder]: C:\fp6

Clicking on the [OK] button opens the [Setup] dialog box.



Select "C:\fp6\sample.mot" as the program file on the [Program File] tabbed page.

| Program File | October 199       | Diash Caulas  | Flash Ostion | Original California |         |        |    |
|--------------|-------------------|---------------|--------------|---------------------|---------|--------|----|
| ⊢Program Fil | Operation Setting | Block Setting | Flash Option | Connect Setting     |         |        |    |
| 1100101111   |                   |               |              |                     |         |        |    |
|              |                   |               |              |                     |         |        |    |
| C:¥fp        | 6¥sample.mot      |               |              |                     |         | Browse | 1  |
|              |                   |               |              | CRC-32: A9636       | 8C2     |        |    |
|              |                   |               |              |                     |         | Browse |    |
|              |                   |               |              |                     |         |        |    |
|              |                   |               |              |                     |         | Browse | L. |
|              |                   |               |              |                     |         |        |    |
|              |                   |               |              |                     |         | Browse | L. |
|              |                   |               |              |                     |         |        |    |
|              |                   |               |              |                     |         |        |    |
|              |                   |               |              | Do                  | ownload | Cance  | el |

Figure 2.5 [Program File] Tabbed Page



Select [Verify] in the [Command] category on the [Operation Setting] tabbed page.

| Program File | Operation Setting           | Block Setting | Flash Optic | n Connect Setti  | ng       |       |  |
|--------------|-----------------------------|---------------|-------------|--|----------|-------|--|
| <b>⊘</b> Ver | gram<br>If <b>y</b>         |               |             | e Option<br>Erase Chip<br>gram & Verify Opt<br>Verify in the M |          | ~     |  |
|              | ecksum<br>gram Flash Option |               |             |  |          |       |  |
|              |                             |               |             |  | Download | Cance |  |

Figure 2.6 [Operation Setting] Tabbed Page



Make sure that [Chip Mode] is selected for [Mode] on the [Block Setting] tabbed page.

| ro | gram File | Operation Setting | Block Setting | Flash Option | Connect Setti | ng |        |
|----|-----------|-------------------|---------------|--------------|---------------|----|--------|
| ~  |           | on Area           |               |              |               |    |        |
|    | Mode      |                   |               | Chip Mode    |               |    | $\sim$ |
| ~  |           | ccess Window 👘    |               |              |               |    |        |
|    | AW Start  |                   |               |              | 0 (Block 0)   |    | <br>   |
|    | AW End    |                   |               | 0×0000FFF    | F (Block 63)  |    | <br>   |
|    |           |                   |               |              |               |    |        |
|    |           |                   |               |              |               |    |        |
|    |           |                   |               |              |               |    |        |
|    |           |                   |               |              |               |    |        |
|    |           |                   |               |              |               |    |        |
|    |           |                   |               |              |               |    |        |
|    |           |                   |               |              |               |    |        |
|    |           |                   |               |              |               |    |        |
|    |           |                   |               |              |               |    |        |
|    |           |                   |               |              |               |    |        |
|    |           |                   |               |              |               |    |        |
|    |           |                   |               |              |               |    |        |
|    |           |                   |               |              |               |    |        |

Figure 2.7 [Block Setting] Tabbed Page



Make sure that [Do Nothing] is selected for [Set Option] on the [Flash Option] tabbed page.

| Pro | gram File | Operation Setting    | Block Setting | Flash Option | Connect Setting |      |  |
|-----|-----------|----------------------|---------------|--------------|-----------------|------|--|
| ~   | Security  | 2                    |               |              |                 |      |  |
|     | Set Optio | n                    |               | Do Nothing   |                 |      |  |
| ~   |           | nd Protection        |               |              |                 |      |  |
|     |           | Block Erase          |               | No           |                 |      |  |
|     | Disable F |                      |               | No           |                 | <br> |  |
|     | Disable B | Boot Block Cluster I | Programming   | No           |                 |      |  |
|     |           |                      |               |              |                 |      |  |
|     |           |                      |               |              |                 |      |  |
|     |           |                      |               |              |                 |      |  |
|     |           |                      |               |              |                 |      |  |

Figure 2.8 [Flash Option] Tabbed Page



On the [Connect Setting] tabbed page, select "1,000,000" as the communications speed. Also make sure that [Power supply from FP6] is selected and the value of VCC is 5.0 V.

#### Figure 2.9 [Connect Setting] Tabbed Page

Click on the [Download] button to download a parameter file, setting file, and program file to be stored in the PG-FP6.



| 👼 FP6 Terminal V1.04.01  |        |                  | _            |          | ×     |
|--|--------|------------------|--------------|----------|-------|
| <u>File Programmer Target H</u> elp  |        |                  |              |          |       |
| 📴   🛶 🚥   🔄 🛷 🦯 🕕 🥔 🌮  |        |                  |              |          |       |
|  | A [    | Programmer       |              |          |       |
| >ver   |        | Port             | COM4         |          |       |
| Firmware Version V1.04.00  |        | Version          | V1.04.00     |          |       |
| Board H/W V2<br>Serial No.: 123456789                                      |        | Serial Number    | 123456789    |          |       |
| Standard mode unsecured  |        | Programming Area | 1            |          |       |
| >  |        | FP6 Mode         | Standard m   | ode unse | cured |
| >downprm<br>Now loading  |        | Project          |              |          |       |
| PASS   |        | Setting Name     | sample.es6   |          |       |
| >downset fname="sample.es6"  |        | Target Info.     | R5F100LE     |          |       |
| Now loading  |        | Talget 1110.     | V1.12        |          |       |
| PASS   |        | <b>T</b> . (     | 1 wire UAR   | <b>-</b> |       |
| >lod fname="sample.mot" ftime="2017-09-11 15:02"<br>Preparing storage PASS |        | Interface        |              | .1       |       |
| Now loading  |        | Speed            | 1,000,000    |          |       |
| Address range code: 0x00000000 to 0x0000FFFF, CRC32: 0xC24FAB3F            |        | Power Suppy      | 5V           |          |       |
| PASS   |        | Program File     |              |          |       |
| >  |        | File Name        | sample.mot   |          |       |
|  |        | Date             | 11/09/2017   |          |       |
|  |        | Code CBC         | C24FAB3F     |          |       |
|  |        | Code CRC         | C24FAB3F     |          |       |
|  |        |                  |              |          |       |
|  |        |                  |              |          |       |
|  |        |                  |              |          |       |
|  |        |                  |              |          |       |
|  |        |                  | Pass         |          |       |
|  | $\sim$ | Clear stat       | us and messa | ige      |       |

Figure 2.10 Downloading



# 2.4 Executing Commands

Selecting [Start] from the [Target] menu executes erasure, programming, and verification of the flash memory in the R5F100LE.

| FP6 Terminal V1.04.01  |   |   | _ | ×     |
|--|---|---|---|-------|
| <u>File P</u> rogrammer <u>T</u> arget <u>H</u> elp  |   |   |   |       |
| 📴   🛺 🚥   🖣 🛷 🦯 🕕 🥔 🌮  |   |   |   |       |
| <pre>&gt;ver<br/>Firmware Version V1.04.00<br/>Board H/W V2<br/>Serial No.: 123456789<br/>Standard mode unsecured<br/>&gt;<br/>downprm<br/>Now loading<br/>PASS<br/>&gt;downset fname="sample.es6"<br/>Now loading<br/>PASS<br/>&gt;lod fname="sample.mot" ftime="2017-09-11 15:02"<br/>Preparing storage PASS<br/>Now loading<br/>Address range code: 0x00000000 to 0x0000FFFF, CRC32: 0xC24FAB3F<br/>PASS<br/>&gt;</pre> |   | Programmer<br>Port<br>Version<br>Serial Number<br>Programming Area<br>FP6 Mode<br>Project<br>Setting Name<br>Target Info.<br>Interface<br>Speed<br>Power Suppy<br>Program File<br>File Name<br>Date<br>Code CRC |   | cured |
|  | ÷ |   |   |       |



Select [Exit] from the [File] menu to close the FP6 Terminal.



# 2.5 Executing Commands via the Communications Software

Starting up Tera Term opens the [New connection] dialog box.

Select [Serial] and "COMx: PG-FP6(CDC) (COMx)", where x indicates the number of the port you wish to use, as the port. Click on the [OK] button to open the main window.

#### Figure 2.12 [New connection] Dialog Box

Press the Enter key, and the ">" (prompt) character appears on the screen.

| 🔟 COM                     | 4 - Tera T    | erm VT           |                |              | _ | ×      |
|---------------------------|---------------|------------------|----------------|--------------|---|--------|
| <u>F</u> ile <u>E</u> dit | <u>S</u> etup | C <u>o</u> ntrol | <u>W</u> indow | <u>H</u> elp |   |        |
|                           |               |                  |                |              |   | ^      |
| >                         |               |                  |                |              |   |        |
| >                         |               |                  |                |              |   |        |
|                           |               |                  |                |              |   |        |
|                           |               |                  |                |              |   |        |
|                           |               |                  |                |              |   |        |
|                           |               |                  |                |              |   |        |
|                           |               |                  |                |              |   |        |
|                           |               |                  |                |              |   |        |
|                           |               |                  |                |              |   |        |
|                           |               |                  |                |              |   |        |
|                           |               |                  |                |              |   |        |
|                           |               |                  |                |              |   |        |
|                           |               |                  |                |              |   |        |
|                           |               |                  |                |              |   |        |
|                           |               |                  |                |              |   |        |
|                           |               |                  |                |              |   |        |
|                           |               |                  |                |              |   | $\sim$ |

#### Figure 2.13 Main Window



Enter "ep" to execute erasure, programming, and verification of the flash memory in the R5F100LE.

| 🔟 COM4 - Tera Term VT   |               |                          | _ |   | × |
|---|---------------|--------------------------|---|---|---|
| <u>File Edit Setup Control</u>                                | Window Help   |                          |   | _ |   |
| >   | <u></u>       |                          |   |   | ^ |
| >ep   |               |                          |   |   |   |
| Blank check Skipped.<br>Erase Code Flash:                     |               |                          |   |   |   |
| PASS  |               |                          |   |   |   |
| Blank check Skipped   |               |                          |   |   |   |
| Erase Data Flash:<br>PASS                                     |               |                          |   |   |   |
| Program Code Flash:   |               |                          |   |   |   |
| 10%   |               |                          |   |   |   |
| 20%<br>30%  |               |                          |   |   |   |
| 40%   |               |                          |   |   |   |
| 50%   |               |                          |   |   |   |
| 60%<br>70%  |               |                          |   |   |   |
| 80%   |               |                          |   |   |   |
| 90%   |               |                          |   |   |   |
| 100%<br>PASS  |               |                          |   |   |   |
| Program Data Flash:   |               |                          |   |   |   |
| 10%   |               |                          |   |   |   |
| 20%<br>30%  |               |                          |   |   |   |
| 40%<br>50%  |               |                          |   |   |   |
| 50%<br>60%  |               |                          |   |   |   |
| 70%   |               |                          |   |   |   |
| 80%   |               |                          |   |   |   |
| 90%<br>100%   |               |                          |   |   |   |
| PASS  |               |                          |   |   |   |
| Verify Code Flash:<br>10%                                     |               |                          |   |   |   |
| 20%   |               |                          |   |   |   |
| 30%   |               |                          |   |   |   |
| 40%<br>50%  |               |                          |   |   |   |
| 10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%                 |               |                          |   |   |   |
| 70%<br>80%  |               |                          |   |   |   |
| 90%   |               |                          |   |   |   |
| 100%  |               |                          |   |   |   |
| PASS<br>Verify Data Flash:                                    |               |                          |   |   |   |
| 10%   |               |                          |   |   |   |
| 20%   |               |                          |   |   |   |
| 30%<br>40%  |               |                          |   |   |   |
| 50%   |               |                          |   |   |   |
| 60%<br>70%  |               |                          |   |   |   |
| 30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%<br>PASS |               |                          |   |   |   |
| 90%   |               |                          |   |   |   |
| 100%<br>PASS  |               |                          |   |   |   |
| E <u>r</u> ase,Program operat                                 | ion finished. |                          |   |   |   |
| $\geq$  |               |                          |   |   | * |
| -   |               | <b>F</b> ue and <b>b</b> |   |   |   |

Figure 2.14 Executing a Command

# 2.6 Creating a Macro File

Enter the following code in a text editor and save it as "C:\fp6\sample.ttl".

sendln 'ep'

end





# 2.7 Executing the Macro File

Select [Macro] from the [Control] menu and open the "sample.ttl" macro file. This executes erasure, programming, and verification of the flash memory in the R5F100LE.

| Image: Condent Term VT   |  |   |   |
|--|--|---|---|
| > cp<br>Blank check Skipped.<br>Erase Code Flash:<br>PASS<br>Blank check Skipped.<br>Erase Data Flash:<br>PASS<br>Program Code Flash:<br>10%<br>20%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%<br>PASS<br>Program Data Flash:<br>10%<br>20%<br>80%<br>90%<br>90%<br>100%<br>PASS<br>Verify Code Flash:<br>10%<br>20%<br>80%<br>90%<br>90%<br>100%<br>PASS<br>Verify Data Flash:<br>10%<br>80%<br>90%<br>90%<br>90%<br>100%<br>PASS<br>Verify Data Flash:<br>10%<br>80%<br>90%<br>90%<br>100%<br>PASS<br>Verify Data Flash:<br>10%<br>80%<br>90%<br>90%<br>100%<br>PASS<br>Verify Data Flash:<br>10%<br>20%<br>80%<br>90%<br>100%<br>PASS<br>Verify Data Flash:<br>10%<br>20%<br>80%<br>90%<br>10%<br>20%<br>20%<br>20%<br>20%<br>20%<br>20%<br>20%<br>20%<br>20%<br>2   | 🔟 COM4 - Tera Term VT                      | - |   |
| >ce           Blank check Skipped.           Frase Code Flash:           PASS           Blank check Skipped.           Frase Data Flash:           PASS           Program Code Flash:           10%           20%           30%           40%           50%           80%           90%  | <u>File Edit Setup Control Window Help</u> |   |   |
| Blank check Skipped.<br>Frase Code Flash:<br>PASS<br>Blank check Skipped.<br>Frase Data Flash:<br>Program Code Flash:<br>10%<br>20%<br>20%<br>20%<br>20%<br>20%<br>20%<br>80%<br>90%<br>90%<br>90%<br>90%<br>90%<br>90%<br>90%<br>9  | >  |   | ^ |
| Erase Code Flash:<br>PASS<br>Pass<br>Program Code Flash:<br>103<br>203<br>203<br>203<br>203<br>203<br>203<br>203<br>2  | >ep  |   |   |
| PASS<br>Blank check Skipped.<br>Frase Data Flash:<br>PYOgram Code Flash:<br>10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%<br>PYOgram Data Flash:<br>10%<br>80%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>90%<br>100%<br>10  | Blank check Skipped.                       |   |   |
| Blank check Skipped.<br>Frase Data Flash:<br>PASS<br>Program Code Flash:<br>10%<br>20%<br>20%<br>20%<br>20%<br>20%<br>20%<br>20%<br>2  | Erase Code Flash:                          |   |   |
| Erase Data Flash:<br>PASS<br>Program Code Flash:<br>103<br>203<br>303<br>403<br>508<br>603<br>708<br>803<br>903<br>100%<br>PASS<br>Program Data Flash:<br>108<br>208<br>308<br>408<br>208<br>308<br>408<br>508<br>603<br>708<br>808<br>908<br>100%<br>PASS<br>Verify Code Flash:<br>108<br>208<br>508<br>508<br>609<br>708<br>808<br>908<br>100%<br>PASS<br>Verify Data Flash:<br>108<br>208<br>308<br>408<br>503<br>603<br>704<br>805<br>Verify Data Flash:<br>108<br>208<br>308<br>408<br>503<br>603<br>704<br>805<br>Verify Data Flash:<br>108<br>208<br>308<br>408<br>503<br>603<br>704<br>805<br>908<br>100%<br>705<br>807<br>808<br>908<br>100%<br>708<br>808<br>908<br>100%<br>708<br>808<br>908<br>100%<br>708<br>808<br>908<br>100%<br>708<br>808<br>908<br>100%<br>708<br>808<br>908<br>100%<br>708<br>808<br>908<br>100%<br>708<br>808<br>908<br>100%<br>708<br>808<br>908<br>100%<br>708<br>808<br>908<br>100%<br>708<br>808<br>908<br>100%<br>708<br>808<br>808<br>808<br>808<br>808<br>808<br>808  |  |   |   |
| PASS<br>Program Code Flash:<br>10%<br>20%<br>20%<br>40%<br>50%<br>60%<br>70%<br>80%<br>100%<br>PASS<br>Program Data Flash:<br>10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>80%<br>80%<br>80%<br>80%<br>80%<br>80%<br>8  | Blank check Skipped.<br>E D-t- El          |   |   |
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| 100%<br>PASS<br>Verify Code Flash:<br>10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%<br>PASS<br>Verify Data Flash:<br>10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%   | 70%  |   |   |
| 100%<br>PASS<br>Verify Code Flash:<br>10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%<br>PASS<br>Verify Data Flash:<br>10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%   | 80%  |   |   |
| Verify Code Flash:<br>10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%<br>PASS<br>Verify Data Flash:<br>10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%<br>PASS   | 90%<br>100%                                |   |   |
| Verify Code Flash:<br>10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%<br>PASS<br>Verify Data Flash:<br>10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%<br>PASS   | 100%<br>PASS                               |   |   |
| 10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%<br>PASS<br>Verify Data Flash:<br>10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%   | Verify Code Elash:                         |   |   |
| 100%<br>PASS<br>Verify Data Flash:<br>10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%  | 10%  |   |   |
| 100%<br>PASS<br>Verify Data Flash:<br>10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%  | 20%  |   |   |
| 100%<br>PASS<br>Verify Data Flash:<br>10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%  | 30%  |   |   |
| 100%<br>PASS<br>Verify Data Flash:<br>10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%  | 40%  |   |   |
| 100%<br>PASS<br>Verify Data Flash:<br>10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%  | 50%  |   |   |
| 100%<br>PASS<br>Verify Data Flash:<br>10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%  | 6U%  |   |   |
| 100%<br>PASS<br>Verify Data Flash:<br>10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%  | 00%  |   |   |
| 100%<br>PASS<br>Verify Data Flash:<br>10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%  | 90%  |   |   |
| Verify Data Flash:<br>10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%  | 100%                                       |   |   |
| Verify Data Flash:<br>10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%  | PASS                                       |   |   |
| 10%<br>20%<br>30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%  | Verify Data Flash:                         |   |   |
| 30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%  | 10%  |   |   |
| 30%<br>40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%<br>PASS<br>Erase,Program operation finished.   | 20%  |   |   |
| 40%<br>50%<br>60%<br>70%<br>80%<br>90%<br>100%<br>PASS<br>Erase,Program operation finished.<br>>   | 30%  |   |   |
| SU%<br>60%<br>70%<br>80%<br>90%<br>100%<br>PASS<br>Erase,Program operation finished.<br>>  | 40%  |   |   |
| 00%<br>70%<br>80%<br>90%<br>100%<br>PASS<br>Erase,Program operation finished.<br>>   | 50%  |   |   |
| 70%<br>80%<br>90%<br>100%<br>PASS<br>Erase,Program operation finished.<br>>  | 0U%  |   |   |
| 90%<br>100%<br>PASS<br>Erase,Program operation finished.<br>>  | 706<br>009                                 |   |   |
| 100%<br>PASS<br>Erase,Program operation finished.  | 00%<br>QN%                                 |   |   |
| PASS<br>Erase,Program operation finished.  | 100%                                       |   |   |
| Erase,Program operation finished.  | PASS                                       |   |   |
| >  | Erase,Program operation finished.          |   |   |
|  | $\geq$                                     |   | ~ |

Figure 2.16 Executing the Macro File



#### 3. Examples of Executing Communications Commands from Macro Files

This chapter describes automated programming via Tera Term by using macro files that include PG-FP6 communications commands. For details on the communications commands, refer to the user's manual.

https://www.renesas.com/pg-fp6

#### 3.1 Downloading a Parameter File, Setting File, and Program File

The **downprm**, **downset**, and **lod** commands can be used to download parameter files, setting files, and programs files, respectively.

The following is an example of downloading the parameter file "R5F100LE.pr5", setting file "sample.es6", and program file "sample.mot".

sendIn 'downprm'
wait 'Now loading...'
sendfile 'C:\fp6\sample\R5F100LE.pr5' 1
wait '>'
sendIn 'downset'
wait 'Now loading...'
sendfile 'C:\fp6\sample\sample.es6' 1
wait '>'
sendIn 'lod fname="sample.mot" ftime="2017-09-11 15:02"'
wait 'Now loading...'
sendfile 'C:\fp6\sample.mot' 1
end"

Executing this macro file leads to the following output in Tera Term.

| >downprm  |
|---|
| Now loading   |
| PASS  |
| >downprm  |
| Now loading   |
| PASS  |
| >downset  |
| Now loading   |
| PASS  |
| >lod fname="sample.mot" ftime="2017-09-11 15:02"                |
| Preparing storage PASS  |
| Now loading   |
| Address range code: 0x00000000 to 0x0000FFFF, CRC32: 0xC24FAB3F |
| PASS  |
| >   |



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# 3.2 Programming Multiple Program Files

Using the **add** option of the **lod** command allows you to download multiple program files.

The following example is for the downloading and programming of "sample1.mot" and "sample2.mot".

| sendIn 'lod fname="sample1.mot" ftime="2020-06-18 16:24"'    |
|--|
| wait 'Now loading'   |
| sendfile 'C:\fp6\sample1.mot' 1                              |
| wait '>'   |
| sendIn 'lod add fname="sample2.mot" ftime="2020-06-18 16:26" |
| wait 'Now loading'   |
| sendfile 'C:\fp6\sample2.mot' 1                              |
| wait '>'   |
| sendin 'ep'  |
| end  |

Executing this macro file leads to the following output in Tera Term.

| >lod fname="sample1.mot" ftime="2020-06-18 16:24"               |
|---|
| Preparing storage PASS  |
| Now loading   |
| Address range code: 0x00000000 to 0x00007FFE, CRC32: 0xC7D7C74C |
| PASS  |
| >lod add fname="sample2.mot" ftime="2020-06-18 16:26"           |
| Now loading   |
| Address range code: 0x00008000 to 0x0000FFFF, CRC32: 0x25FDCC28 |
| PASS  |
| >ep   |
| (Omitted)   |
| Erase, Program operation finished.                              |
| >   |



# 3.3 Consecutive Programming with Multiple Sets of Files

You can download multiple sets of files in different programming areas of the PG-FP6 for use in consecutive programming of desired flash areas of the target MCU.

The following shows an example of downloading two pairs of a setting file and program file in programming areas 0 and 1 of the PG-FP6, respectively, along with a single parameter file (for a specific MCU). These files are to be programmed in areas 0 and 1 of the target MCU.

```
sendin 'progarea 0'
wait '>'
sendIn 'downprm'
wait 'Now loading...'
sendfile 'C:\fp6\sample\R5F100LE.pr5' 1
wait '>'
sendIn 'downset'
wait 'Now loading...'
sendfile 'C:\fp6\sample\sample1.es6' 1
wait '>'
sendln 'lod fname="sample1.mot" ftime="2020-06-18 16:24"'
wait 'Now loading...'
sendfile 'C:\fp6\sample1.mot' 1
wait '>'
sendln 'progarea 1'
wait '>'
sendln 'downprm'
wait 'Now loading...'
sendfile 'C:\fp6\sample\R5F100LE.pr5' 1
wait '>'
sendln 'downset'
wait 'Now loading...'
sendfile 'C:\fp6\sample\sample2.es6' 1
wait '>'
sendIn 'lod fname="sample2.mot" ftime="2020-06-18 16:26"'
wait 'Now loading...'
sendfile 'C:\fp6\sample2.mot' 1
wait '>'
sendIn 'progarea 0'
wait '>'
sendln 'ep'
wait '>'
sendln 'progarea 1'
wait '>'
sendln 'ep'
```



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end

Executing this macro file leads to the following output in Tera Term.

| >progarea 0   |
|---|
| Active Program Area: 0  |
| >downprm  |
| Now loading   |
| PASS  |
| >downset  |
| Now loading   |
| PASS  |
| >lod fname="sample1.mot" ftime="2020-06-18 16:24"               |
| Preparing storage PASS  |
| Now loading   |
| Address range code: 0x00000000 to 0x00007FFE, CRC32: 0xC7D7C74C |
| PASS  |
| >progarea 1   |
| Active Program Area: 1  |
| >downprm  |
| Now loading   |
| PASS  |
| >downset  |
| Now loading   |
| PASS  |
| >lod fname="sample2.mot" ftime="2020-06-18 16:26"               |
| Preparing storage PASS  |
| Now loading   |
| Address range code: 0x00008000 to 0x0000FFFF, CRC32: 0x25FDCC28 |
| PASS  |
| >progarea 0   |
| Active Program Area: 0  |
| >ep   |
| Blank check Skipped.  |
| Erase Code Flash Block 0000 to 0031:                            |
| PASS  |
| Program Code Flash Block 0000 to 0031:                          |
| (Omitted)   |
| PASS  |
| Verify Code Flash Block 0000 to 0031:                           |
| (Omitted)   |
|   |



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| PASS                                   |
|--|
| Erase, Program operation finished.     |
| >progarea 1                            |
| Active Program Area: 1                 |
| >ep                                    |
| Blank check Skipped.                   |
| Erase Code Flash Block 0032 to 0063:   |
| PASS                                   |
| Program Code Flash Block 0032 to 0063: |
| (Omitted)                              |
| PASS                                   |
| Verify Code Flash Block 0032 to 0063:  |
| (Omitted)                              |
| PASS                                   |
| Erase, Program operation finished.     |
| >                                      |



# 3.4 Executing Multiple Consecutive Commands (e.g. Erasure, Programming, and Verification)

You can also execute multiple consecutive commands (e.g. erasure, programming, and verification).

The following shows an example of the specification of three commands (**ers**: erasure, **prg**: programming, and **vrf**: verification).

Executing these commands leads to the following output in Tera Term.

| >ers                        |
|-----------------------------|
| (Omitted)                   |
| PASS                        |
| Erase operation finished.   |
| >prg                        |
| (Omitted)                   |
| PASS                        |
| Program operation finished. |
| >vrf                        |
| (Omitted)                   |
| PASS                        |
| Verify operation finished.  |
| >                           |



# 3.5 **Programming Unique Code**

The serno command can be used to embed a unique code at a specific address in the program file.

If you wish to use this command, ensure that the unique code function is enabled by the given FP6 Manager setting. Select [Programmer] -> [FP6 Manager] from the menu bar of the FP6 Terminal to open the [FP6 Manager] dialog box and deselect the [Restrict Unique Code Function] checkbox.

| P6 Security                       | Encryption                     |
|-----------------------------------|--------------------------------|
| P6 Manager Password               | RPE File Password: Disabled    |
| Change Password                   | Change Password Clear Password |
| Boot Protection: Disabled         |                                |
| Change Pattern Clear Boot Protect | Encrypt Program File Data      |
| Limit FP6 Authentication          |                                |
| $\sim$                            |                                |
| estrict FP6 Operations            | Optional Functions             |
| Limit Target Programming          | Checksum Comparison            |
| Restrict Upload from FP6          | ☑ Program File Size Monitor    |
| Restrict Setup                    | Standalone Mode                |
| Restrict Unique Code Function     | Standard Mode $\sim$           |
| Restrict Read Command             |                                |

Figure 3.1 [FP6 Manager] Dialog Box

The following shows an example of embedding 0x00010203 at address 0x8000.

| sendln 'serno 8000 00010203' |  |
|------------------------------|--|
| wait '>'                     |  |
| sendln 'ep'                  |  |
| end                          |  |

Executing this command leads to the following output in Tera Term.

| >serno 8000 00010203               |
|------------------------------------|
| ОК                                 |
| >ep                                |
| (Omitted)                          |
| Erase, Program operation finished. |
| >                                  |



#### 3.6 Programming with a Specified PG-FP6

The **connect** command, which is one of the Tera Term macro commands, can be used to specify the number of the COM port to which a PG-FP6 you wish to use is connected.

The following shows an example of programming through a PG-FP6 connected to the COM4 port.

connect '/C=4'

sendln 'ep'

end

Executing this command leads to the following output in Tera Term.

>ep

(Omitted)

Erase, Program operation finished.

>



# 3.7 Simultaneous Programming of Multiple Devices from a Single Host PC (Gang Programming)

Multiple Tera Term windows can be opened on a host PC. You can specify the COM port numbers of different PG-FP6 units in those windows to program multiple devices. The **start** command for the Windows command prompt can also be used to set up a batch file that launches multiple batch files which continue running at the same time.

Note: The FP6 Gang Programmer, a programming GUI that comes with the FP6 Terminal, makes simultaneous programming through multiple PG-FP6 units possible.

This section introduces an example where two Tera Term windows are started at the same time in a single environment (which consists of a parameter file, setting file, and program file) to program the devices connected to two PG-FP6 units.

"ttpmacro.exe" provided by Tera Term is used to run the macro files.

In advance, download a set of a parameter file, setting file, and program file to two PG-FP6 units.

The following example is the "sample1.bat" batch file, which is used to handle programming through the first PG-FP6 (COM port number: x).

ttpmacro.exe "C:\fp6\sample1.ttl" exit

0/lit

The following example is the "sample1.ttl" macro file, which is used to handle programming through the first PG-FP6 (COM port number: x).

```
connect '/C=x'
sendIn 'ep'
wait 'Erase, Program operation finished.' 'ERROR'
        if result=0 goto timeouterror
        if result=1 goto pass
        if result=2 goto comerror
wait '>'
        if result=0 goto timeouterror
:pass
messagebox 'Erase, Program operation finished.' 'PASS'
closett
end
:comerror
messagebox 'Command operation error' 'ERROR'
closett
end
:timeouterror
messagebox 'Timeout error' 'ERROR'
closett
end
```



The following example is the "sample2.bat" batch file, which is used to handle programming through the second PG-FP6 (COM port number: y).

ttpmacro.exe "C:\fp6\sample2.ttl"

exit

The following example is the "sample2.ttl" macro file, which is used to handle programming through the second PG-FP6 (COM port number: y).

| connect '/C=y'   |  |  |
|--|--|--|
| sendin 'ep'  |  |  |
| wait 'Erase,Program operation finished.' 'ERROR'       |  |  |
| if result=0 goto timeouterror                          |  |  |
| if result=1 goto pass                                  |  |  |
| if result=2 goto comerror                              |  |  |
| wait '>'   |  |  |
| if result=0 goto timeouterror                          |  |  |
|  |  |  |
| :pass  |  |  |
| messagebox 'Erase, Program operation finished.' 'PASS' |  |  |
| closett  |  |  |
| end  |  |  |
|  |  |  |
| :comerror  |  |  |
| messagebox 'Command operation error' 'ERROR'           |  |  |
| closett  |  |  |
| end  |  |  |
|  |  |  |
| :timeouterror  |  |  |
| messagebox 'Timeout error' 'ERROR'                     |  |  |
| closett  |  |  |
| end  |  |  |

This example is the "sample.bat" batch file, which executes calls of both "sample1.bat" and "sample2.bat".

| :LOOP             |  |
|-------------------|--|
| start sample1.bat |  |
| start sample2.bat |  |
| PAUSE             |  |
| goto LOOP         |  |



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