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M16C/64 Group

Operation of Timer A (timer mode, pulse output function)

1. Abstract

In timer mode, choose functions from those listed in Table 1. Operations of the circled items are described below.

2. Introduction

This application note is applied to the M16C/64 group microcomputers.

This program can be operated under the condition of M16C family products with the same SFR (Special Function Register) as M16C/64 Group products. Because some functions may be modified of the M16C family products, see the user's manual. When using the functions shown in this application note, evaluate them carefully for an operation.



3. Chosen functions

Table 1. Chosen functions

Item	Set-up			
Count source	0	Internal count source (f1TIMAB/f2TIMAB/f8TIMAB/f32TIMAB/f64TIMAB/foco-s/fc32)		
Pulse output function	No pulse output			
	0	Pulses output		
Gate function	0	No gate function		
		Performs count only for the period in which the TAilN pin is at "L" level		
		Performs count only for the period in which the TAilN pin is at "H" level		
Output polar control O Output waveform "H" active		Output waveform "H" active		
		Output waveform "L" active (output reversed)		

Note: i=0~4

4. Operation

- (1) Setting the count start flag to "1" causes the counter to perform a down count on the count source.
- (2) If an underflow occurs, the content of the reload register is reloaded to the counter, and the count continues. At this time, the timer Ai interrupt request bit goes to "1". Also, the output polarity of the TAiOUT pin reverses.
- (3) Setting the count start flag to "0" causes the counter to stop and to hold its value. Also, the output polarity of the TAiOUT pin outputs an "L" level.

Figure 1 shows the operation timing.

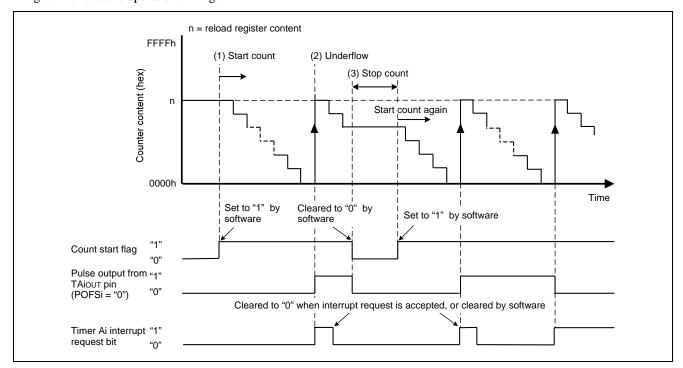


Figure 1. Operation timing of timer mode, pulse output function



5. Set-up procedure

Table 2 shows Timer A count source, Figure 2 shows block diagram of Timer A count source in timer mode.

Table 2. Count Source Selection of Timer A

TACSj reg	: 1)		TAiMR register		Count source	Count source period	
TCS3/ TCS7	TCS2/ TCS6	TCS1/ TCS5	TCS0/ TCS4	TCK1	ТСК0		f(PLL):24MHz f(Xcin):32.768kHz
0	-	-	-	0	0	f1TIMAB/f2TIMAB (Note 2)	41.7ns or 83.3ns
0	-	-	-	0	1	f8TIMAB	333.3ns
0	-	-	-	1	0	f32TIMAB	1333.3ns
0	-	-	-	1	1	fc32	976.56µs
1	0	0	0	-	-	f1TIMAB/f2TIMAB (Note 2)	41.7ns or 83.3ns
1	0	0	1	-	-	f8TIMAB	333.3ns
1	0	1	0	-	-	f32TIMAB	1333.3ns
1	0	1	1	-	-	f64TIMAB	2666.7ns
1	1	0	1	-	-	foco-s	About 8µs
1	1	1	0	-	-	fc32	976.56µs

Note 1: TCS3~TCS0 bits of TACS0 register correspond to Timer A0 count source selection, TCS7~TCS4 bits of TACS0 register correspond to Timer A1 count source selection, TCS3~TCS0 bits of TACS1 register correspond to Timer A2 count source selection, TCS7~TCS4 bits of TACS1 register correspond to Timer A3 count source selection, and TCS3~TCS0 bits of TACS2 register correspond to Timer A4 count source selection. Note 2: When the PCLK0 bit in the PCLKR register is "1", the selected clock source is f1TIMAB. When the PCLK0 bit is "0", the selected clock source is f2TIMAB.

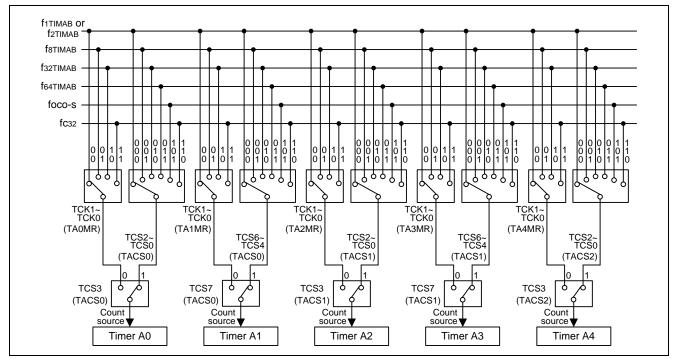
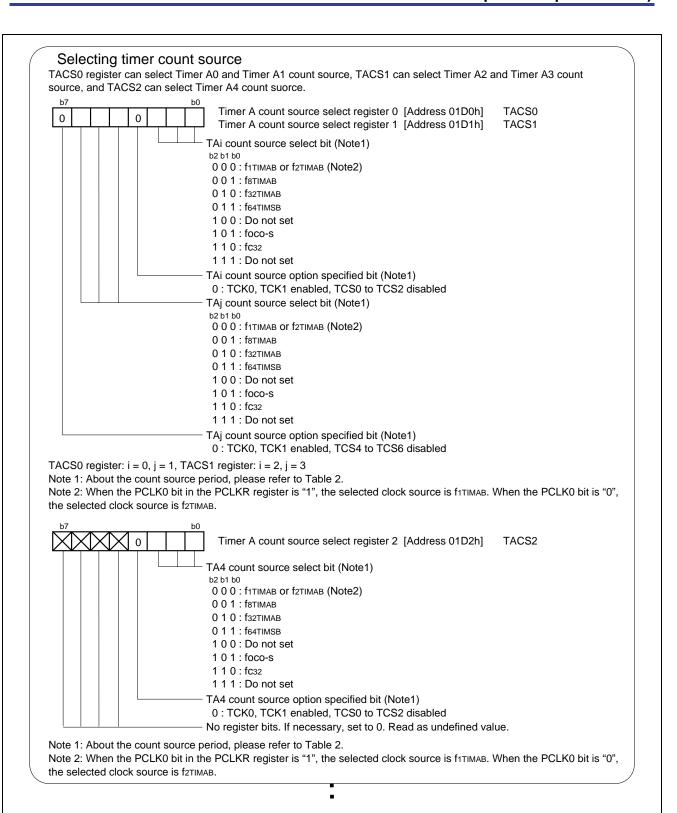
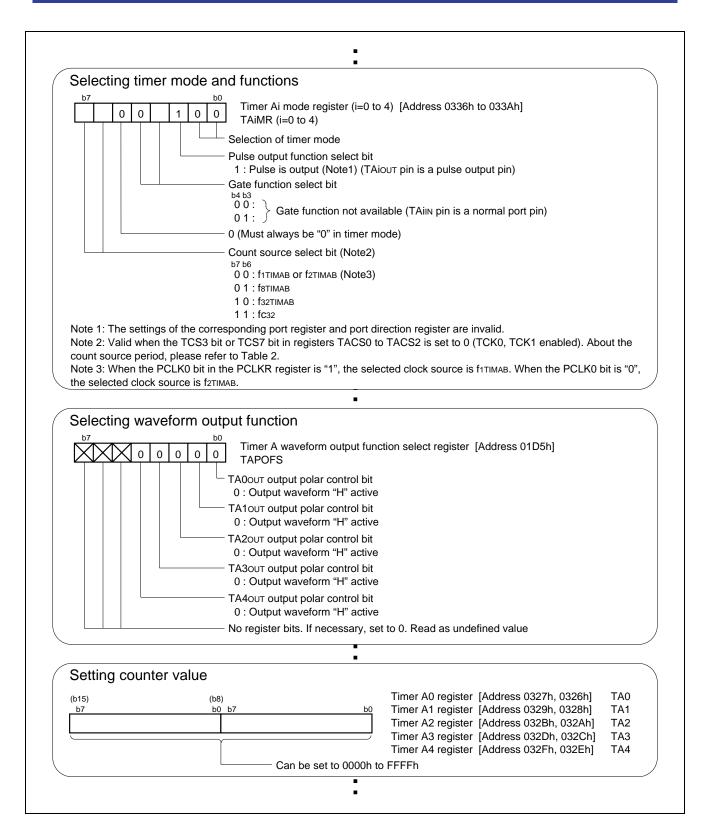


Figure 2. Count source of Timer A

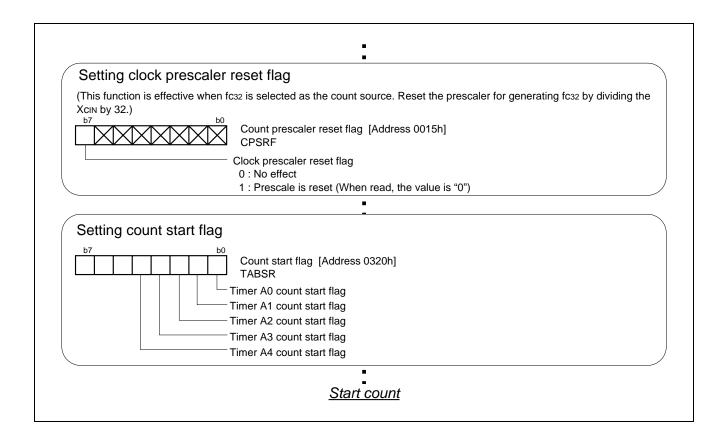














6. Reference

Hardware manual

M16C/64 Group Hardware Manual

(Use the most recent version of the document on the Renesas Technology Web site.)

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Revision

Rev.	Issue date	Revised				
		Page	Point			
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