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# M16C/65 Group

Operation of Timer A (one-shot timer mode)

### 1. Abstract

In one-shot timer mode, choose functions from those listed in Table 1. Operations of the circled items are described below.

### 2. Introduction

This application note is applied to the M16C/65 group Microcomputers.

This application note can be used with other M16C Family MCUs which have the same special function registers (SFRs) as the above group. Check the manual for any modifications to functions. Careful evaluation is recommended before using the program described in this application note.



## 3. Chosen functions

#### **Table 1. Chosen functions**

Item	Set	Set-up				
Count source		Internal count source				
		(f1TIMAB/f2TIMAB/f8TIMAB/f32TIMAB/f64TIMAB/foco-F/foco-S/fc32)				
Pulse output function	output function No pulse output					
	0	Pulses output				
Count start condition		External trigger input (falling edge of input signal to the TAiIN pin)				
		External trigger input (rising edge of input signal to the TAiIN pin)				
		Timer overflow (TB2/TAj/TAk overflow)				
	0	Writing "1" to the one-shot start flag				
Output polar control	Output polar control O Output waveform "H" active					
		Output waveform "L" active (output reversed)				

Note: j = i - 1, but j = 4 when i = 0; k = i + 1, but k = 0 when i = 4.

## 4. Operation

- (1) Setting the one-shot start flag to "1" with the count start flag set to "1" causes the counter to perform a down count on the count source. At this time, the TAiout pin outputs an "H" level.
- (2) The instant the value of the counter becomes "0000h", the TAiOUT pin outputs an "L" level, and the counter reloads the content of the reload register and stops counting. At this time, the timer Ai interrupt request bit goes to "1".
- (3) If a trigger occurs while a count is in progress, the counter reloads the value in the reload register again and continues counting. The reload timing is in step with the next count source input after the trigger.
- (4) Setting the count start flag to "0" causes the counter to stop and to reload the content of the reload register. Also, the TAiOUT pin outputs an "L" level. At this time, the timer Ai interrupt request bit goes to "1".

Note: When the timer Ai register is set to "0000h", the counter does not operate and the timer Ai interrupt request is not generated. When the pulse is set to output, the pulse does not output from the TAiOUT pin.

Figure 1 shows the operation timing.



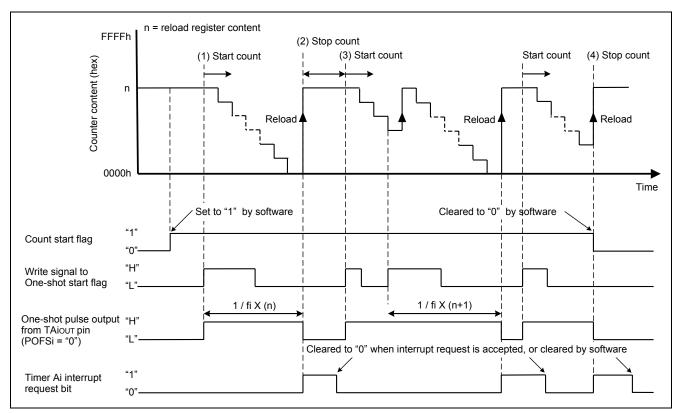


Figure 1. Operation timing of one-shot



## 5. Set-up procedure

Table 2 shows Timer A count source, Figure 2 shows block diagram of Timer A count source in timer mode.

**Table 2. Count Source Selection of Timer A** 

TCKDIVC0 register (Note 1)	TACSj	register	(Note 2)		TAiMR register		Count source	Count source period
TCDIV00	TCS3/ TCS7	TCS2/ TCS6	TCS1/ TCS5	TCS0/ TCS4	TCK1	ТСК0		f(Xin):20MHz f(Xcin):32.768kHz f(oco-F):about 20MHz f(oco-s):about 125kHz
0	0	-	-	-	0	0	f1TIMAB/ f2TIMAB (Note 3)	50ns/100ns
0	0	-	-	-	0	1	f8TIMAB	400ns
0	0	-	-	-	1	0	f32TIMAB	1600ns
0	0	-	-	-	1	1	fc32	976.56µs
0	1	0	0	0	-	-	f1TIMAB/ f2TIMAB (Note 3)	50ns/100ns
0	1	0	0	1	-	-	f8TIMAB	400ns
0	1	0	1	0	-	-	f32TIMAB	1600ns
0	1	0	1	1	-	-	f64TIMAB	3200ns
0	1	1	0	0	-	-	foco-F	about 50ns
0	1	1	0	1	-	-	foco-s	about 8µs
0	1	1	1	0	-	-	fc32	976.56µs
1	1	0	0	0	-	-	f1TIMAB/ f2TIMAB (Note 3)	about 50ns/100ns
1	1	0	0	1	-	-	f8TIMAB	about 400ns
1	1	0	1	0	-	-	f32TIMAB	about 1600ns
1	1	0	1	1	-	-	f64TIMAB	about 3200ns

Note 1: TCDIV00 bit is clock select prior to timer AB division bit. Set the TCDIV00 bit before setting other registers associated with timer A. After changing the TCDIV00 bit, set other registers associated with timer A again.

Note 2: TCS3~TCS0 bits of TACS0 register correspond to Timer A0 count source selection, TCS7~TCS4 bits of TACS0 register correspond to Timer A1 count source selection, TCS3~TCS0 bits of TACS1 register correspond to Timer A2 count source selection, TCS7~TCS4 bits of TACS1 register correspond to Timer A3 count source selection, and TCS3~TCS0 bits of TACS2 register correspond to Timer A4 count source selection. Note 3: When the PCLK0 bit in the PCLKR register is "1", the selected clock source is f1TIMAB. When the PCLK0 bit is "0", the selected clock source is f2TIMAB.



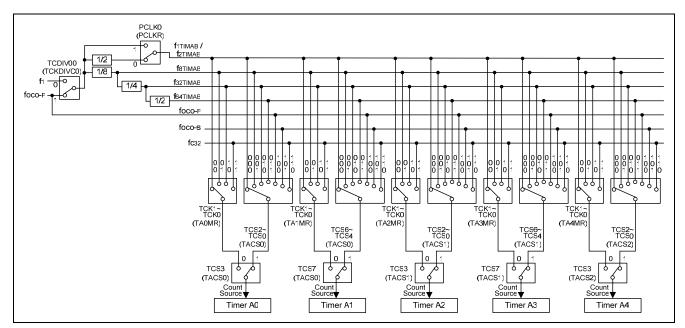
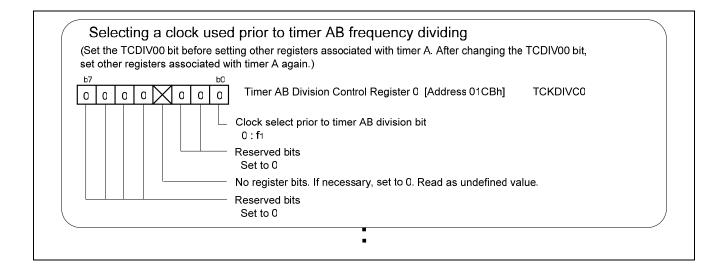


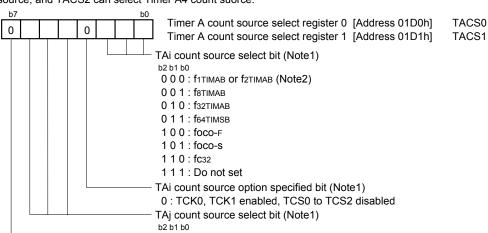
Figure 2. Count source of Timer A





#### Selecting timer count source

TACS0 register can select Timer A0 and Timer A1 count source, TACS1 can select Timer A2 and Timer A3 count source, and TACS2 can select Timer A4 count suorce.



0 0 0: f1TIMAB or f2TIMAB (Note2)

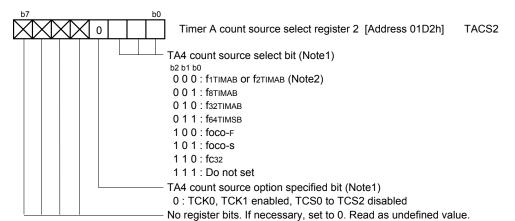
0 0 1 : f8TIMAB 0 1 0 : f32TIMAB 0 1 1 : f64TIMSB 1 0 0 : foco-F 1 0 1 : foco-s 1 1 0 : fc32 1 1 1: Do not set

TAj count source option specified bit (Note1) 0: TCK0, TCK1 enabled, TCS4 to TCS6 disabled

TACS0 register: i = 0, j = 1, TACS1 register: i = 2, j = 3

Note 1: About the count source period, please refer to Table 2.

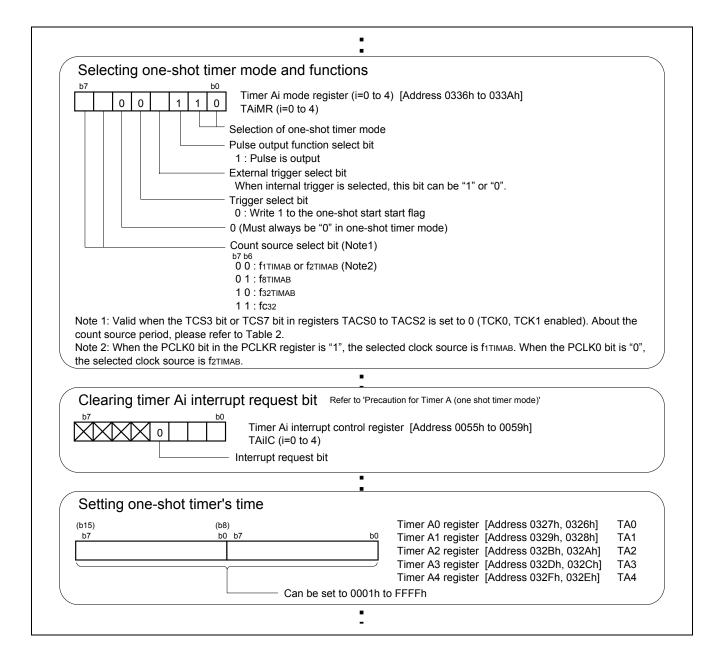
Note 2: When the PCLK0 bit in the PCLKR register is "1", the selected clock source is f1TIMAB. When the PCLK0 bit is "0", the selected clock source is f2TIMAB.



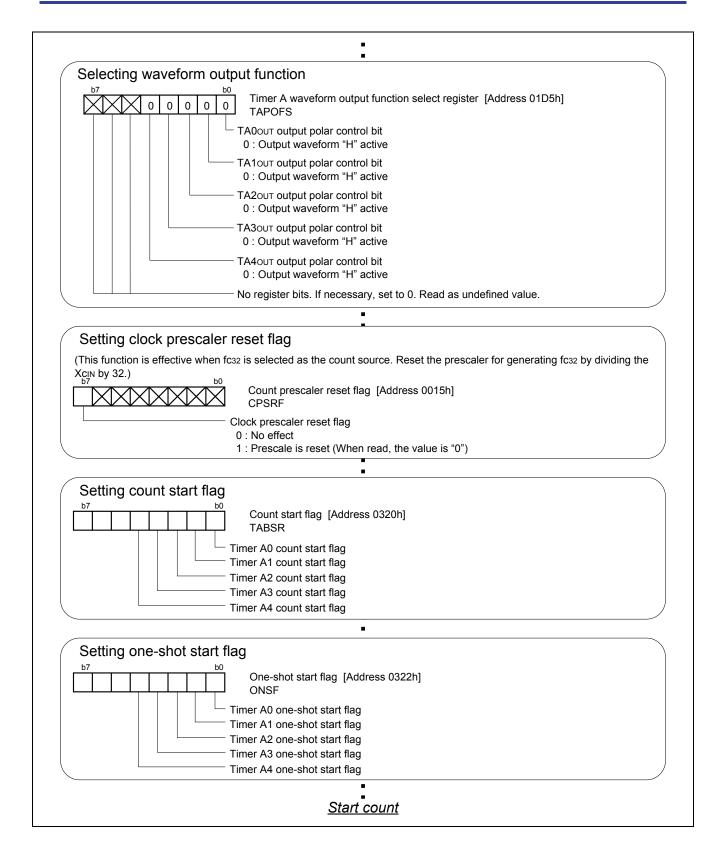
Note 1: About the count source period, please refer to Table 2.

Note 2: When the PCLK0 bit in the PCLK0 register is "1", the selected clock source is fitimab. When the PCLK0 bit is "0", the selected clock source is f2TIMAB.











## 6. Reference

Hardware manual

M16C/65 Group Hardware Manual

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# Revision

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