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# M16C/64 Group

# Long-period timers

### 1. Abstract

In this process, Timer A0 and Timer A1 are connected to make a 16-bit timer with a 16-bit prescaler. Use the following peripheral function:

- Timer mode of timer A
- Event counter mode of timer A

### 2. Introduction

This application note is applied to the M16C/64 group microcomputers.

This program can be operated under the condition of M16C family products with the same SFR (Special Function Register) as M16C/64 Group products. Because some functions may be modified of the M16C family products, see the user's manual. When using the functions shown in this application note, evaluate them carefully for an operation.



#### 3. Introduction

- (1) Set timer A0 to timer mode, and set timer A1 to event counter mode.
- (2) Perform a count on count source f1TIMAB using timer A0 to count for 1 ms, and perform a count on timer A0 using timer A1 to count for 1 second.
- (3) Connect a 16MHz oscillator to XIN.
- (4) Using POFSi bit in TAPOFS register, the output polarity of the TAiOUT pin is not inverted (the TAi bit is set to 0 (stop counting)), the pin outputs "L". (i = 0, 1)

## 4. Operation

- (1) Setting the count start flag to "1" causes the counter to begin counting. The counter of timer A0 performs a down count on count source fitimab.
- (2) If the counter of timer A0 underflows, the counter reloads the content of the reload register and continues counting. At this time, the timer A0 interrupt request bit goes to "1". The counter of timer A1 performs a down count on underflows in timer A0.
- (3) If the counter of timer A1 underflows, the counter reloads the content of the reload register and continues counting. At this time, the timer A1 interrupt request bit goes to "1".



Figure 1 shows the operation timing.

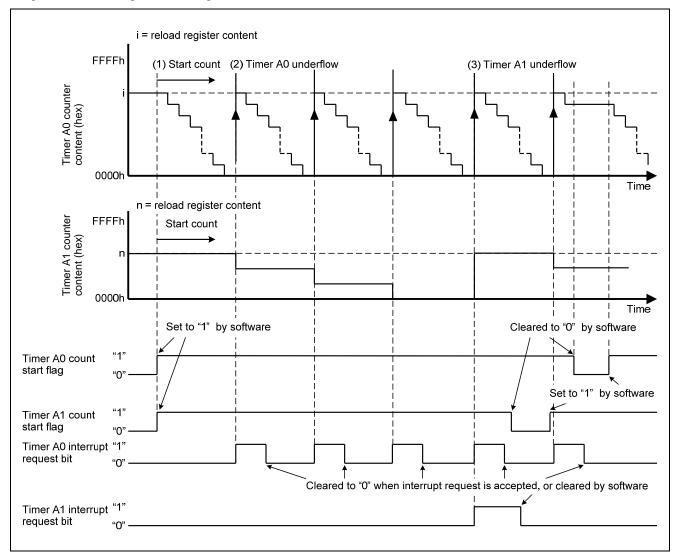


Figure 1. Operation timing of long-period timers

Figure 2 shows the connection diagram.

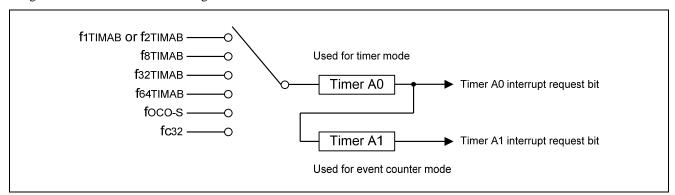


Figure 2. Connection diagram of long-period timers



## 5. Set-up procedure

Table 1 shows Timer A count source, Figure 3 shows block diagram of Timer A count source in timer mode.

**Table 1. Count Source Selection of Timer A** 

TACSj register (Note 1)				TAiMR register		Count source	Count source period
TCS3/ TCS7	TCS2/ TCS6	TCS1/ TCS5	TCS0/ TCS4	TCK1	ТСК0		f(PLL):24MHz f(Xcin):32.768kHz
0	-	-	-	0	0	f1TIMAB/f2TIMAB (Note 2)	41.7ns or 83.3ns
0	-	-	-	0	1	f8TIMAB	333.3ns
0	-	-	-	1	0	f32TIMAB	1333.3ns
0	-	-	-	1	1	fc32	976.56µs
1	0	0	0	-	-	f1TIMAB/f2TIMAB (Note 2)	41.7ns or 83.3ns
1	0	0	1	-	-	f8TIMAB	333.3ns
1	0	1	0	-	-	f32TIMAB	1333.3ns
1	0	1	1	-	-	f64TIMAB	2666.7ns
1	1	0	1	-	-	foco-s	About 8µs
1	1	1	0	-	-	fc32	976.56µs

Note 1: TCS3~TCS0 bits of TACS0 register correspond to Timer A0 count source selection, TCS7~TCS4 bits of TACS0 register correspond to Timer A1 count source selection, TCS3~TCS0 bits of TACS1 register correspond to Timer A2 count source selection, TCS7~TCS4 bits of TACS1 register correspond to Timer A3 count source selection, and TCS3~TCS0 bits of TACS2 register correspond to Timer A4 count source selection. Note 2: When the PCLK0 bit in the PCLKR register is "1", the selected clock source is f1TIMAB. When the PCLK0 bit is "0", the selected clock source is f2TIMAB.

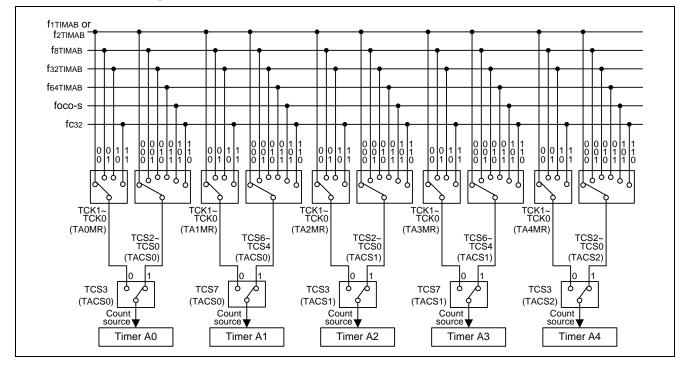
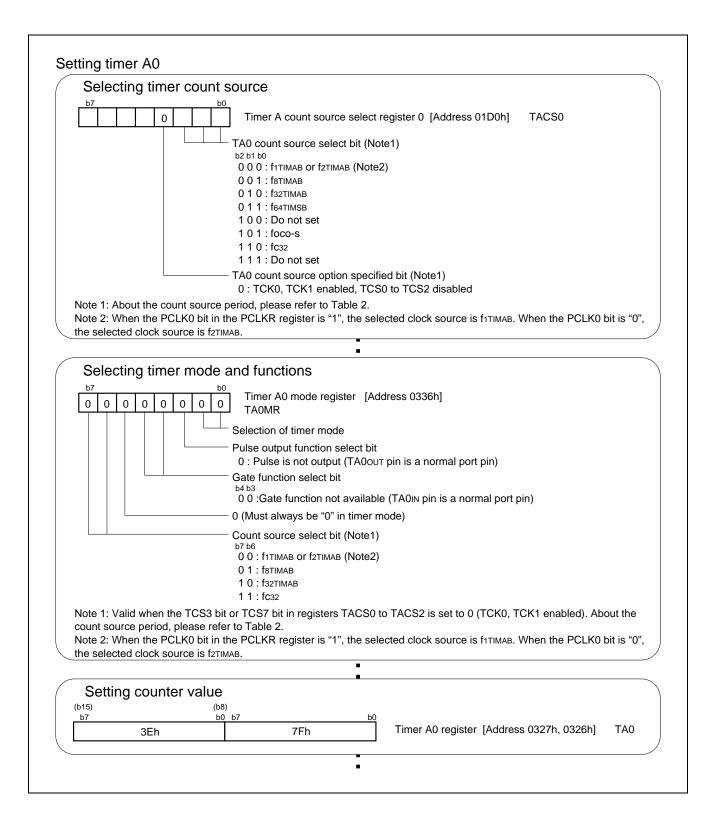
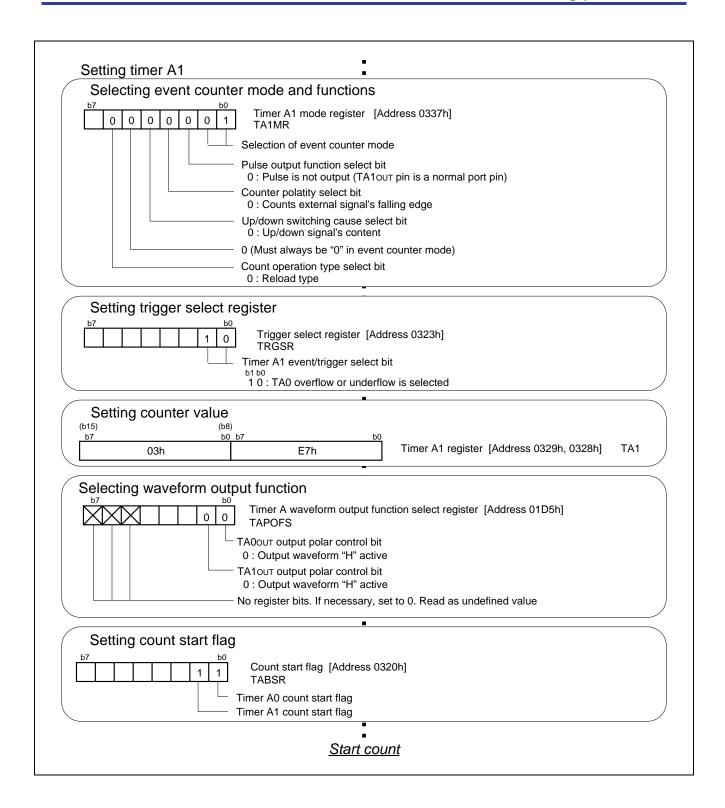


Figure 3. Count source of Timer A











#### 6. Reference

Hardware manual

M16C/64 Group Hardware Manual

(Use the most recent version of the document on the Renesas Technology Web site.)

Technical news/Technical update

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## Revision

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		Page	Point		
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