

**Introduction**

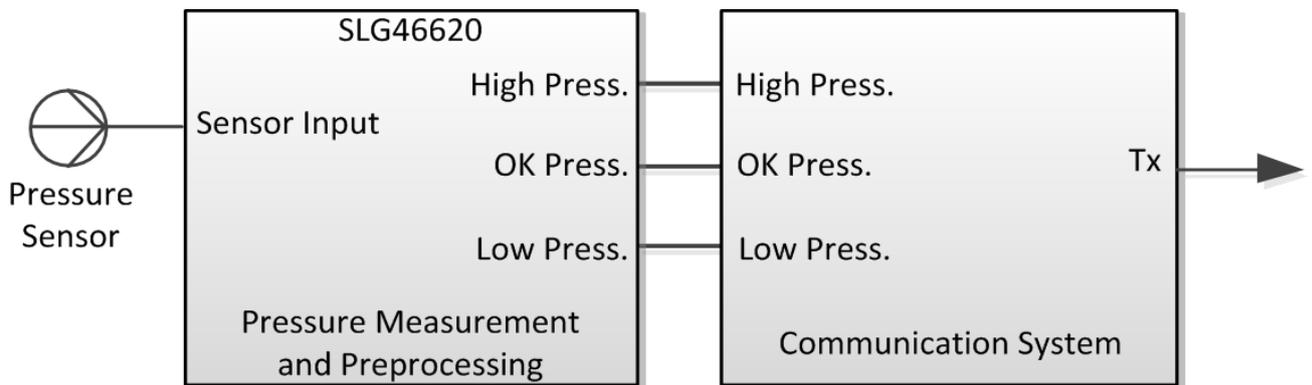
Newer cars typically feature a tire pressure monitoring systems (TPMS) that will warn the driver when a tire is significantly under-inflated. This safety feature was deemed important enough that the United States Department of Transportation (DOT) and the National Highway Traffic Safety Administration (NHTSA) published a Federal Motor Vehicle Safety Standard. Tire pressure affects a vehicle's fuel economy, handling, and possible catastrophic tire failure. In this application note, over-inflated detection is also included. The system may also be used for anti-theft, warning when one of the sensors is not sending information (in that case, one of the tires may have been stolen).

There are two TPMS system types. One of them is called Direct System. It is based on installing a pressure sensor in each wheel to directly measure the pressure in each tire, sending the information to the vehicle's on-board computer which warns drivers when the air pressure in any of their tires drops at least 25% below the recommended cold tire inflation pressure, or if the tire has 25% over the recommended inflation pressure.

Direct systems are typically more accurate and reliable, and most are able to indicate which tire is under-inflated.

The other system is called Indirect System. It uses the vehicle's anti-lock braking system's wheel speed sensors to compare the rotational speed of one tire versus the others. If a tire is low/high on pressure, it will roll at a different number of revolutions per kilometer than the other three and alert the vehicle's on-board computer. Indirect systems are unable to generate accurate readings in cases where all four tires are losing pressure at the same rate, such as the effects of time and temperature.

In this implementation, the Direct Tire Pressure Measurement System will be used. In this case, the pressure will be measured and analyzed locally on each tire with the SLG46620V, sending the information of under-pressure, over-pressure or correct pressure to the central system (onboard computer or dedicated system) via the communication system (Figure 1). With this implementation, a TPMS may also be retrofitted on older cars by adding a small central system to the console of the car.



**Figure 1. Pressure measurement and Preprocessing System schematic block diagram**

**Pressure Sensor**

In this type of application, choosing the correct sensor is one of the most important stages in the design. An automotive application not only requires a sensor with the correct resolution and pressure range, it also requires certified sensors which are able to be used for automotive safety applications and with a low current consumption.

In this case, there are two options about the type of sensor: Differential Pressure Sensors and Absolute Pressure Sensors. Differential ones measure the difference between the actual pressure and the atmosphere pressure. Absolute ones use absolute zero as its reference pressure, measured relative to a full vacuum (outer space).

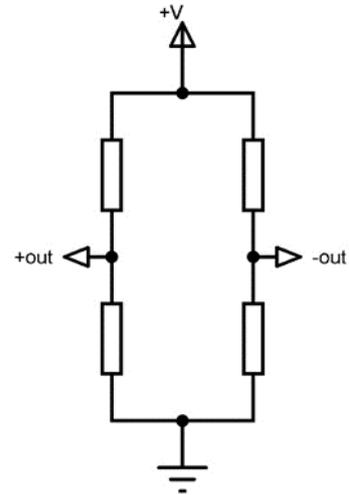
Since absolute pressure uses absolute zero as a definitive reference point, absolute pressure remains precise and accurate regardless of changes in ambient or process temperature. This is the main reason for choosing Absolute Pressure Sensors.

The selected pressure sensor for this application is the SM5420C-060 from SMI Pressure Sensors. It is an absolute pressure sensor with an operating pressure range of 0 to 60 PSI. The power supply must be 5V (compatible with SLG46620V) and with a current low consumption of 1mA. One of the advantages of this sensor is that it is certified to be used for automotive applications, being qualified to meet AEC Q100 standards (the Automotive Electronics Council standard about Failure Mechanism based Stress Test Qualification for Integrated Circuits).

The selected pressure sensor has a differential output proportional to the measured pressure and it can be modelled as

$$V_o[mV] = \frac{100}{60PSI} \times P; \quad P = \text{Absolute Measured Pressure [PSI]}$$

The output circuit can be thought as a Wheatstone bridge, as it can be seen in Figure 2.



**Figure 2. Output circuit – Pressure Sensor**

Most important characteristics of the sensor are shown in table 1.

| Parameter             | Value                      |
|-----------------------|----------------------------|
| Power Supply          | 5V                         |
| Input Current         | 1 mA                       |
| Operating Temperature | -40 to 125 °C              |
| Operating Pressure    | 0 to 60 PSI                |
| Span                  | 100 mV typical (135mV max) |
| Zero Offset           | 0 mV                       |

**Table 1. Pressure Sensor Characteristics**

**Schematic Diagram**

Due to the differential outputs of the sensor, and considering the simpler way for conditioning the signal for being acquired by SLG46620V’s ADC, the external implemented circuit is shown in Figure 3.

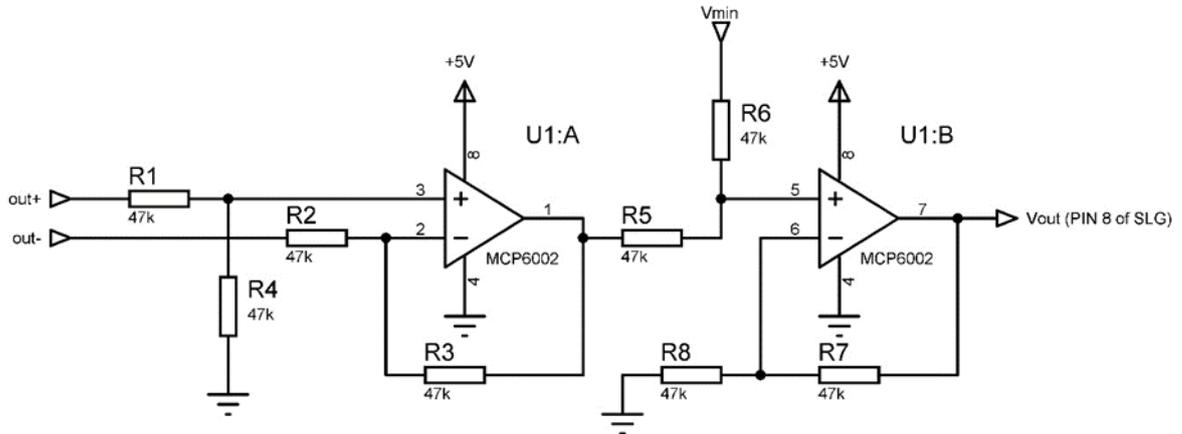


Figure 3a. Signal conditioning schematic circuit

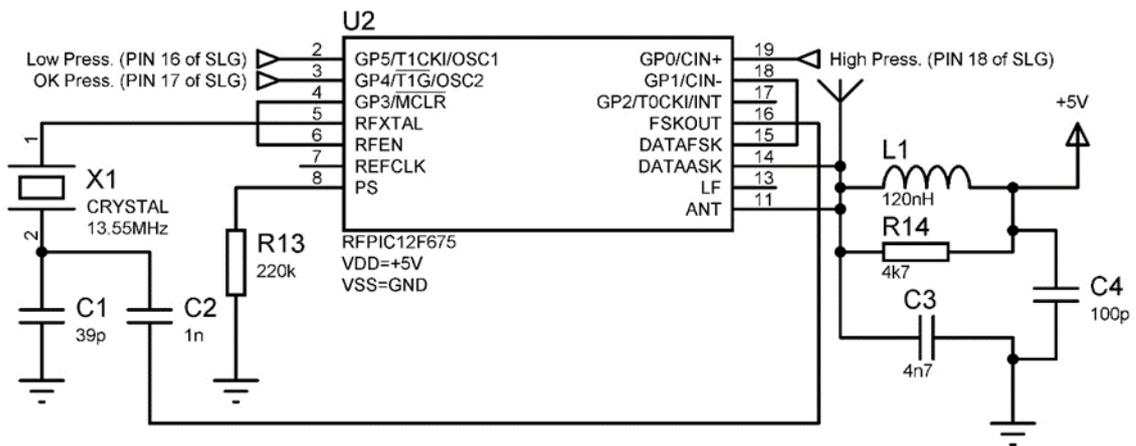


Figure 3b. Wireless Communication System

The signal conditioning circuit can be divided in two parts to describe it.

First of all, the differential output of the sensor (out+ and out-) is converted to a single ended signal with the operational amplifier U1. This is done by a typical differential configuration with unity gain. With this circuit, the signal obtained at the output of U1 is

$$V_{U1} = out_+ - out_-$$

It's important to mention that, if pressure is zero, the output voltage is zero.

That's why the operational amplifier must be a rail-to-rail operational amplifier.

The other part of the design is the second operational amplifier (U2). It is required to condition the level of the signal to meet the input specifications of the SLG46620V analog-to-digital converter.

SLG46620V, in the electrical specifications of the datasheet, specifies that the ADC with the single ended configuration, must have a minimum voltage input of 30mV/G (where G is the gain of the Programmable Gain Amplifier of the ADC) to acquire the signal. To obtain this minimum level voltage, the second operational amplifier adds  $V_{min}$  voltage to the single ended signal coming from the sensor and the first operational amplifier. With this configuration, the output signal ( $V_{out}$ ) can be directly connected to the analog input of the SLG46620V.

$$V_{out} = V_{U1} + V_{min}$$

When the specifications of the ADC are considered, the maximum input level voltage in single-ended mode is 1030mv/G. If the worst case is analyzed, the maximum differential output level of the sensor can be 135mV. In this case, it can be risky if the Gain is configured to 8 (because the maximum input level is 137mV) because low pressures may not be compatible with the minimum input voltage of the ADC.

For this reason, the ADC and the PGA are configured with a gain of 4. With this configuration,  $V_{min}$  must be between 23mV and 99mV. The selected value is 60mV, so the output range of the conditioned signal is 60mV to 195mV. The  $V_{min}$  voltage is obtained from the SLG46620V DAC, connecting its output to one of the GPIOs.

### Implementation

As described above, the pressure measurement described in this application note is part of a car safety system. The aim of this implementation is to get benefits of the small size and low current consumption of the SLG46620V, allowing to measure and process the pressure locally.

Another important benefit of this implementation is the speed of processing. Considering the timing requirement of the NHTSA standard, the SLG46620V processes the sensor data very fast, so the onboard computer is free to make all the necessary verifications before reporting low or high pressure.

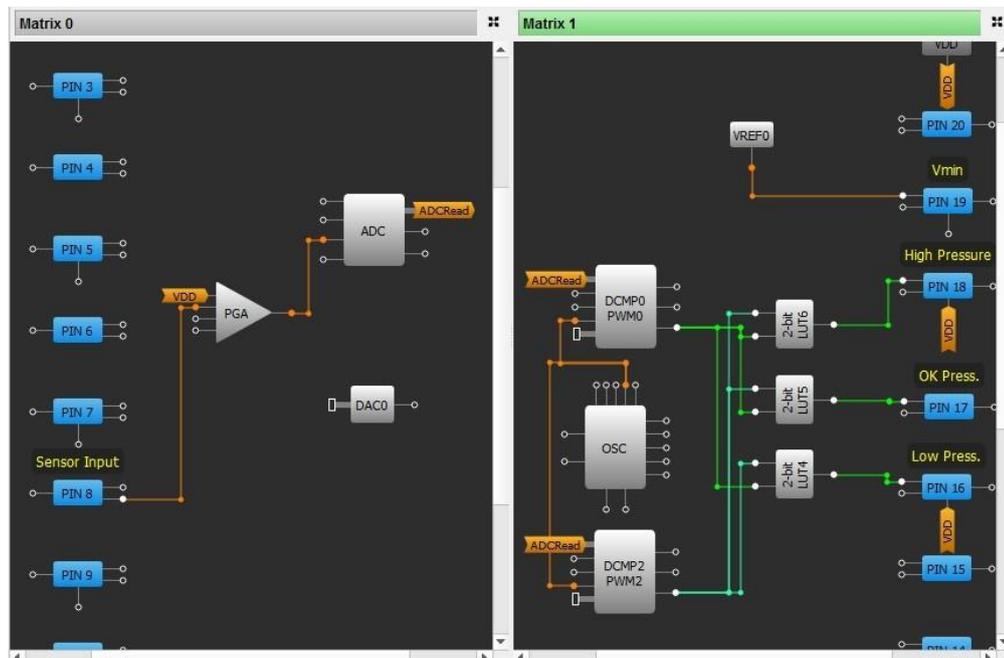
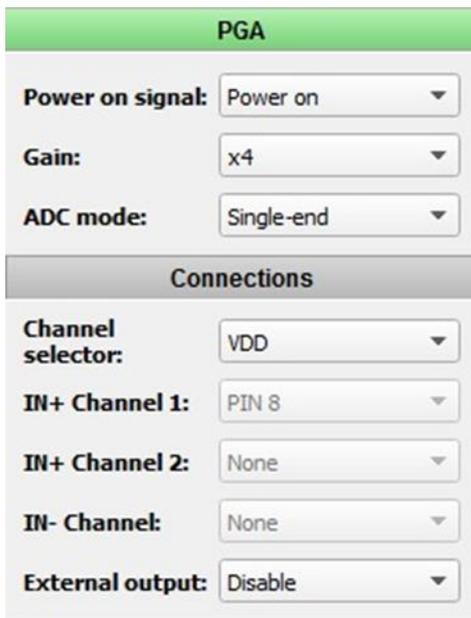


Figure 4. Pressure Measurement block diagram

The GreenPAK circuit design implementation is shown in Figure 4.

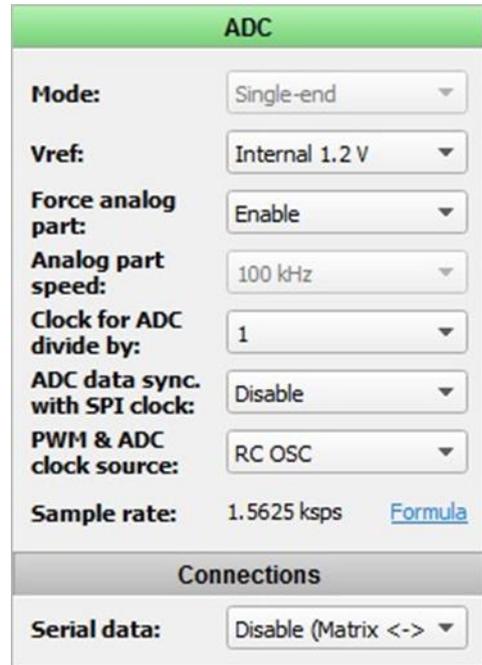
The single ended signal from the sensor is obtained from PIN 8, which connects to the input of the PGA. The PGA configuration is shown in Figure 5. It shows the PGA configured in Single-Ended Mode with a Gain of 4 and it's always powered on.



**Figure 5. Programmable Gain Amplifier configuration**

The output of the PGA is connected to the Analog to Digital Converter. The configuration of the ADC is single-ended mode, with the RC oscillator as the ADC clock as shown in Figure 6. With this clock configuration, the ADC sample rate is 1.56 kps.

The ADC conversion is analyzed with the DCMP/PWM blocks. DCMP0 compares the pressure with the low limit, indicating when the pressure is lower than the configured value with a low level on its OUT+ output. The DCMP/PWM 0 block is configured as DCMP, comparing the positive input with the value stored in Register 0.



**Figure 6. ADC configuration**

DCMP2 compares the pressure with the high limit, indicating when the pressure is higher than the configured value with a high level on its OUT+ output. The PWM/DCMP 2 block is configured as DCMP, comparing the positive input with the value stored in Register 2.

DCMP0 configuration is shown in Figure 7. Configuration of DCMP2 is the same as the configuration of DCMP0.

To determine the outputs of the system, 2-bit LUT4, LUT5 and LUT6 are used. LUT4 output is high only when a low pressure is detected (low level at OUT+ of DCMP0 and at OUT+ of DCMP2). LUT5 output is high only when the correct pressure is detected (high level at OUT+ of DCMP0 and low level at OUT+ of DCMP2). LUT6 output is high only when a high pressure is detected (high level at OUT+ of DCMP0 and high level at OUT+ of DCMP2). Figures 8 to 10 show the configurations of 2-bit LUT4, LUT5 and LUT6.

DCMP0/PWM0

DCMP/PWM power register: Power on

Function selection: DCMP

PD sync to clock: Off

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Clock source: OSC X CLK

Clock invert: Disable

PWM & ADC clock source: RC OSC

PWM data sync with SPI clock: Disable

Duty cycle: 0% - 99.6%

PWM deadband time: 10 ns

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Register 0: 93  
MTRX SEL: (0:0)

Register 1: 0  
MTRX SEL: (0:1)

Register 2: 101  
MTRX SEL: (1:0)

Register 3: 0  
MTRX SEL: (1:1)

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Connections

IN+ selector: ADC [7:0]

IN- selector: Register 0

**Figure 7. DCMP/PWM0 configuration**

The output of 2-bit LUT4 (Low Pressure output) is connected to Pin 16, the output of 2-bit LUT5 (Correct Pressure output) is connected to Pin 17 and the output of 2-bit LUT6 (High Pressure output) is connected to Pin 18.

DAC0 is included in the design as the voltage reference  $V_{min}$ . It is configured to generate 60mV and is connected to GPIO19 via the VREF0 block. Its configuration is shown in Figure 11.

2-bit LUT4

| IN3 | IN2 | IN1 | IN0 | OUT |
|-----|-----|-----|-----|-----|
| 0   | 0   | 0   | 0   | 1   |
| 0   | 0   | 0   | 1   | 0   |
| 0   | 0   | 1   | 0   | 0   |
| 0   | 0   | 1   | 1   | 0   |
| 0   | 1   | 0   | 0   | 0   |
| 0   | 1   | 0   | 1   | 0   |
| 0   | 1   | 1   | 0   | 0   |
| 0   | 1   | 1   | 1   | 0   |
| 1   | 0   | 0   | 0   | 0   |
| 1   | 0   | 0   | 1   | 0   |
| 1   | 0   | 1   | 0   | 0   |
| 1   | 0   | 1   | 1   | 0   |
| 1   | 1   | 0   | 0   | 0   |
| 1   | 1   | 0   | 1   | 0   |
| 1   | 1   | 1   | 0   | 0   |
| 1   | 1   | 1   | 1   | 0   |

Standard gates: NOR

Regular shape

All to 0, All to 1, Invert

**Figure 8. LUT4 configuration**

2-bit LUT5

| IN3 | IN2 | IN1 | IN0 | OUT |
|-----|-----|-----|-----|-----|
| 0   | 0   | 0   | 0   | 0   |
| 0   | 0   | 0   | 1   | 1   |
| 0   | 0   | 1   | 0   | 0   |
| 0   | 0   | 1   | 1   | 0   |
| 0   | 1   | 0   | 0   | 0   |
| 0   | 1   | 0   | 1   | 0   |
| 0   | 1   | 1   | 0   | 0   |
| 0   | 1   | 1   | 1   | 0   |
| 1   | 0   | 0   | 0   | 0   |
| 1   | 0   | 0   | 1   | 0   |
| 1   | 0   | 1   | 0   | 0   |
| 1   | 0   | 1   | 1   | 0   |
| 1   | 1   | 0   | 0   | 0   |
| 1   | 1   | 0   | 1   | 0   |
| 1   | 1   | 1   | 0   | 0   |
| 1   | 1   | 1   | 1   | 0   |

Standard gates: Defined by user

Regular shape

All to 0, All to 1, Invert

**Figure 9. LUT5 configuration**

**2-bit LUT6**

| IN3 | IN2 | IN1 | IN0 | OUT |
|-----|-----|-----|-----|-----|
| 0   | 0   | 0   | 0   | 0   |
| 0   | 0   | 0   | 1   | 0   |
| 0   | 0   | 1   | 0   | 0   |
| 0   | 0   | 1   | 1   | 1   |
| 0   | 1   | 0   | 0   | 0   |
| 0   | 1   | 0   | 1   | 0   |
| 0   | 1   | 1   | 0   | 0   |
| 0   | 1   | 1   | 1   | 0   |
| 1   | 0   | 0   | 0   | 0   |
| 1   | 0   | 0   | 1   | 0   |
| 1   | 0   | 1   | 0   | 0   |
| 1   | 0   | 1   | 1   | 0   |
| 1   | 1   | 0   | 0   | 0   |
| 1   | 1   | 0   | 1   | 0   |
| 1   | 1   | 1   | 0   | 0   |
| 1   | 1   | 1   | 1   | 0   |

Standard gates: AND, Regular shape (checked), All to 0, All to 1, Invert

**Figure 10. LUT6 configuration**

**DAC0**

Power on signal: Power on

Input selection: From register

DAC 8 bit register control: 15

**Figure 11. DAC0 configuration**

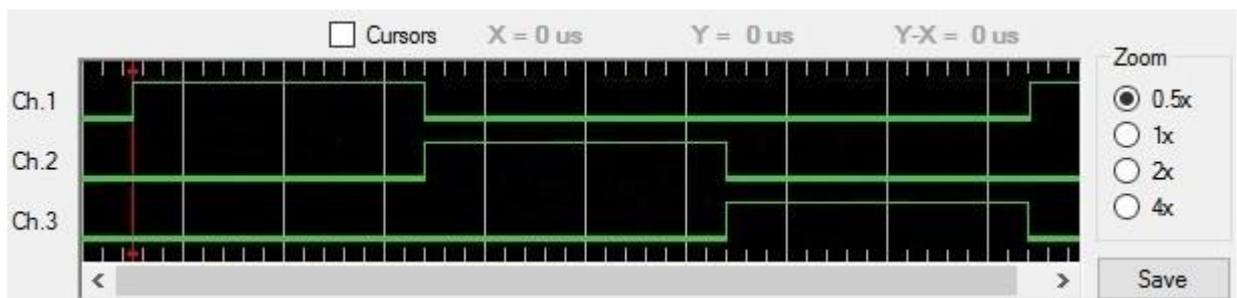
### Test and Conclusions

To test the implementation, a linear ramp of pressure was applied to the sensor, from a low pressure to a high pressure along the analyzed range. To analyze the results, pins 16 to 18 (in this order) were logged with a logic analyzer. These outputs can be seen in Figure 12.

It can be seen that the system is tested for the three possible states, obtaining a high level on the corresponding output pin of the SLG46620V.

### Conclusion

In this application note, SLG46620V is used in a car safety system application as the ADC and preprocessing unit of a bigger system. We have shown how to condition the signal to meet the ADC and PGA specifications of the GreenPAK and the entire implementation is described. It is important to mention that the values used to compare the ADC conversion can be changed for different car and tire models, without changing the logic of the system.



**Figure 12. Pressure Measurement and Preprocessing outputs test**

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