

[Notes]

R20TS0544EJ0100

Rev.1.00

e² studio Code Generator Plug-in

Feb. 01, 2020

Overview

When using the product in the title, note the following point.

1. When using the trace function of the on-chip debug setting

1. When Using the Trace Function of the On-chip Debug Setting

1.1 Applicable Products

- Code Generator plug-in V2.12.0 (e² studio V7.0.0) or later

1.2 Applicable Devices

- RL78 family: The following devices of the RL78/F15 group (*)

R5F113PG, R5F113TG, R5F113PH, R5F113TH, R5F113PJ, R5F113TJ,
R5F113GK, R5F113LK, R5F113MK, R5F113PK, and R5F113TK

(*): Except for R5F113mL (m = G, L, M, P, and T)

1.3 Details

In a GCC project (GCC for Renesas RL78), if you select “Used” in the Trace function setting in the [On-chip debug setting] tab and run a build, the following build error occurs.

(Partially omitted)

```
address 0xf8500 of gcc492201902_r178f15.elf section `.ocd_traceram' is not within region  
address 0xf8500 of gcc492201902_r178f15.elf section `.ocd_traceram' is not within region
```

1.4 Workaround

Select “Unused” in the Trace function setting in the [On-chip debug setting] tab of the code generation tool. This setting is used for securing the RAM area when using the on-chip trace, but the applicable devices do not require securing of the RAM area when using the on-chip trace.

1.5 Schedule for Fixing the Problem

This problem will be fixed in a later version.

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Feb.01.20	-	First edition issued

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