

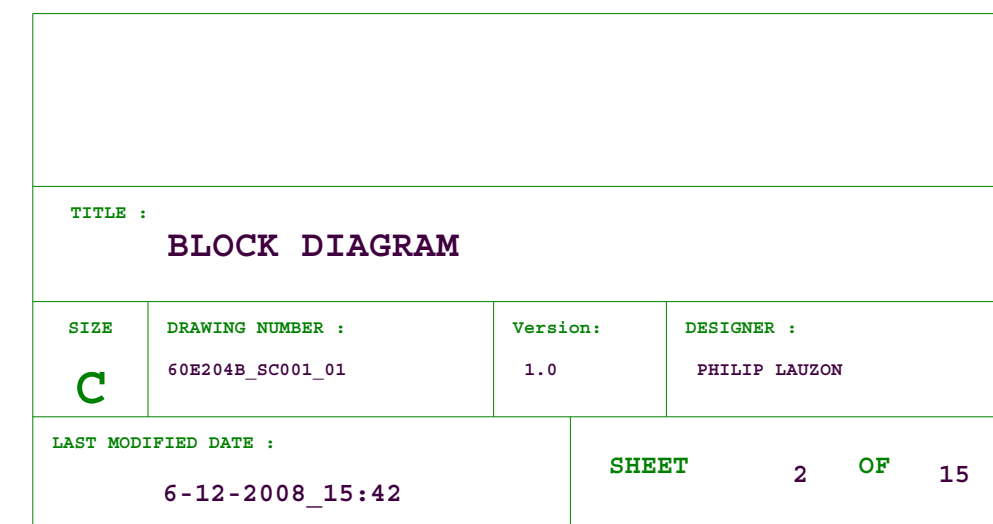
Tsi382A (LQFP) Evaluation Board Schematics

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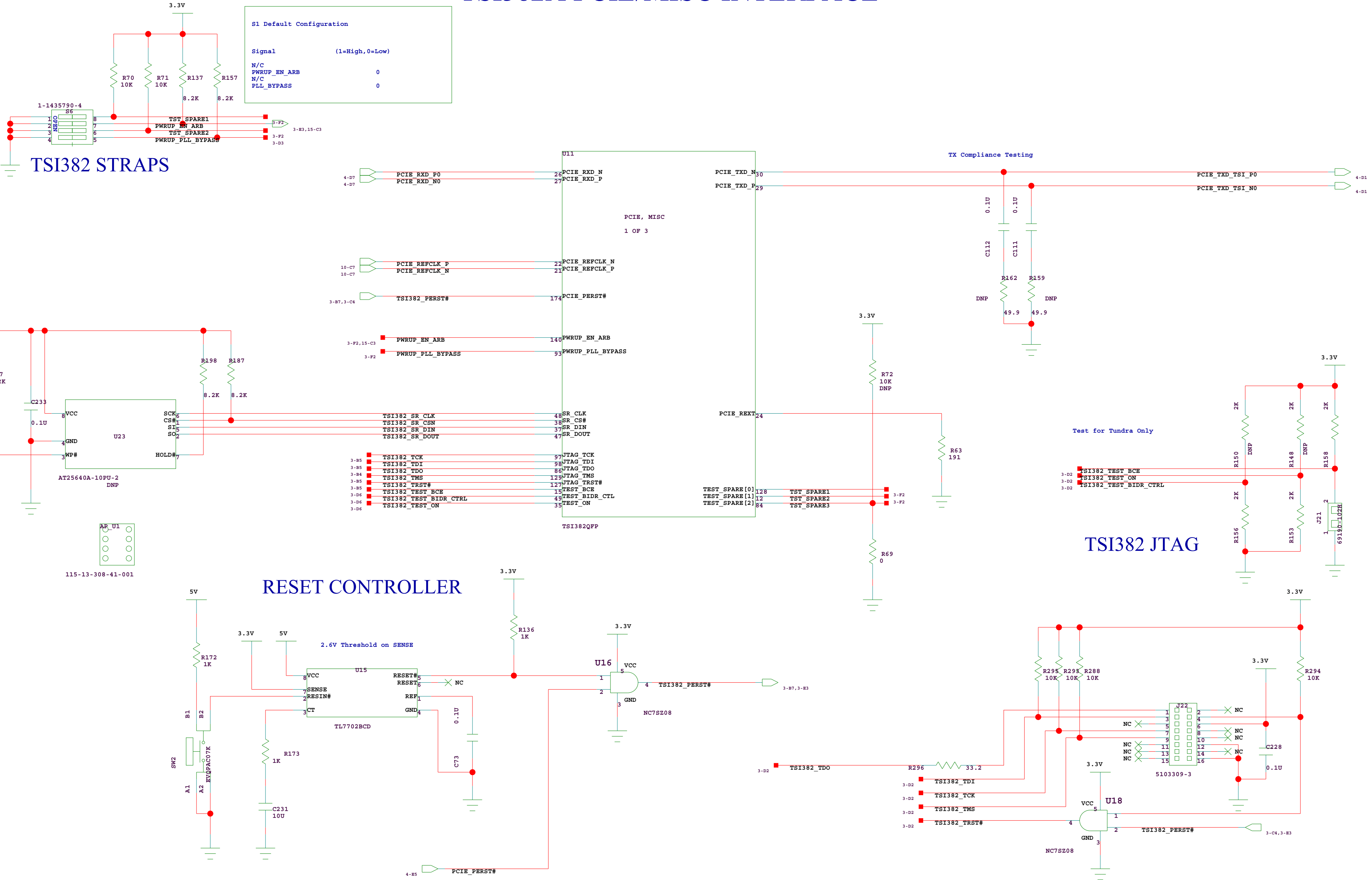
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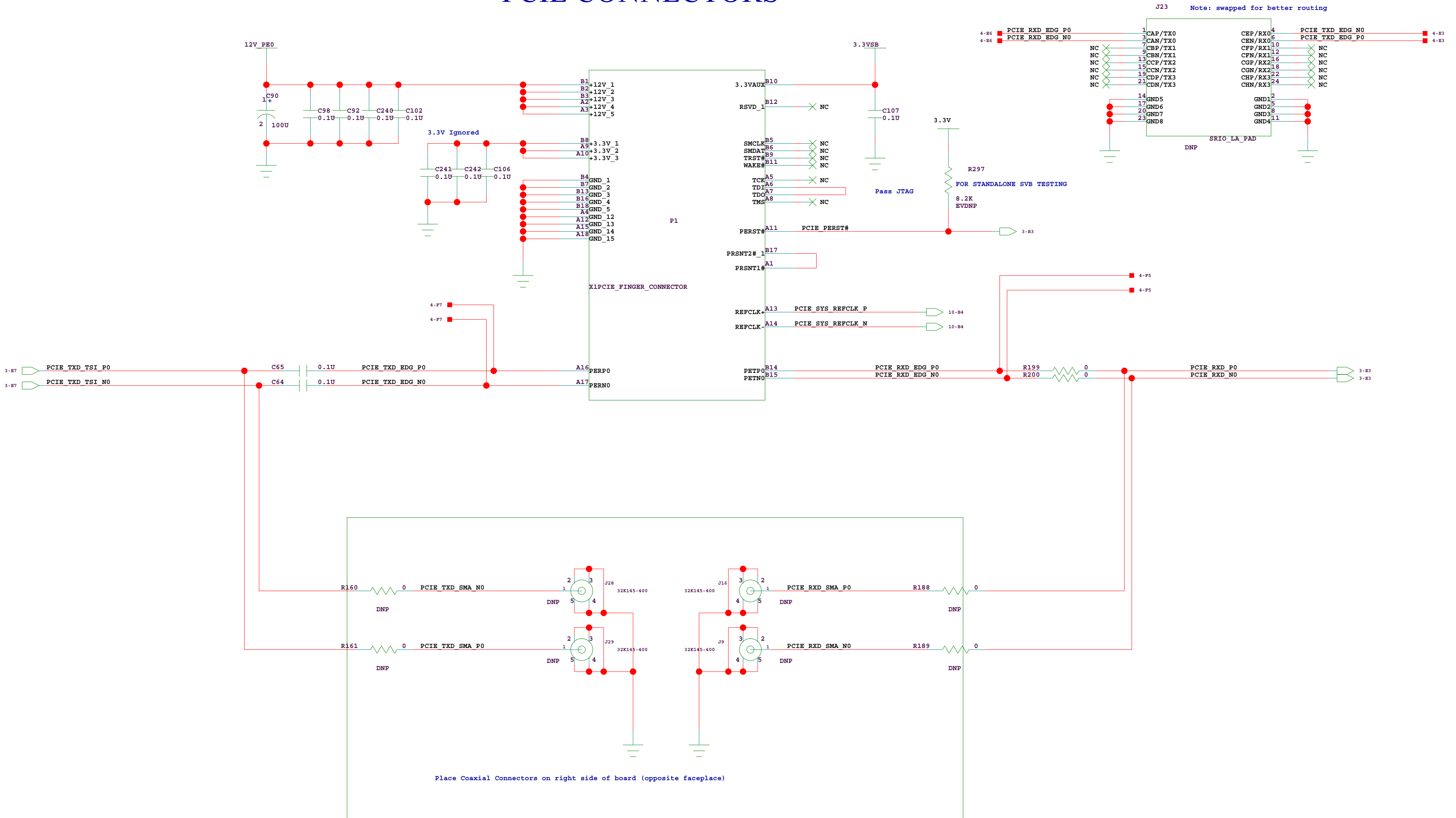


TSI382A PCIE/MISC INTERFACE



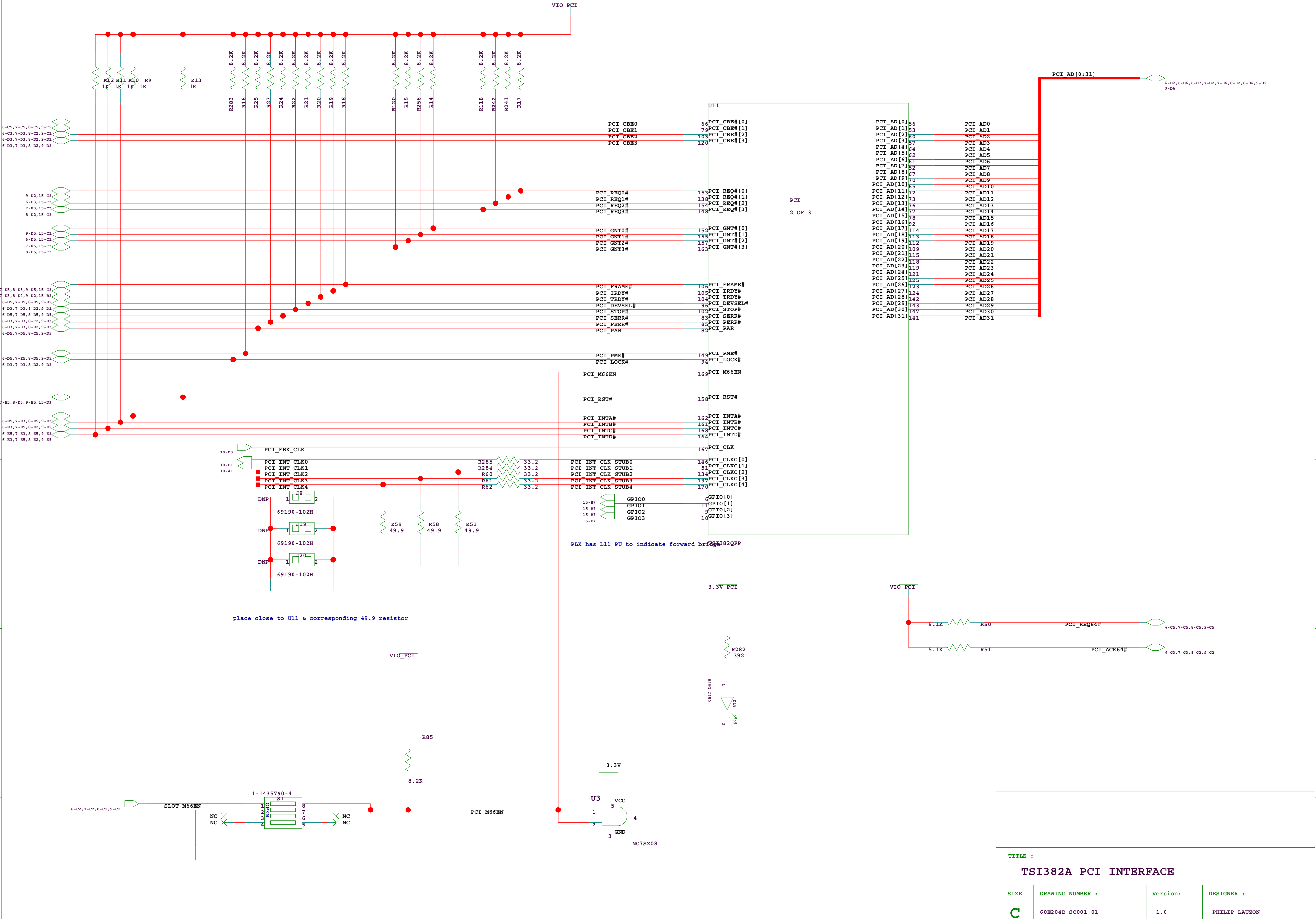
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PCIE CONNECTORS



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TSI382A PCI INTERFACE



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TSI382A PCI INTERFACE			
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PCI SLOT 0 (R/A)

IDSEL AD16

- Required Interrupt Routing

SLOT : BRIDGE

INTA -> INTA

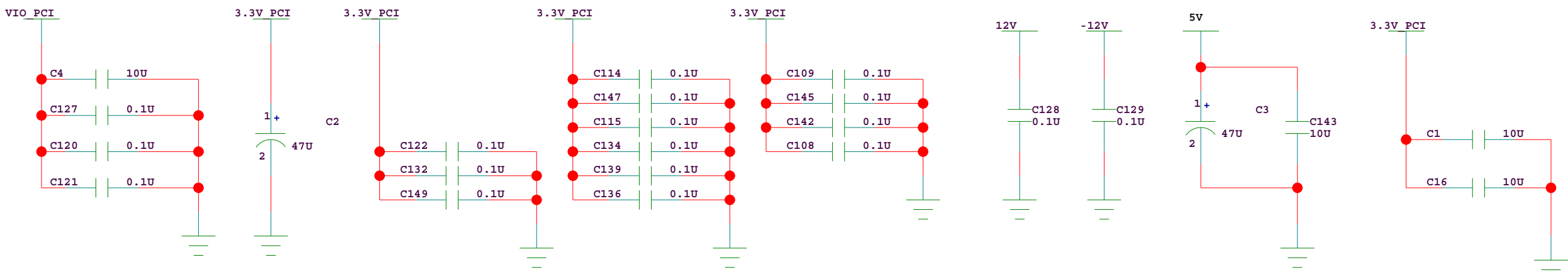
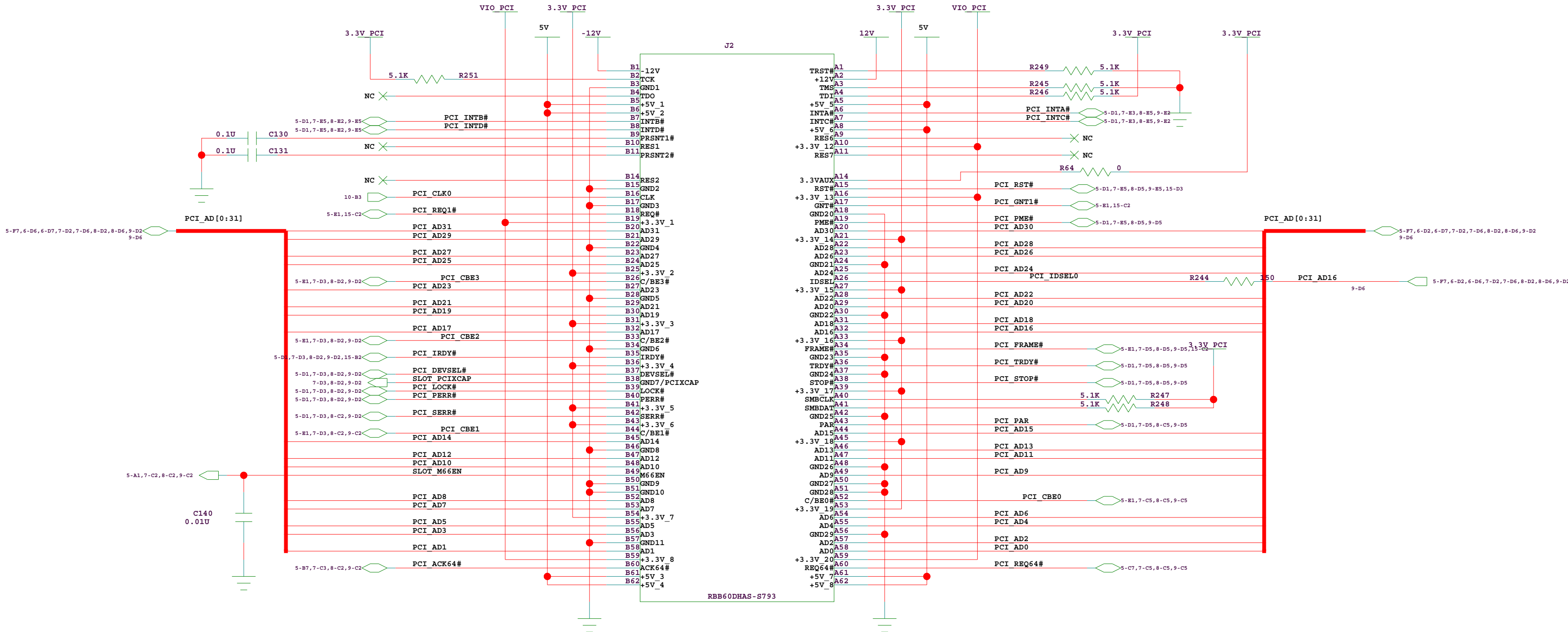
INTB -> INTB

INTC -> INTC

INTD -> INTD

- PCI Clock PCI_CLK0

- PCI Arbitration PCI_GNT#1/PCI_REQ#1



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PCI SLOT 0			
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PCI SLOT 1 (Vertical)

IDSEL AD17

- Required Interrupt Routing

SLOT : BRIDGE

INTA -> INTB

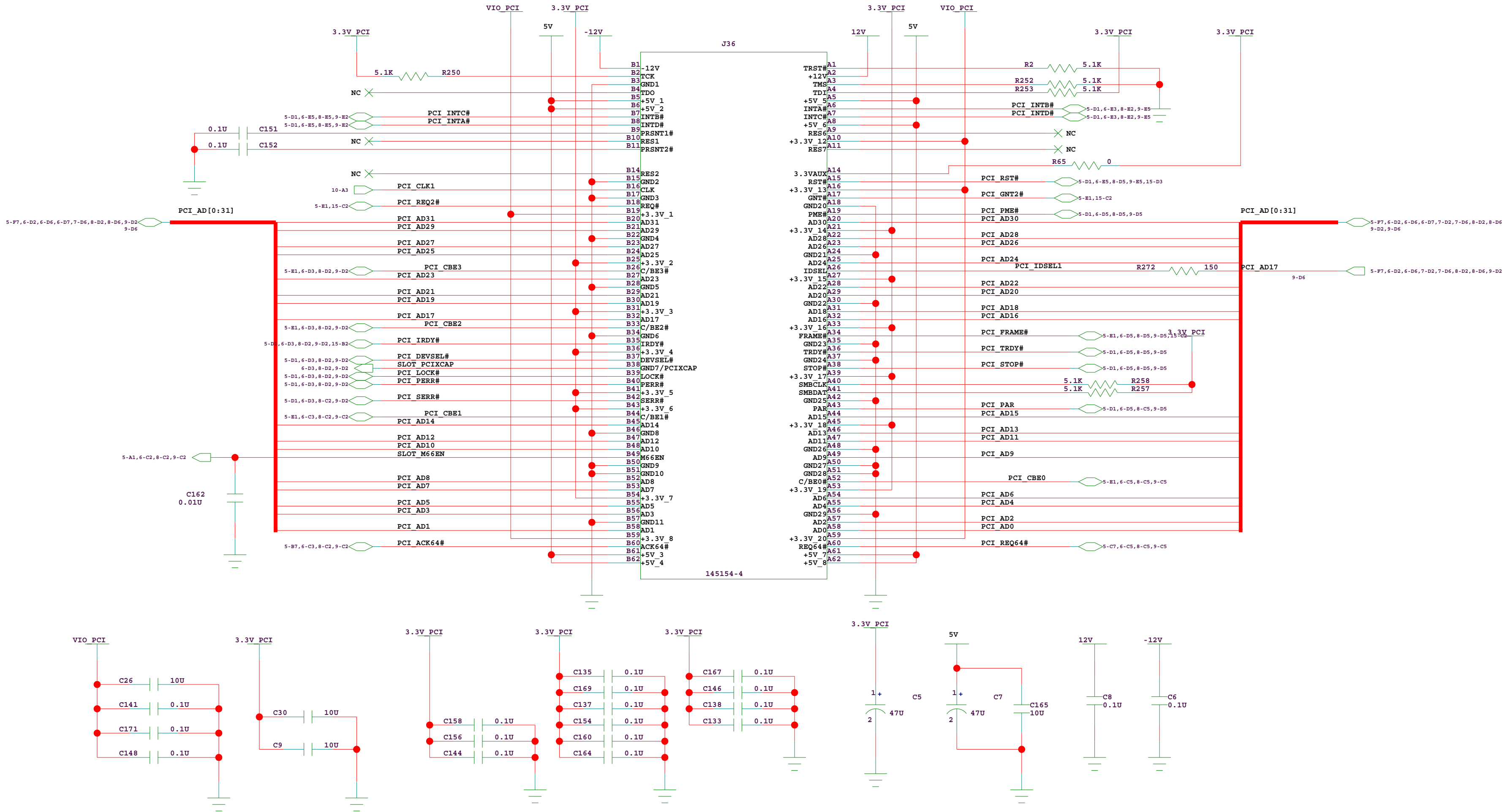
INTB -> INTC

INTC -> INTD

INTD -> INTA

- PCI Clock PCI_CLK1

- PCI Arbitration PCI_GNT#2/PCI_REQ#2



TITLE :			
PCI SLOT 1			
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PCI SLOT 3 (Vertical)

IDSEL AD18

- Required Interrupt Routing

SLOT : BRIDGE

INTA -> INTC

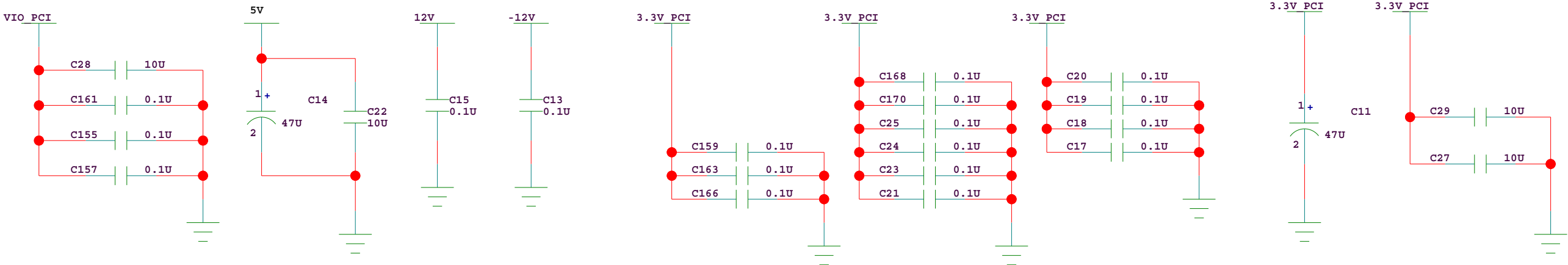
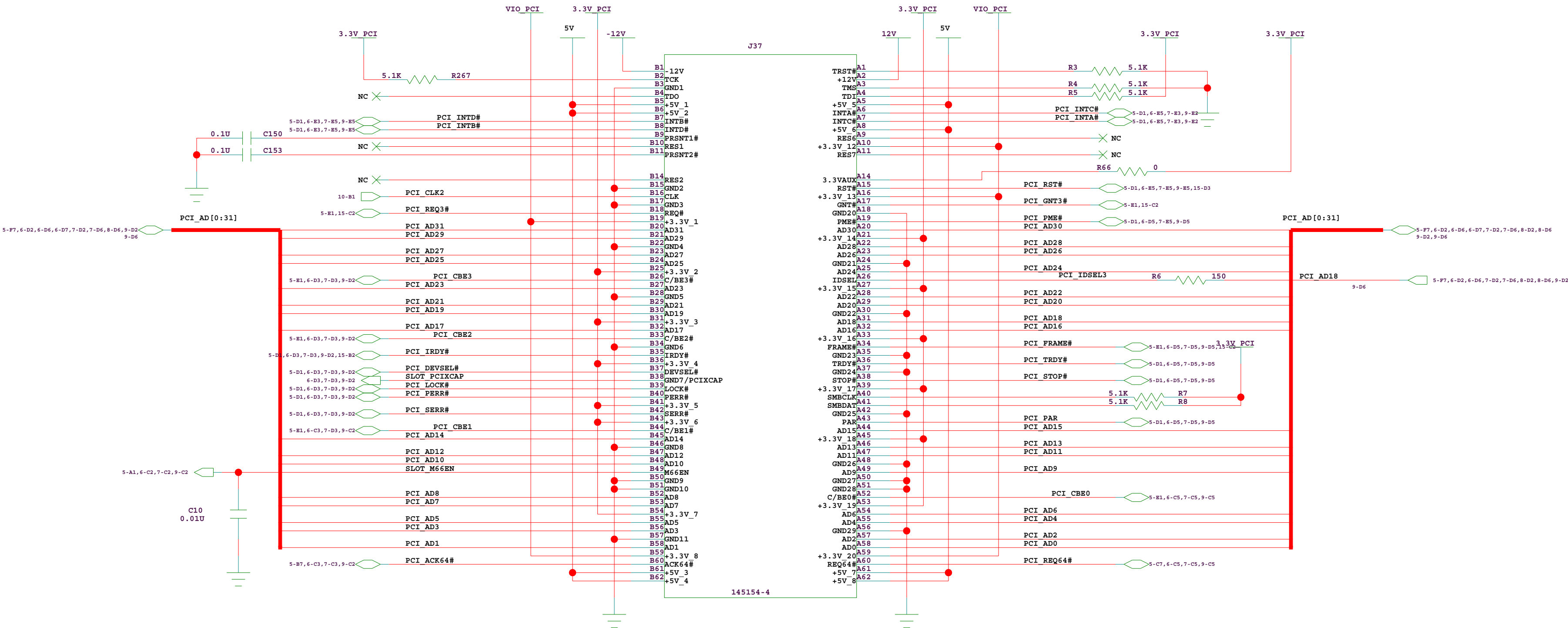
INTB -> INTD

INTC -> INTA

INTD -> INTB

- PCI Clock PCI_CLK2

- PCI Arbitration PCI_GNT#3/PCI_REQ#3



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PCI SLOT 3			
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PCI SLOT 2 (Vertical)

IDSEL AD19

- Required Interrupt Routing

SLOT : BRIDGE

INTA -> INTD

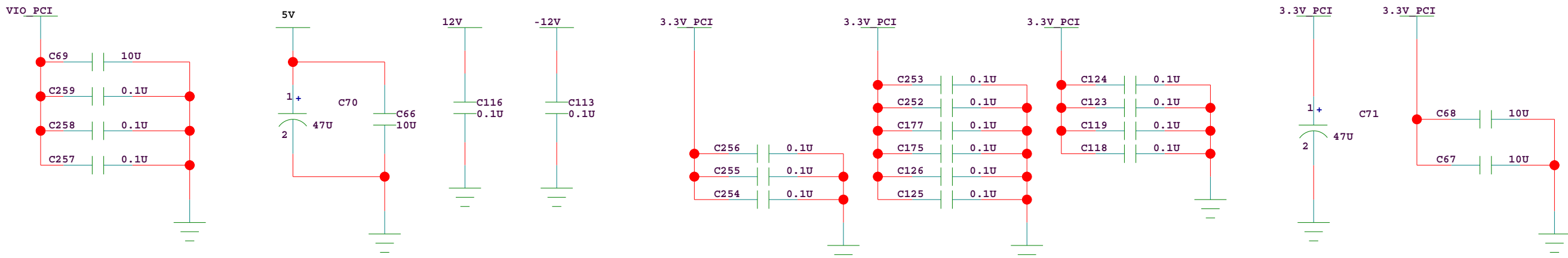
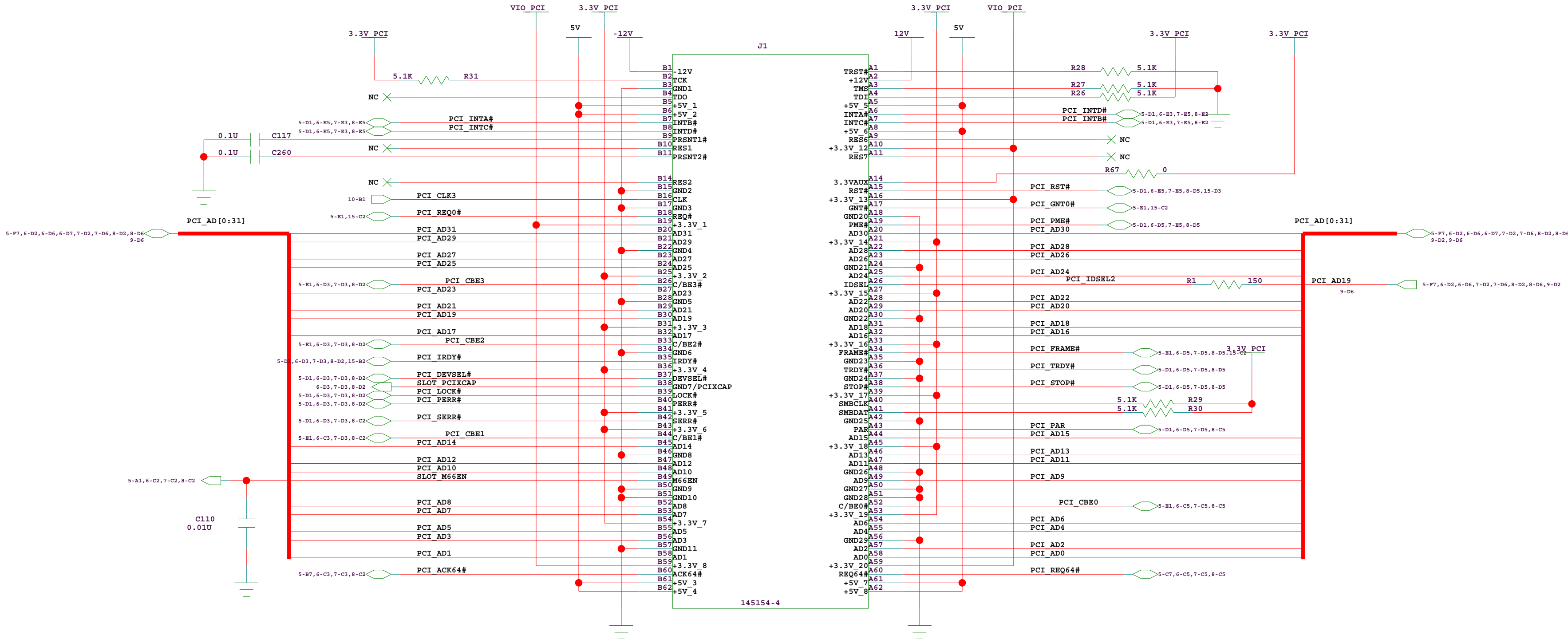
INTB -> INTA

INTC -> INTB

INTD -> INTC

- PCI Clock PCI_CLK3

- PCI Arbitration PCI_GNT#0/PCI_REQ#0



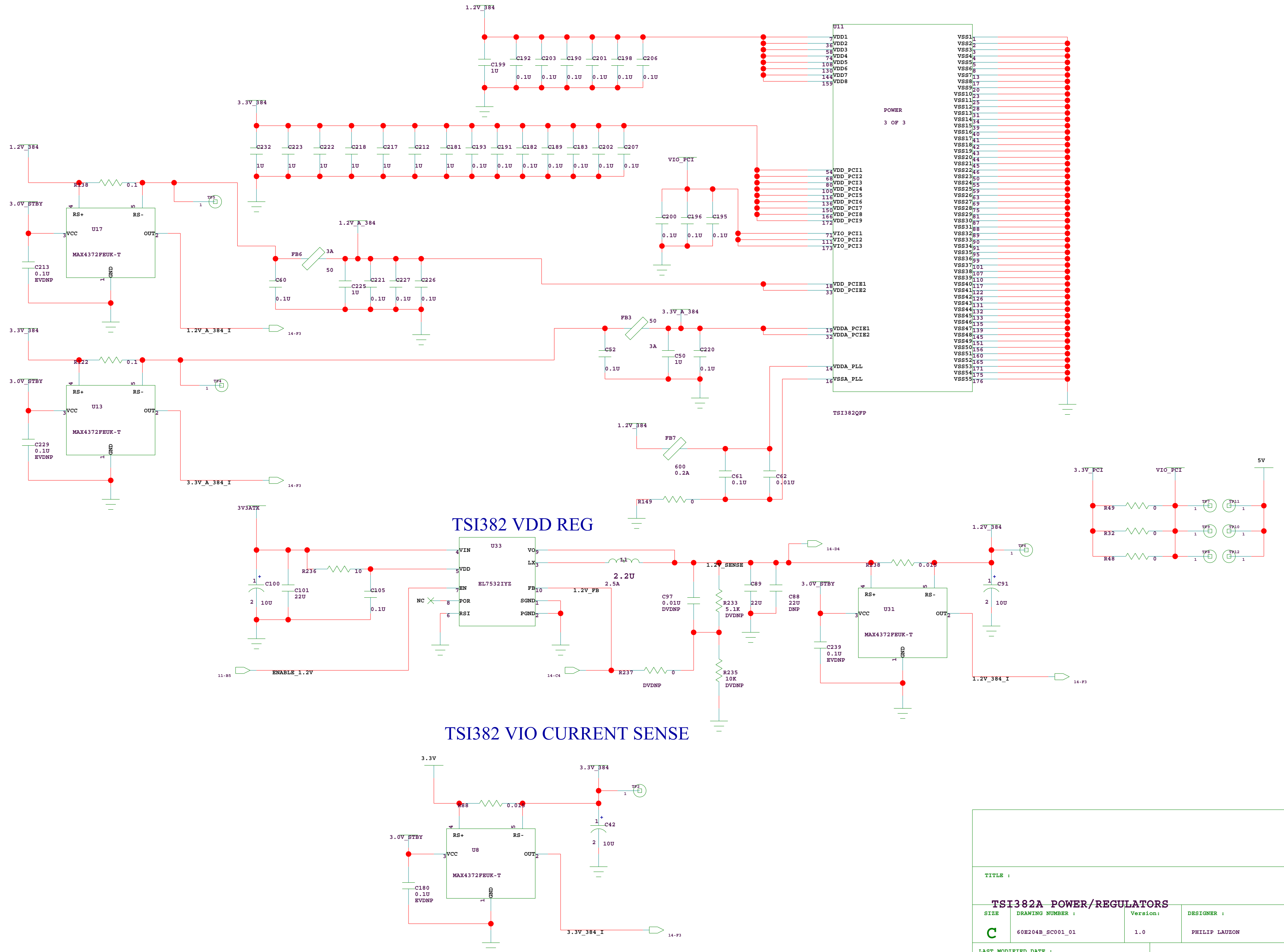
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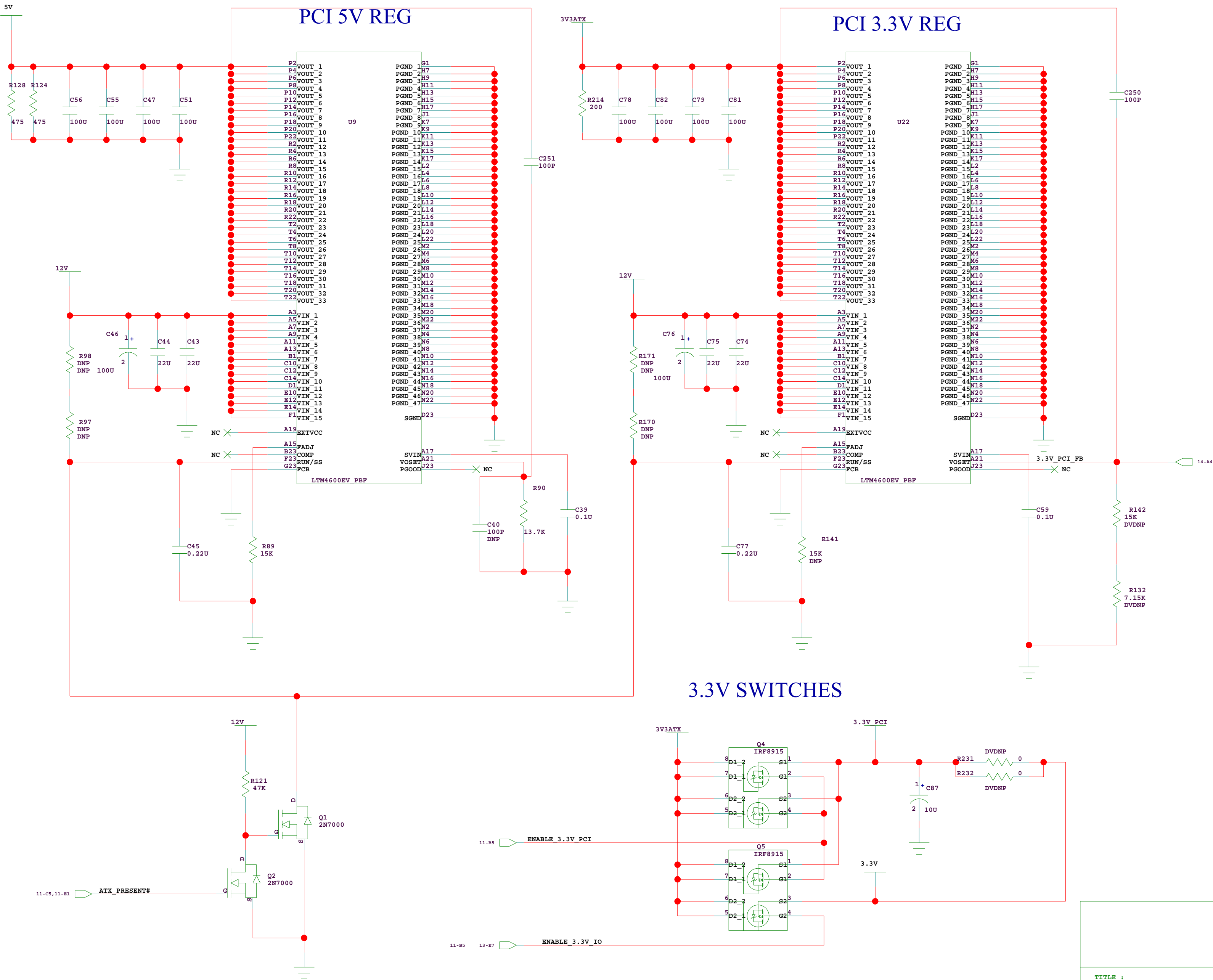


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CLOCK DISTRIBUTION			
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TSI382A POWER/REGULATORS



PCI POWER REGULATORS

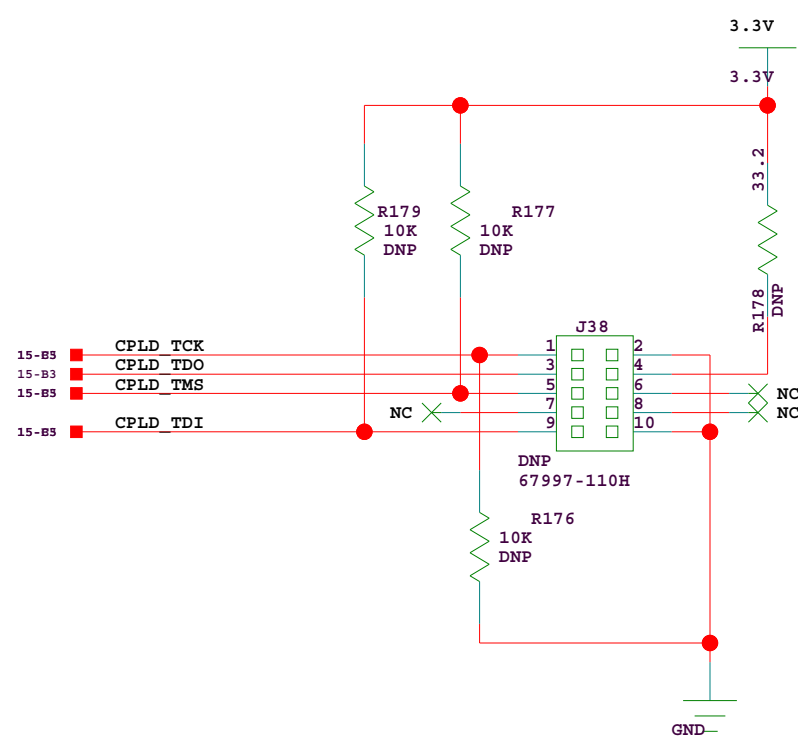


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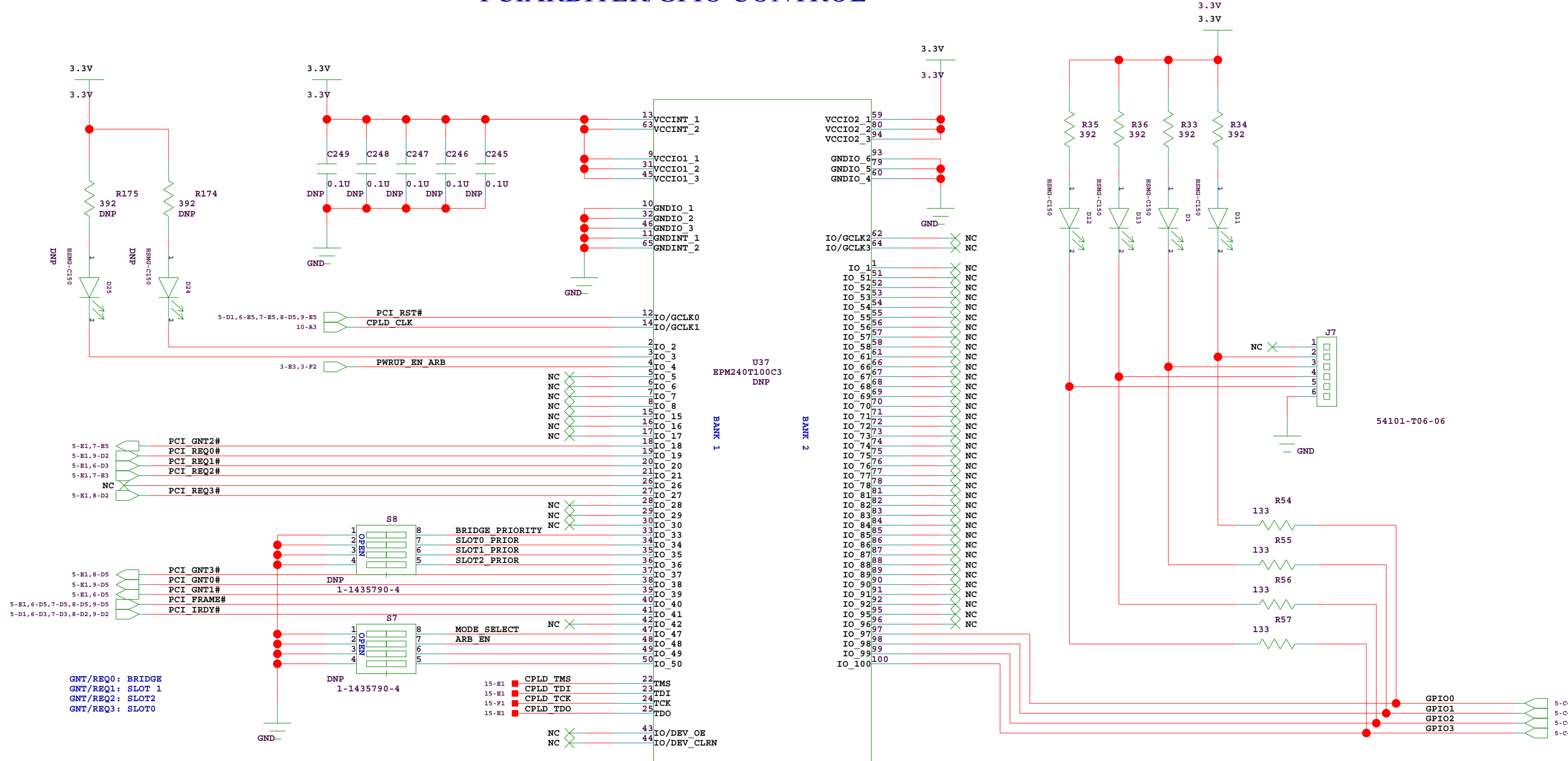
TSI382 V/I ADC



TEST DEVICES CONT'D



PCIARBITER/GPIO CONTROL



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TEST DEVICES CONT'D			
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