

DA14535 Module Development Kit

Design Name	DA14535-Module-Devkit
Ref. Number	610-06-A
Version	A0.0
Date	24 APRIL 2023
Designer	KL/NT

History Table

Version	Date	Comments
A0.0	24/APR/2023	Initial Release

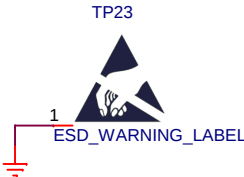
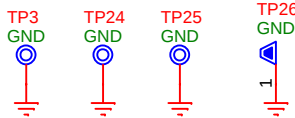
Configuration note

NOTE box

Changes in
this Version.

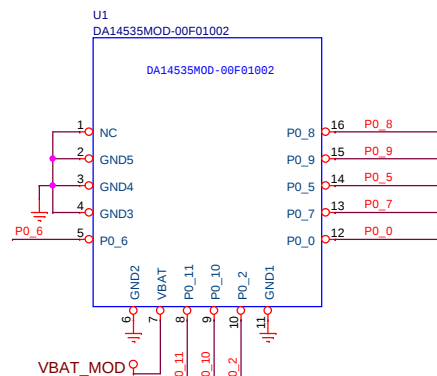
TP1 FIDUCIAL

TP2 FIDUCIAL

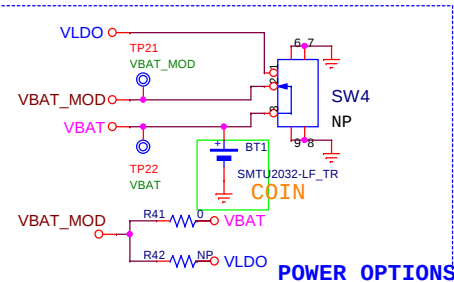
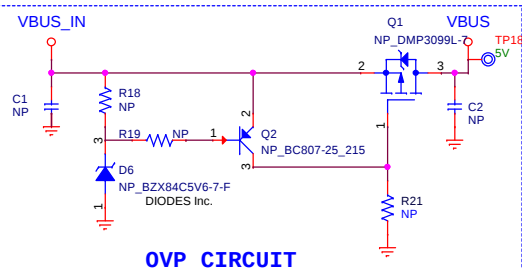
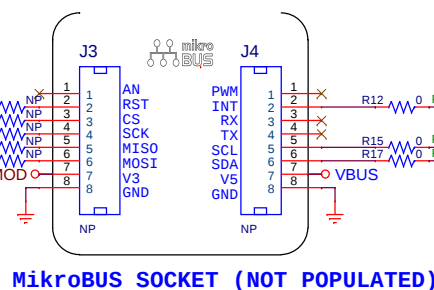
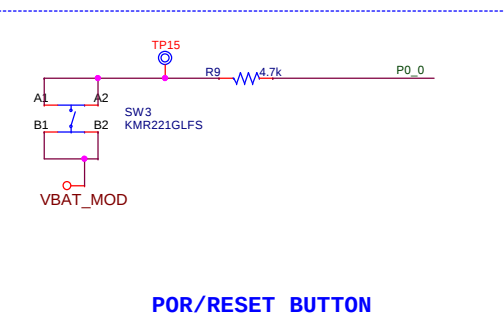
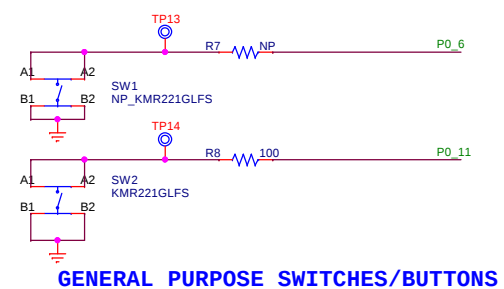
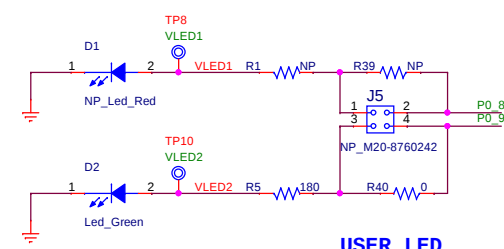


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Card Edge Connector (PCI-E)



NOTE: Pin #1 (NC) has no internal connection and is recommended to be connected to GND



		MikroBus GPIO multiplexing			
		MikroBus			
	Peripherals	I2C	SPI	Other	
P0_6	SW1		SCK		
P0_11	SW2	SDA (default)	MISO		
P0_8	LED D1	SCL (default)	CS		
P0_9	LED D2		MOSI		
P0_7					INT RST (default)

