

RZ/T2L Group

Modbus Sample Program Package Guide

Introduction

This document is a guide for the Modbus sample program package for RZ/T2L microcontrollers. This guide describes the structure of the package and the features of the sample programs.

Target Device

RZ/T2L Group

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1. Overview

This guide describes the structure of this package and the features of the sample programs.

1.1 Abbreviations / Definitions

Table 1.1 Abbreviations / Definitions

Index	Abbreviations / Definitions	Description
1	IP	Internet Protocol
2	TCP	Transmission Control Protocol
3	USB	Universal Serial Bus
4	PC	Personal Computer
5	SW	Switch
6	EWARM	Embedded Workbench® for Arm
7	LED	Light Emitting Diode
8	Wireshark	A free packet capture tool for monitoring LAN traffic
9	Client	Modbus TCP Master / Requesting device
10	Server	Modbus TCP Slave / Responding device
11	MAC	Media Access Control Address
12	LAN	Local Area Network
13	DHCP	Dynamic Host Configuration Protocol
14	PHY	Physical Layer
15	LwIP	Lightweight IP
16	RS485	EIA-485 / Serial communication standard
17	SBOM	Software Bill of Materials

1.2 Reference

1.2.1 About RZ/T2L

Technical information regarding Modbus can be found on the official Modbus Organization website, while details on the RZ/T2L are provided by Renesas.

Table 1.2 Technical References for RZ/T2L

Document Type	Document Title	Description	Document No.
User's Manual	RZ/T2L Group User's Manual Hardware	RZ/T2L Group User's Manual Hardware	r01uh0985ej****
Datasheet	RZ/T2L Group Datasheet	RZ/T2L Group Datasheet	r01ds0409ej****
Getting Started	RZ/T2, RZ/N2 Group FSP Getting Started Guide	RZ/T2, RZ/N2 Group Flexible Software Package Getting Started Guide	r01an6434ej****
User's Manual	Renesas Starter Kit+ for RZ/T2L User's Manual	Renesas Starter Kit+ for RZ/T2L User's Manual	r20ut5164ej****
Reference Guide	Modicon Modbus Protocol Reference Guide Rev.J	Modbus Protocol Reference Guide Rev.J	PI-MBUS-300 Rev. J
Specification	Modbus Application Protocol Specification V1.1b3	Modbus Application Protocol Specification V1.1b3	v1.1b3

2. Features

This package includes firmware for the Modbus communication stack optimized for Renesas RZ/T2L series processors.

This package includes the following features:

- Comprehensive Modbus Protocol Support:
 - Modbus TCP (Ethernet / Server)
 - Modbus RTU (Serial / Client & Server)
 - Modbus ASCII (Serial / Client & Server)
- Modbus Gateway Functions:
 - Transparent bridging between Modbus TCP and Modbus RTU
 - Protocol conversion and routing
- Supported Function Codes:
 - Read / Write Holding Registers
 - Read / Write Coils
 - Read Discrete Inputs
 - Read Input Registers
- I/O Functions:
 - DIP switch input
 - LED output

2.1 Package folder structure

After extracting the package, the following structure is obtained.

Table 2.1 Sample package overview

Item	Description
r01an7151xx0410-rzt2l-modbus-package	RZT2L Modbus Sample Package
├── RZT2L_Modbus_RSK_rev0410	Archive for RZT2L (RSK board)
│ ├── Modbus_tcp_lwip	Modbus_tcp_lwip folder
│ ├── common	Common folder
│ ├── oss	Lightweight TCP/IP stack for embedded systems
│ ├── renesas	Renesas-specific Modbus application code
│ └── project	IDE project folder for tcp
│ ├── e2studio	e ² studio project
│ └── ewarm	EWARM project
│ ├── Modbus_serial	Modbus_serial folder
│ ├── common	Common folder
│ ├── renesas	Renesas-specific Modbus application code
│ └── project	IDE project folder for serial
│ ├── e2studio	e ² studio project
│ └── ewarm	EWARM project
│ ├── Modbus_gateway	Modbus_gateway folder
│ ├── common	Common folder
│ ├── renesas	Renesas-specific Modbus application code
│ └── project	IDE project folder for gateway
│ ├── e2studio	e ² studio project
│ └── ewarm	EWARM project
│ └── Modbus_tool	Tool folder
└── SBOM	SBOM folder
├── r01an7151ej0410-rzt2l-modbus-package.pdf	This guide
├── r01an7150ej0410-rzt2l-modbus-tcp.pdf	Application note for Modbus tcp projects
├── r01an7176ej0410-rzt2l-modbus-serial.pdf	Application note for Modbus serial projects
└── r01an8340ej0410-rzt2l-modbus-gateway.pdf	Application note for Modbus gateway projects

3. Requirements (Software and Hardware)

This project was developed and tested in the environment listed below using the following boards and tools.

3.1 Requirements for this sample package

Table 3.1 Requirements

Category	Name	Version	Description
Board	Renesas Starter Kit+ for RZ/T2L	-	Starter Kit for RZ/T2L Evaluation and Development Renesas Starter Kit+ for RZ/T2L
IDE	Flexible Software Package	V4.1.0	Renesas Flexible Software Package (FSP)
	e ² studio	2026-04.1	
	- GNU ARM Embedded Toolchain	13.3.1.arm-13-24 for CR52	
	IAR Systems - Embedded Workbench for ARM	9.60.3	
	GNU ARM Embedded Toolchain	13.3.1.arm-13-24 for CR52	
Emulator	J-Link™	8.60	SEGGER SEGGER - The Embedded Experts - Downloads - J-Link / J-Trace
Software	ModbusDemoApplication.exe	V1.3	

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Nov 30, 2023	-	First edition issued
2.00	Aug 31, 2024	-	Modbus TCP updated to FSP2.0.0
2.20	Apr 4, 2025	-	Modbus updated to FSP2.2.0
3.00	Nov 7, 2025	-	Supports RZ/T2 FSP3.0.0
4.00	Apr 3, 2026	-	Supports RZ FSP4.0.0
4.10	Jul 31, 2026	-	Supports RZ FSP4.1.0 Added new functions Added SBOM support

Website and Support

Renesas Electronics Website

<http://www.renesas.com/>

Inquiries

<http://www.renesas.com/contact/>

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The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Precaution against Electrostatic Discharge (ESD)

A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity. Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.

2. Processing at power-on

The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power reaches the level at which resetting is specified.

3. Input of signal during power-off state

Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.

4. Handling of unused pins

Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.

5. Clock signals

After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.

6. Voltage application waveform at input pin

Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between V_{IL} (Max.) and V_{IH} (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between V_{IL} (Max.) and V_{IH} (Min.).

7. Prohibition of access to reserved addresses

Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.

8. Differences between products

Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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