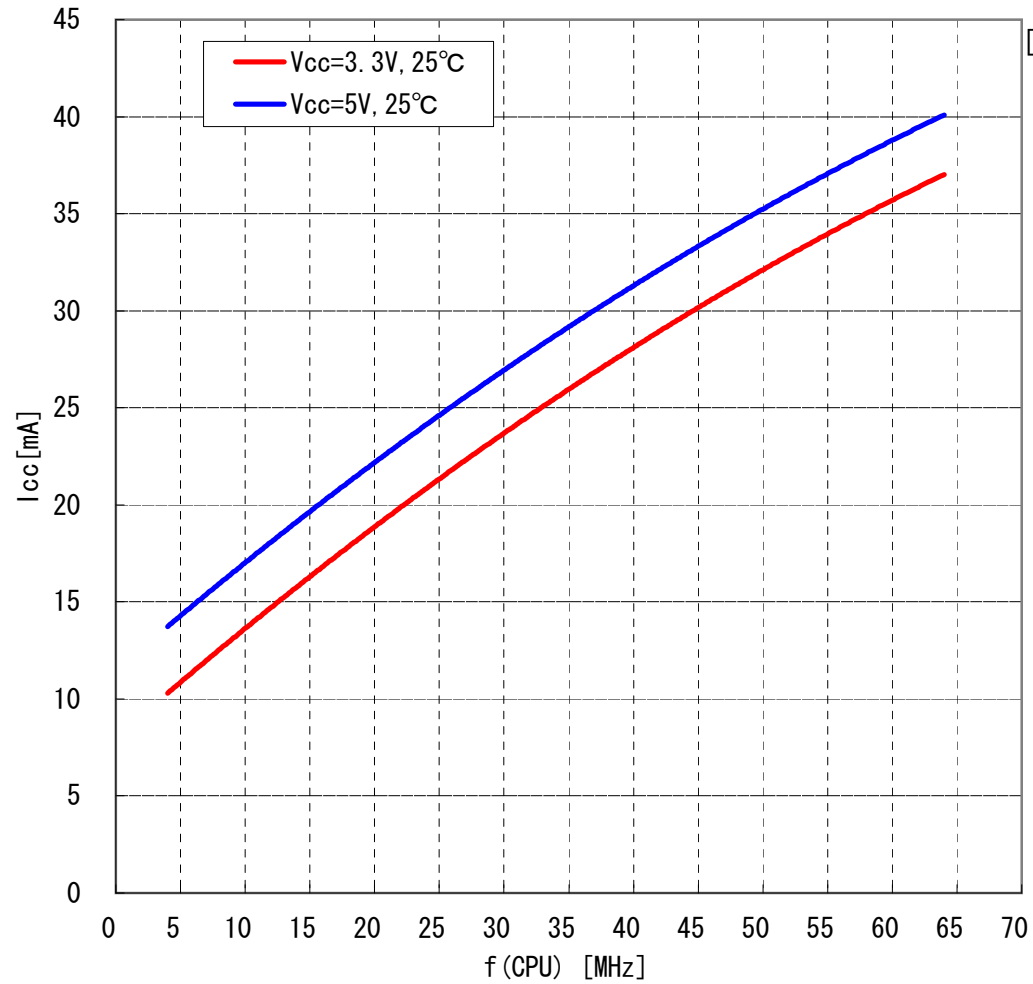R32C/116,117,118 Group Icc-f(CPU) (PLL mode)
Peripheral Function = Full active

[Measurement condition]
In single-chip mode, output pins are left open and others are connected to VSS
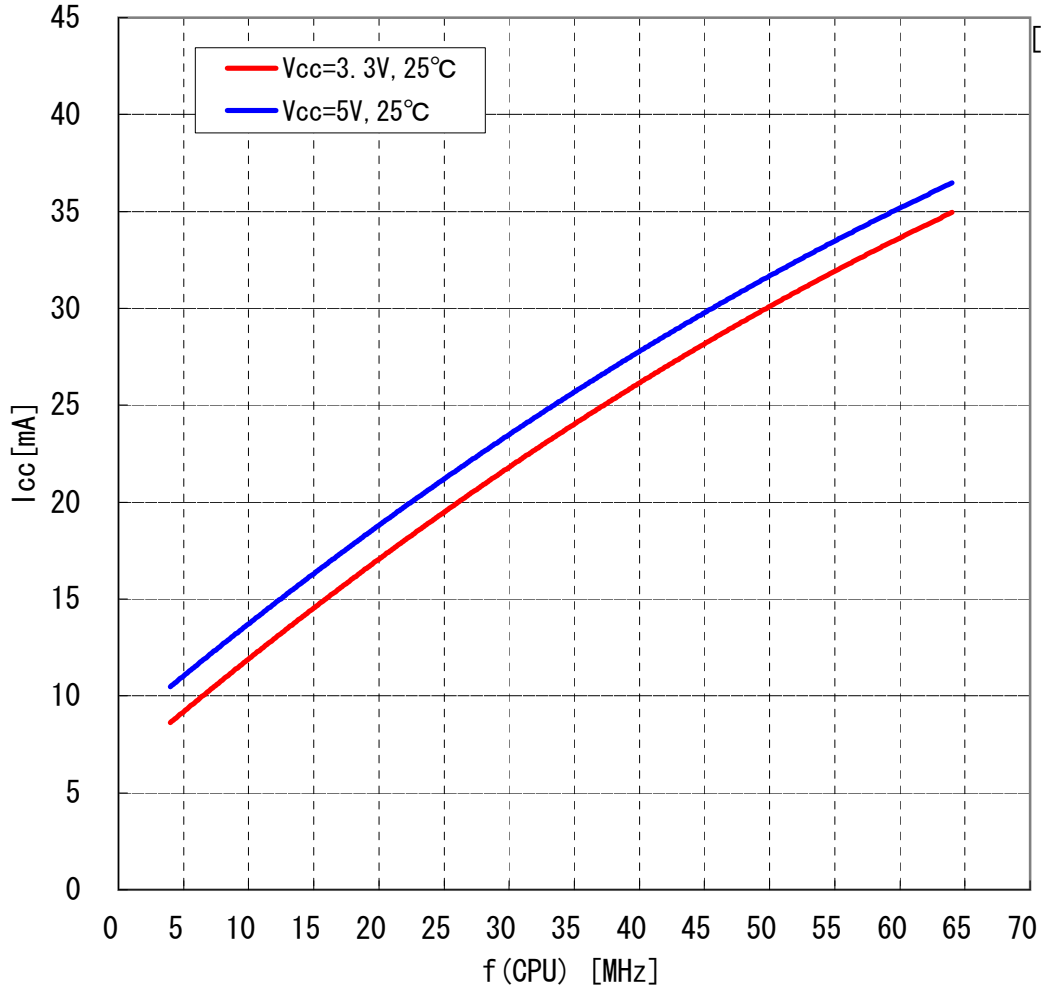
f(XIN)=8MHz
XIN-XOUT Drive power:strong

Active:XIN,PLL
Stopped:XCIN,OCO

Legend:
— Vcc=3.3V, 25℃
— Vcc=5V, 25℃

Y-axis: Icc[mA]
X-axis: f(CPU) [MHz]

R32C/116,117,118 GroupIcc-f(CPU) (PLL mode)
Peripheral Function = not operation

[Measurement condition]
In single-chip mode, output pins are left open
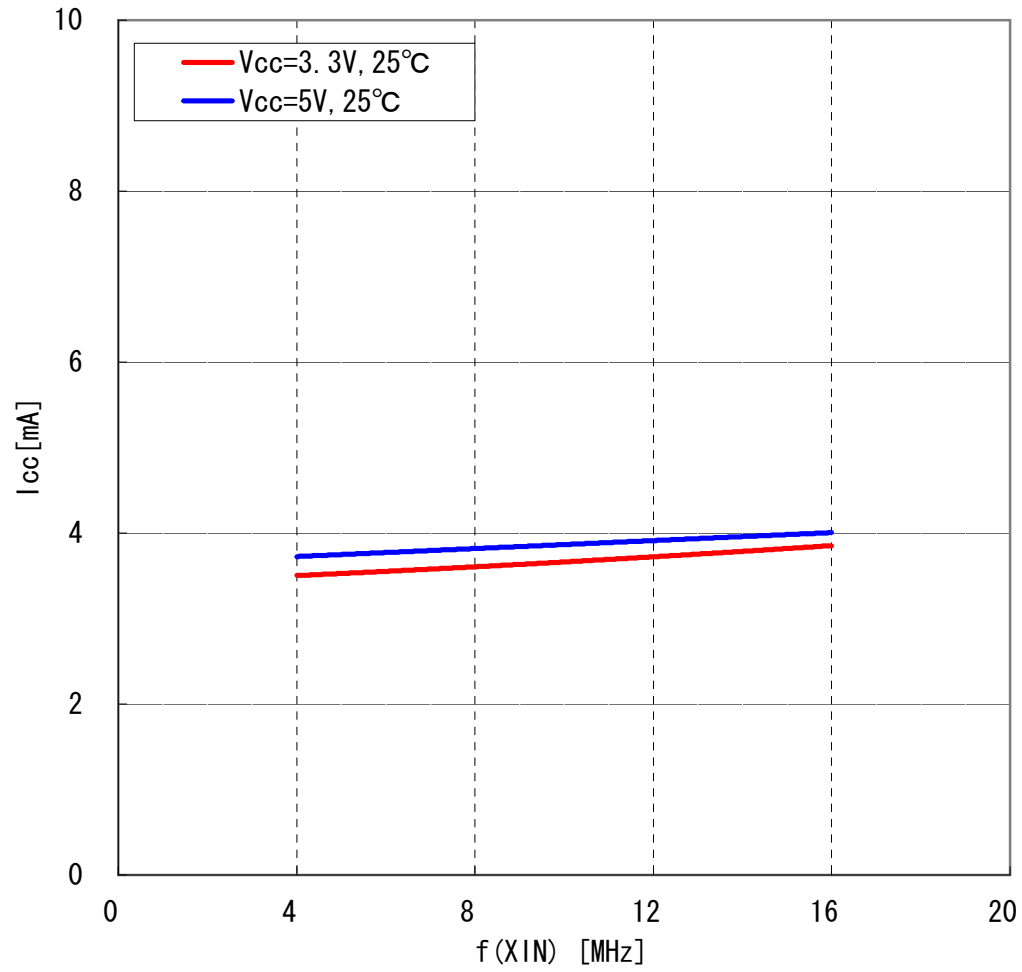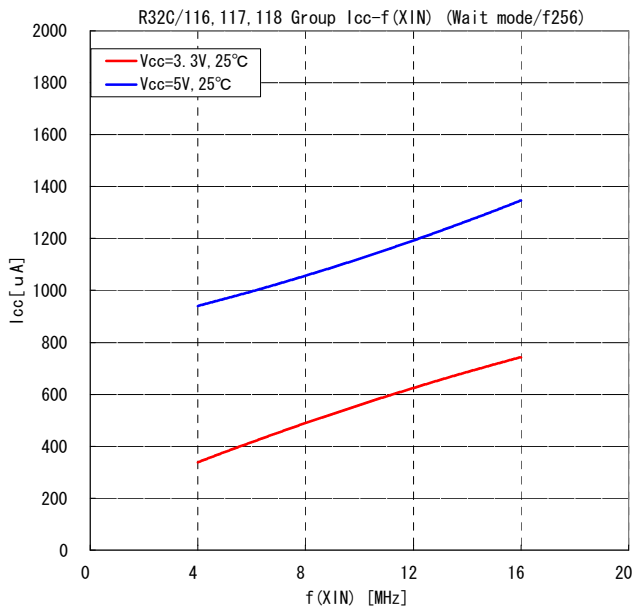and others are connected to VSS

f(XIN)=8MHz
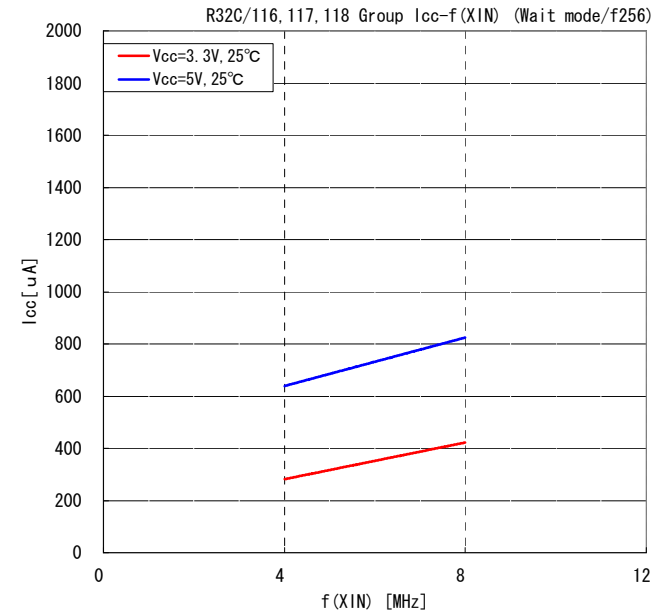XIN-XOUT Drive power:strong

Active:XIN,PLL
Stopped:XCIN,OCO

Legend:
— Vcc=3.3V, 25°C
— Vcc=5V, 25°C

X-axis: f(CPU) [MHz]
Y-axis: Icc[mA]

R32C/116,117,118 Group Icc-f(XIN) (During RESET)

R32C/116,117,118 Group Icc-f(XIN) (Wait mode/f256)

[Measurement condition]
 In single-chip mode, output pins are
 left open and others are connected to VSS

f(CPU)=f(XIN)/256 MHz
XIN-XOUT Drive power:low

Active:XIN
Stopped:PLL,XCIN,OCO

— Vcc=3.3V, 25℃
— Vcc=5V, 25℃

R32C/116,117,118 Group Icc-f(XIN) (Wait mode/f256)
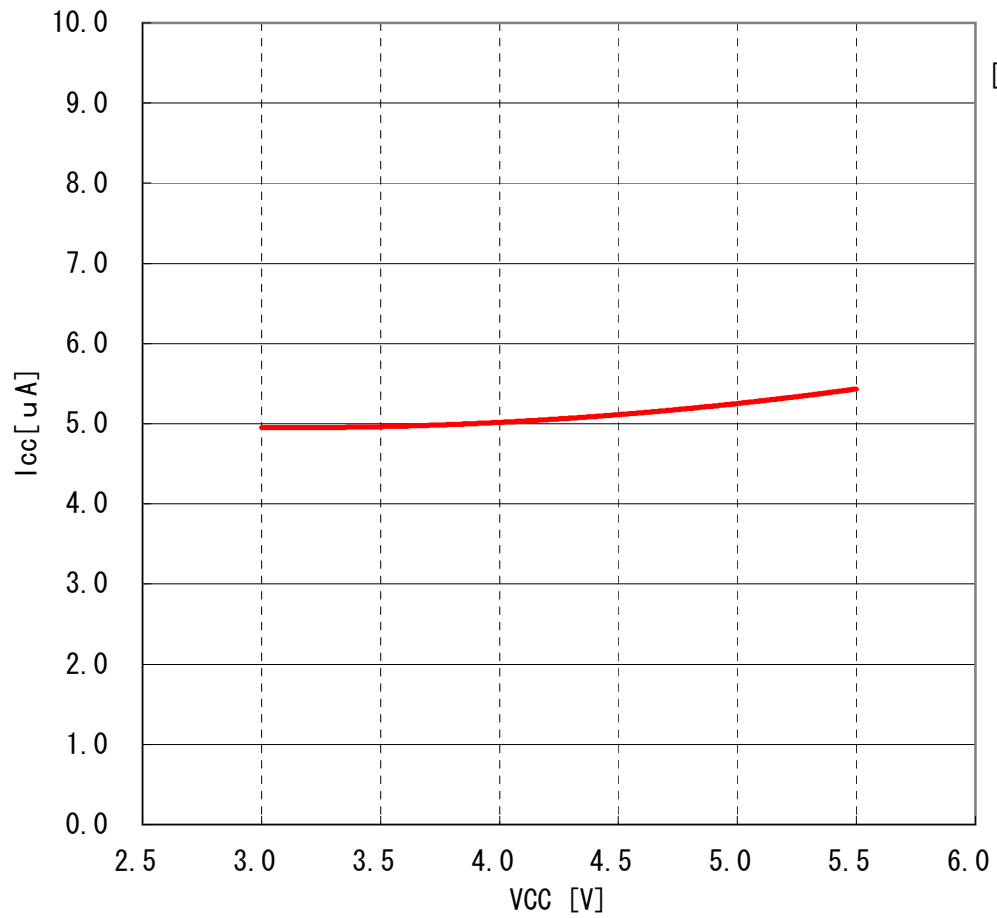
[Measurement condition]
 In single-chip mode, output pins are
 left open and others are connected to VSS

f(CPU)=f(XIN)/256 MHz
XIN-XOUT Drive power:Super low

Active:XIN
Stopped:PLL,XCIN,OCO

— Vcc=3.3V, 25℃
— Vcc=5V, 25℃

R32C/116, 117, 1118 Group Icc-Vcc(Stop mode)

[Measurement condition]
 In single-chip mode, output pins are left open
 and others are connected to VSS

 Stopped:all clocks
 Main regulator shutdown