

M16C/6C

Standard Characteristics

Related Part No. :

R5F36CA6NFA, R5F36CA6NFB, R5F36CA6DFA, R5F36CA6DFB,
R5F36CAENFA, R5F36CAENFB, R5F36CAEDFA, R5F36CAEDFB,
R5F36CAKNFA, R5F36CAKNFB, R5F36CAKDFA, R5F36CAKDFB,
R5F36CAMNFA, R5F36CAMNFB, R5F36CAMDFA, R5F36CAMDFB

Standard Characteristics

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1. Input voltage(1)

(1) "H" Input voltage

■ Related Pin

P0_0-P0_7, P1_0-P1_7, P2_0-P2_7, P3_0

(in single-chip mode)

P3_1-P3_7, P4_0-P4_7, P5_0-P5_7, P6_0-P6_7,
P7_0-P7_7, P8_0-P8_7, P9_0-P9_7, P10_0-P10_7

■ Operating Condition

Topr=-40,25,85 (degreesC)

Vcc=2.7V to 5.5V

■ Related Pin

P0_0-P0_7, P1_0-P1_7, P2_0-P2_7, P3_0

(data input in memory expansion and microprocessor mode)

■ Operating Condition

Topr=-40,25,85 (degreesC)

Vcc=2.7V to 5.5V

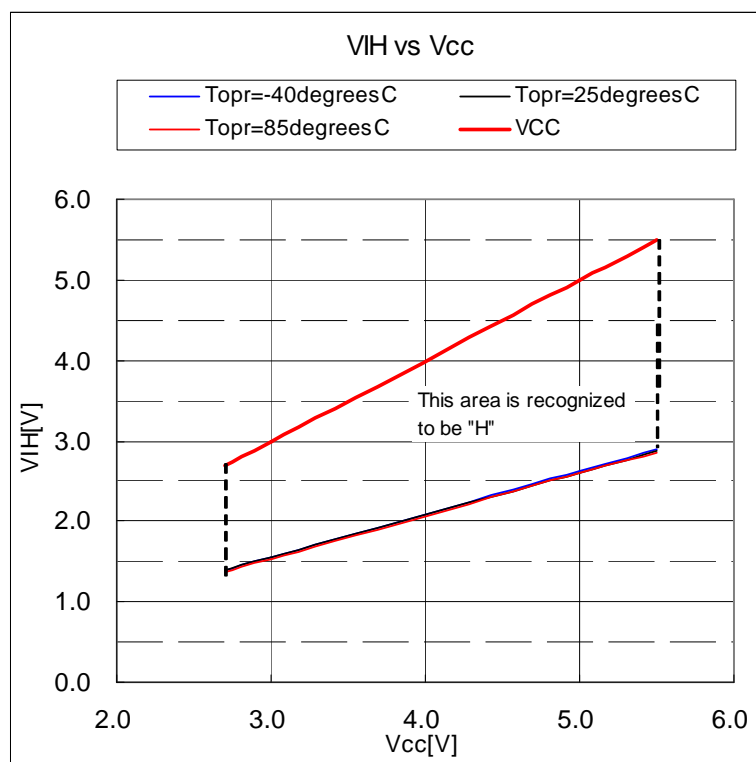


Figure1. VIH vs Vcc(in single-chip mode)

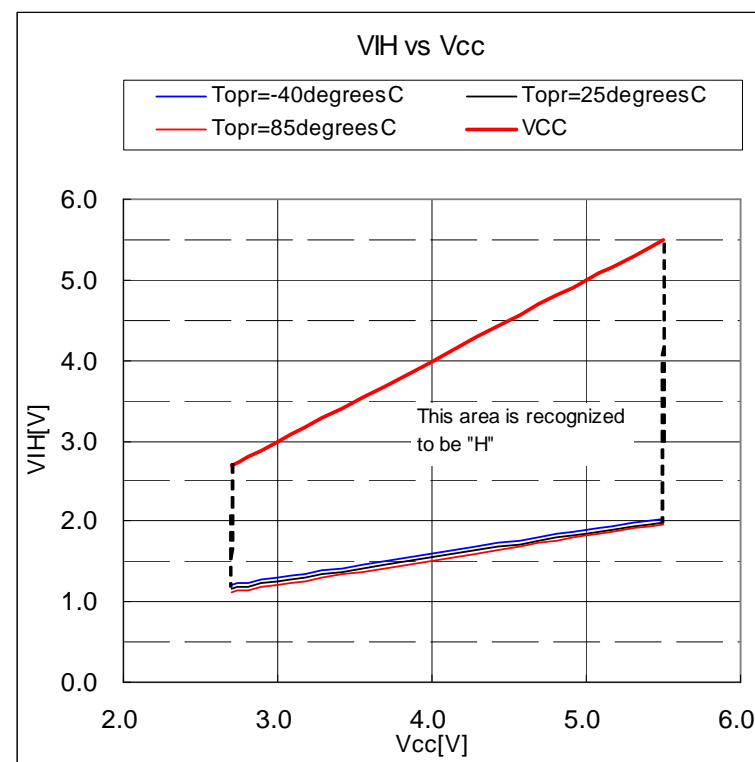


Figure2. VIH vs Vcc(data input in memory expansion and microprocessor mode)

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

1. Input voltage(2)

(2)“L” Input voltage

■Related Pin

P0_0-P0_7, P1_0-P1_7, P2_0-P2_7, P3_0

(in single-chip mode)

P3_1-P3_7, P4_0-P4_7, P5_0-P5_7, P6_0-P6_7,

P7_0-P7_7, P8_0-P8_7, P9_0-P9_7, P10_0-P10_7

■Operating Condition

Topr=-40,25,85 (degreesC)

Vcc=2.7V to 5.5V

■Related Pin

P0_0-P0_7, P1_0-P1_7, P2_0-P2_7, P3_0

(data input in memory expansion and microprocessor mode)

■Operating Condition

Topr=-40,25,85 (degreesC)

Vcc=2.7V to 5.5V

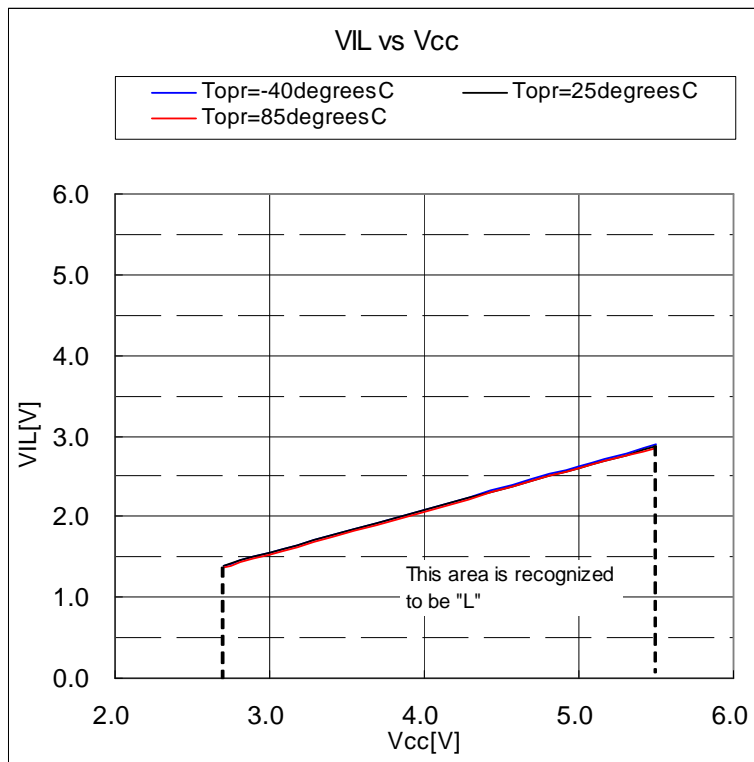


Figure3. VIL vs Vcc(in single-chip mode)

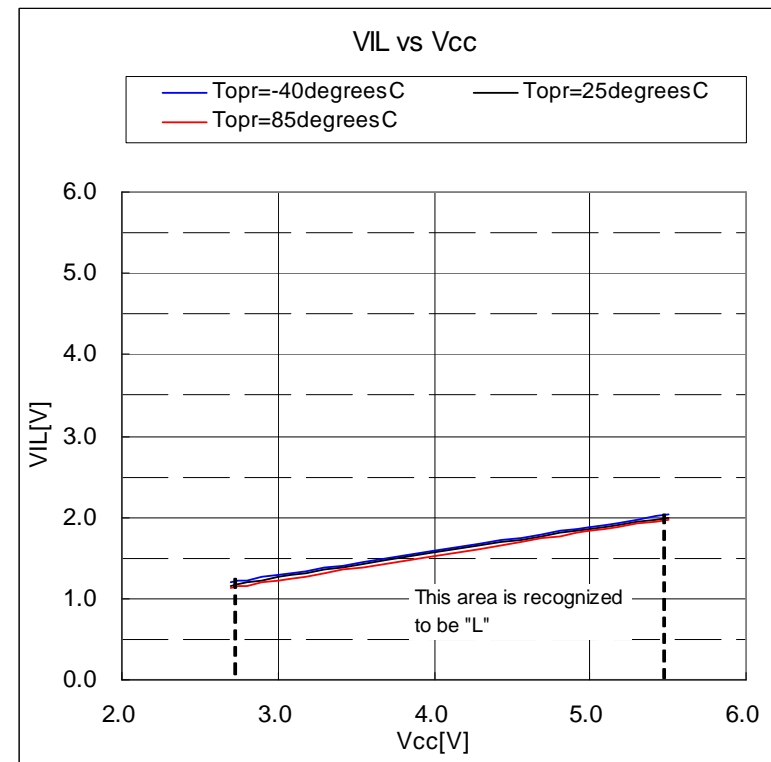


Figure4. VIL vs Vcc(data input in memory expansion and microprocessor mode)

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

2.Hysteresis(1)

(1)Interrupt pin

■Related Pin

HOLD,RDY,TA0IN-TA4IN,TB0IN-TB5IN,INT0-INT7,NMI,ADTRG,CTS0-CTS5,SCL0-SCL5,
SDA0-SDA5,CLK0-CLK5,TA0OUT-TA4OUT,KI0-KI3,RXD0-RXD5,SD,SCLMM,SDAMM,ZP,IDU,IDV,IDW

■Operating Condition

Topr=-40,25,85 (degrees C)

Vcc=2.7V to 5.5V

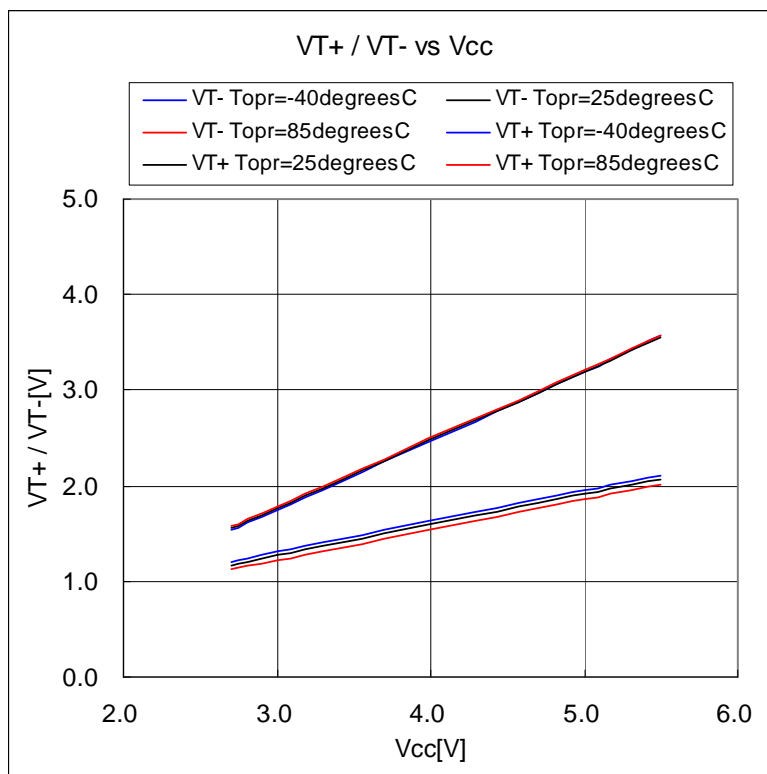


Figure5. VT+/VT- vs Vcc

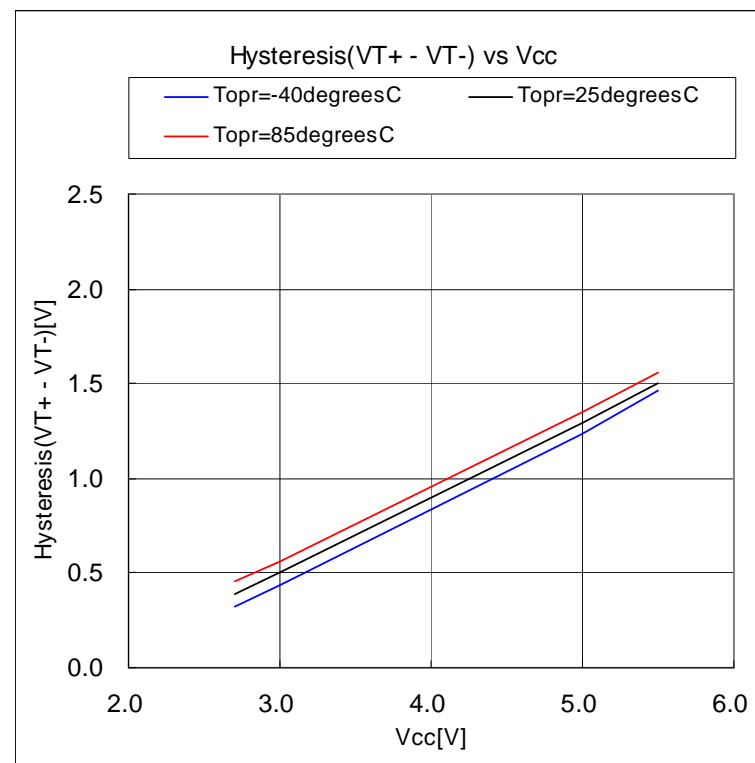


Figure6. Hysteresis (VT+-VT-) vs Vcc

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

2.Hysteresis(2)

(2)RESET

■Related Pin

RESET

■Operating Condition

Topr=-40,25,85 (degrees C)

Vcc=2.7V to 5.5V

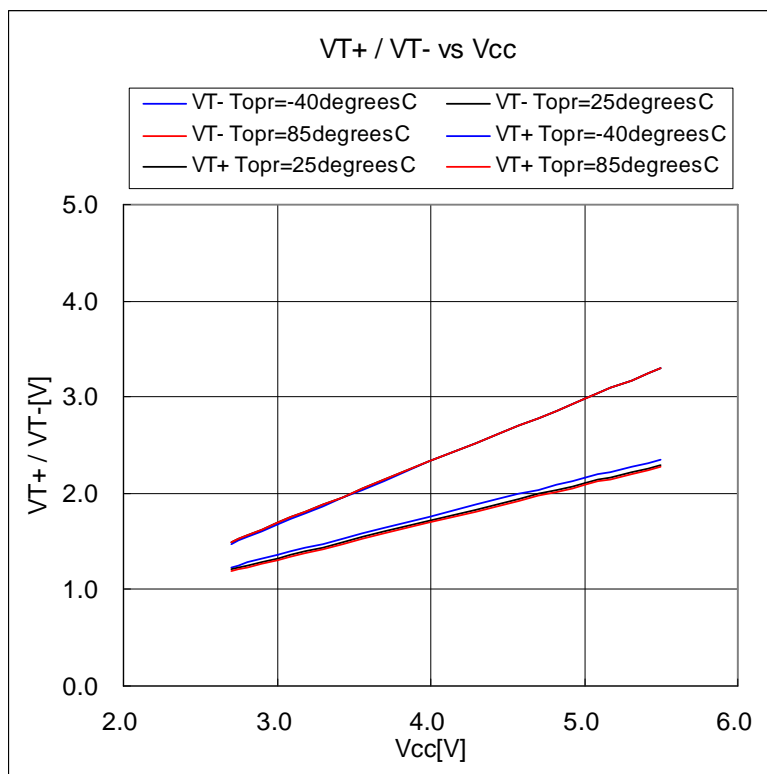


Figure7. VT+/VT- vs Vcc

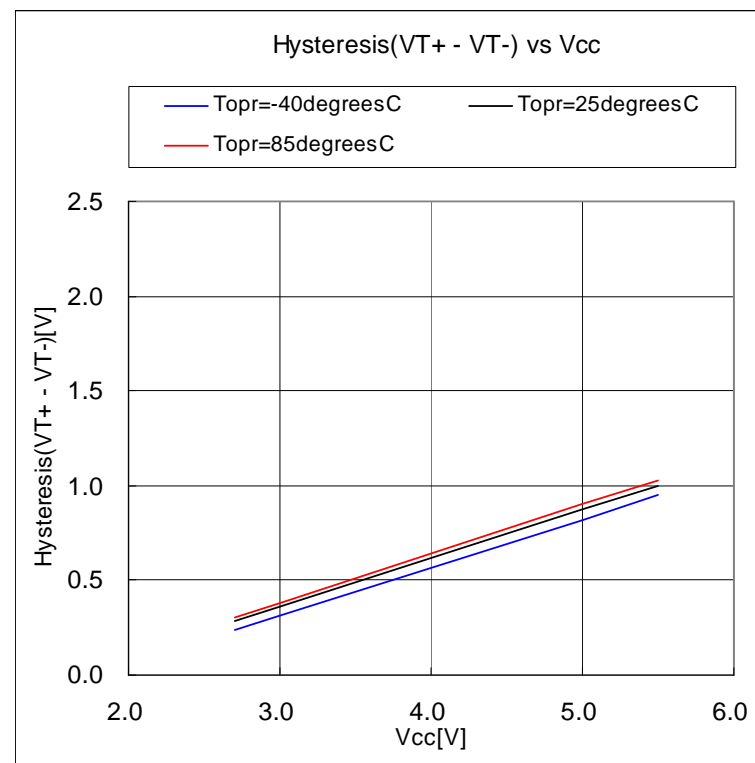


Figure8. Hysteresis (VT+-VT-) vs Vcc

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

3. Output voltage(1)

(1) "H" Output voltage

■ Related Pin

P0_0-P0_7, P1_0-P1_7, P2_0-P2_7, P3_0-P3_7, P4_0-P4_7,
P5_0-P5_7, P6_0-P6_7, P7_2-P7_7, P8_0-P8_4, P8_6-P8_7,
P9_0-P9_7, P10_0-P10_7

■ Operating Condition

Topr=-40,25,85 (degrees C)

Vcc=3.0V

■ Related Pin

P0_0-P0_7, P1_0-P1_7, P2_0-P2_7, P3_0-P3_7, P4_0-P4_7,
P5_0-P5_7, P6_0-P6_7, P7_2-P7_7, P8_0-P8_4, P8_6-P8_7,
P9_0-P9_7, P10_0-P10_7

■ Operating Condition

Topr=-40,25,85 (degrees C)

Vcc=5.0V

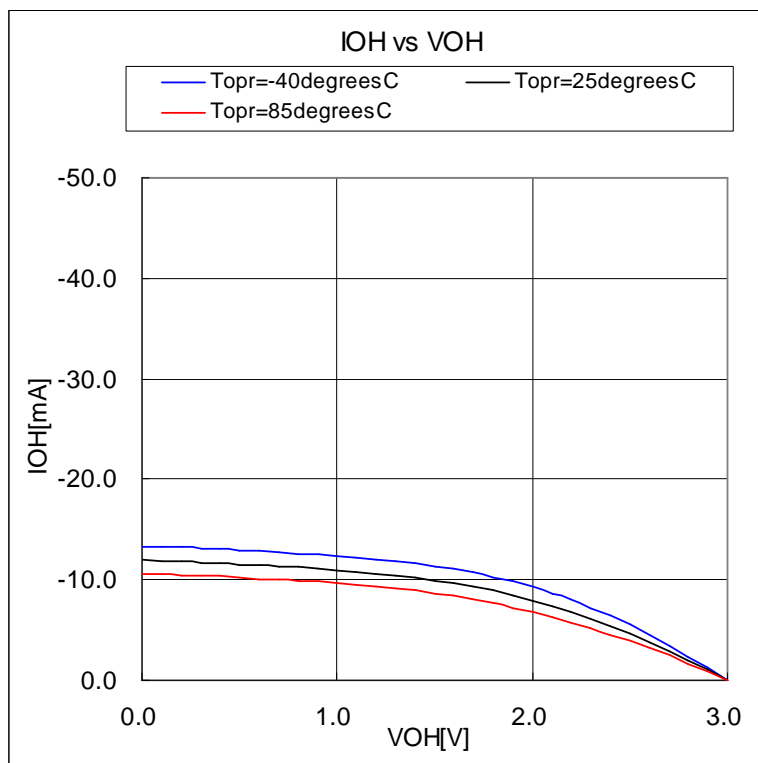


Figure9. IOH vs VOH (Vcc=3.0V)

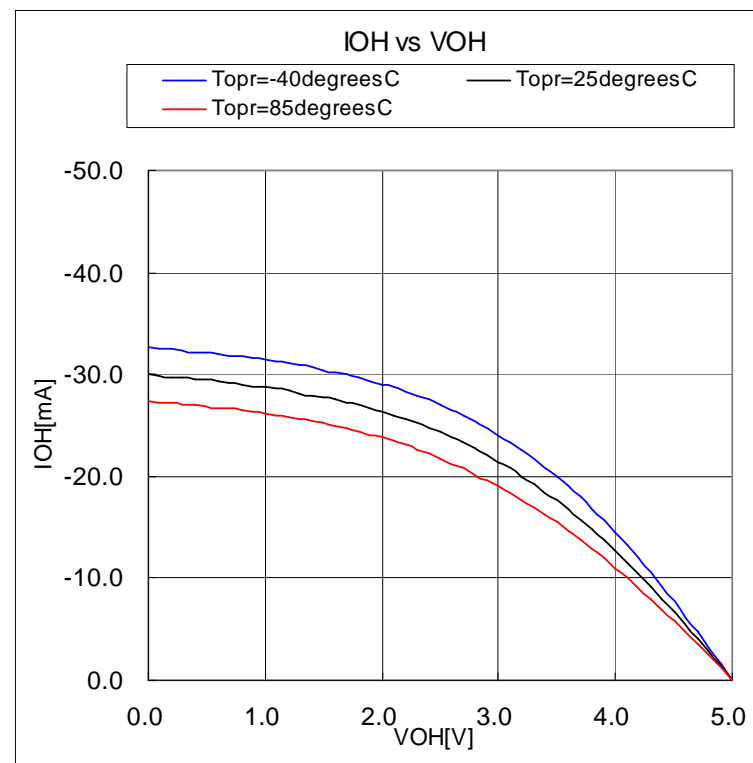


Figure10. IOH vs VOH (Vcc=5.0V)

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

3. Output voltage(2)

(2)“L” Output voltage

■ Related Pin

P0_0-P0_7, P1_0-P1_7, P2_0-P2_7, P3_0-P3_7, P4_0-P4_7,
P5_0-P5_7, P6_0-P6_7, P7_0-P7_7, P8_0-P8_7, P9_0-P9_7,
P10_0-P10_7

■ Operating Condition

Topr=-40,25,85 (degrees C)

Vcc=3.0V

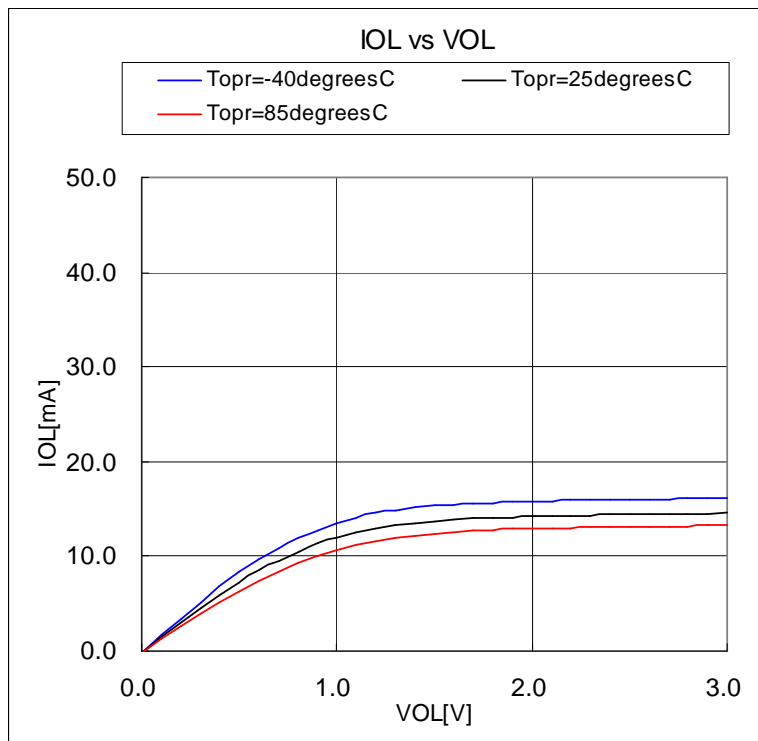


Figure11. IOL vs VOL (Vcc=3.0V)

■ Related Pin

P0_0-P0_7, P1_0-P1_7, P2_0-P2_7, P3_0-P3_7, P4_0-P4_7,
P5_0-P5_7, P6_0-P6_7, P7_0-P7_7, P8_0-P8_7, P9_0-P9_7,
P10_0-P10_7

■ Operating Condition

Topr=-40,25,85 (degrees C)

Vcc=5.0V

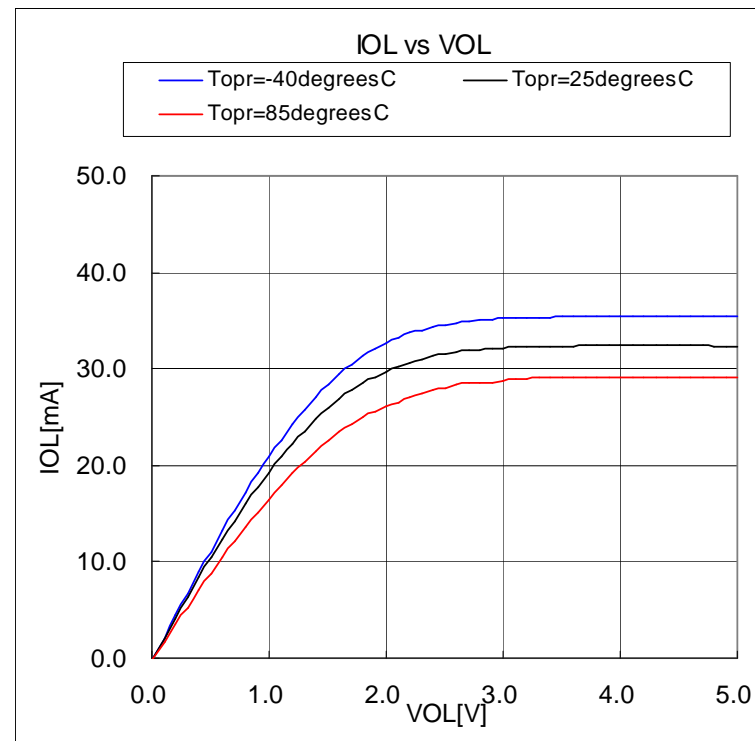


Figure12. IOL vs VOL (Vcc=5.0V)

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

4. Pull-up resistance

(1) Pull-up MOS current(-I_p) vs V_{cc}

■ Related Pin

P0_0-P0_7, P1_0-P1_7, P2_0-P2_7, P3_0-P3_7, P4_0-P4_7,
P5_0-P5_7, P6_0-P6_7, P7_2-P7_7, P8_0-P8_4, P8_6-P8_7,
P9_0-P9_7, P10_0-P10_7

■ Operating Condition

Topr=-40,25,85 (degrees C)

V_{cc}=2.7V to 5.5V

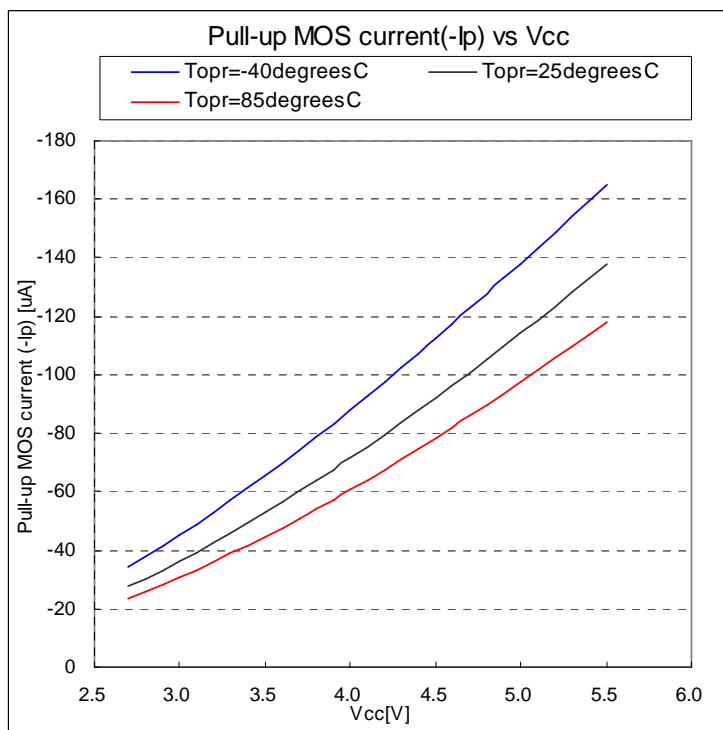


Figure13. Pull-up MOS current(-I_p) vs V_{cc}

(2) R_{pullup} vs V_{cc}

■ Related Pin

P0_0-P0_7, P1_0-P1_7, P2_0-P2_7, P3_0-P3_7, P4_0-P4_7,
P5_0-P5_7, P6_0-P6_7, P7_2-P7_7, P8_0-P8_4, P8_6-P8_7,
P9_0-P9_7, P10_0-P10_7

■ Operating Condition

Topr=-40,25,85 (degrees C)

V_{cc}=2.7V to 5.5V

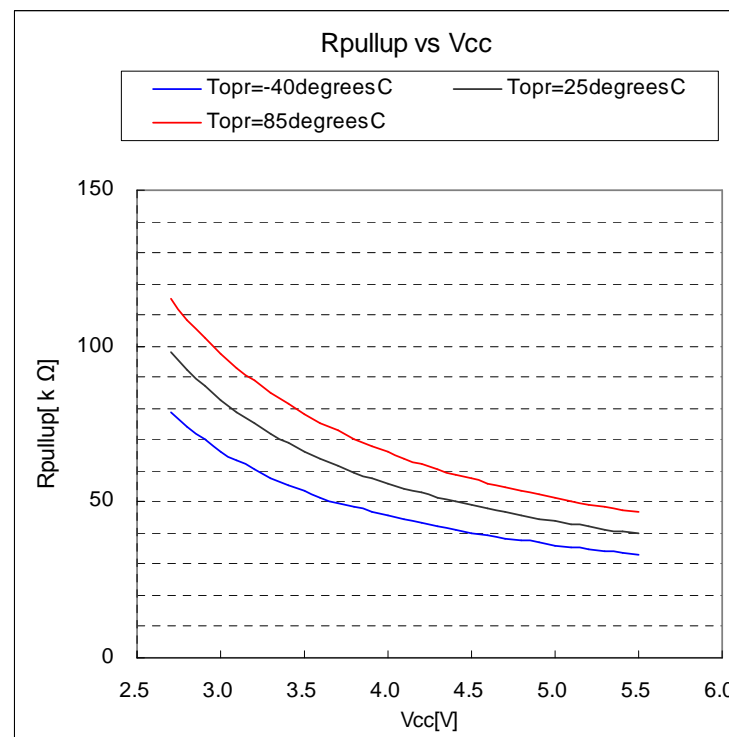


Figure14. R_{pullup} vs V_{cc}

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

5.A/D Accuracy(1)

■ Related Pin

AN0-AN7、 AN0_0-AN0_7、 AN2_0-AN2_7、 ANEX0、 ANEX1

■ Operating Condition

Topr=-40,25,85 (degrees C)

Vcc=AVcc=VREF =5.12V

φAD=24MHz

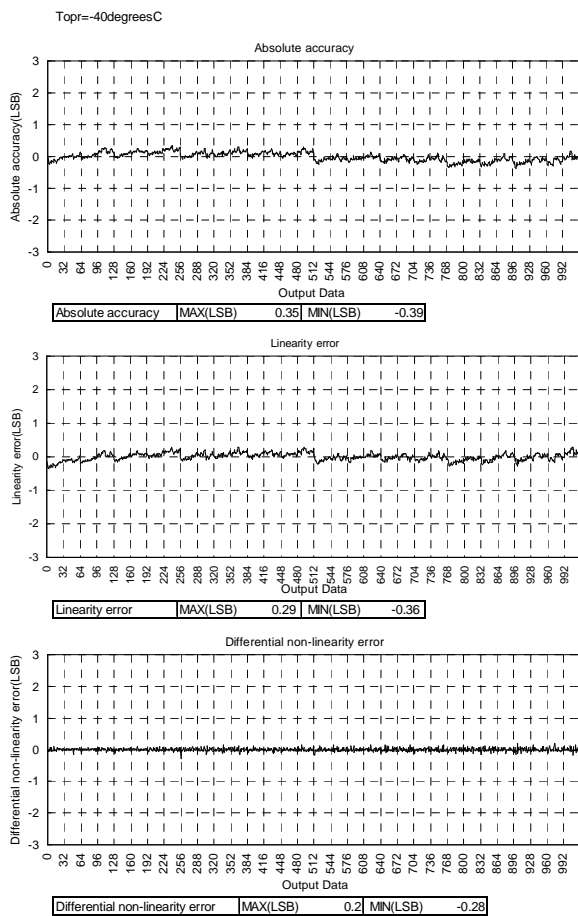


Figure15. Topr=-40(degrees C)

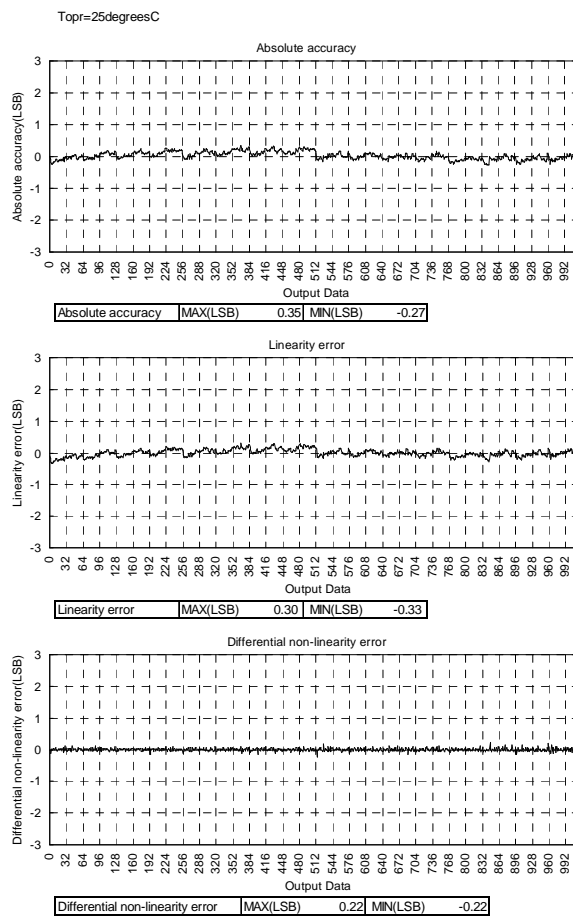


Figure16. Topr=25(degrees C)

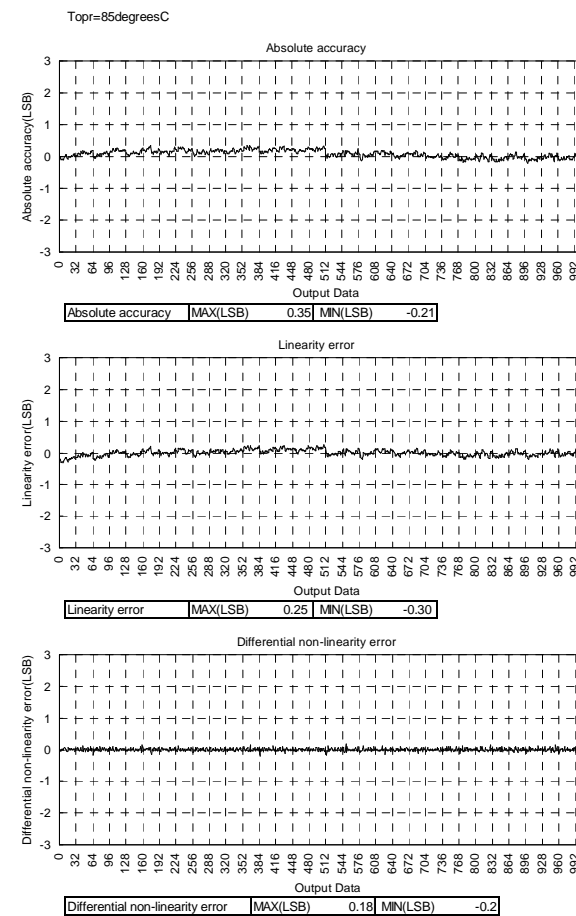


Figure17. Topr=85(degrees C)

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

5.A/D Accuracy(2)

■ Related Pin

AN0-AN7、 AN0_0-AN0_7、 AN2_0-AN2_7、 ANEX0、 ANEX1

■ Operating Condition

Topr=-40,25,85 (degrees C)

Vcc=AVcc=VREF =3.30V

φAD=16MHz

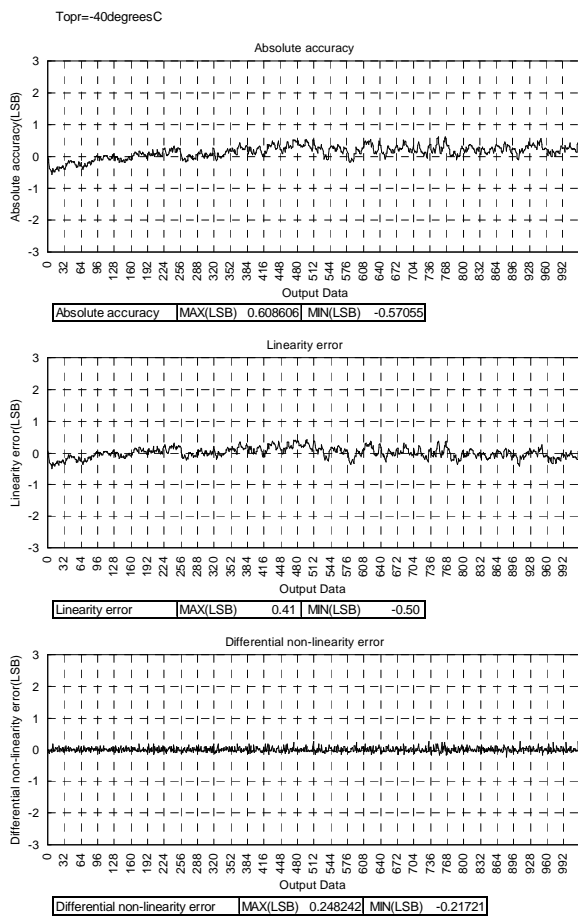


Figure18. Topr=-40(degrees C)

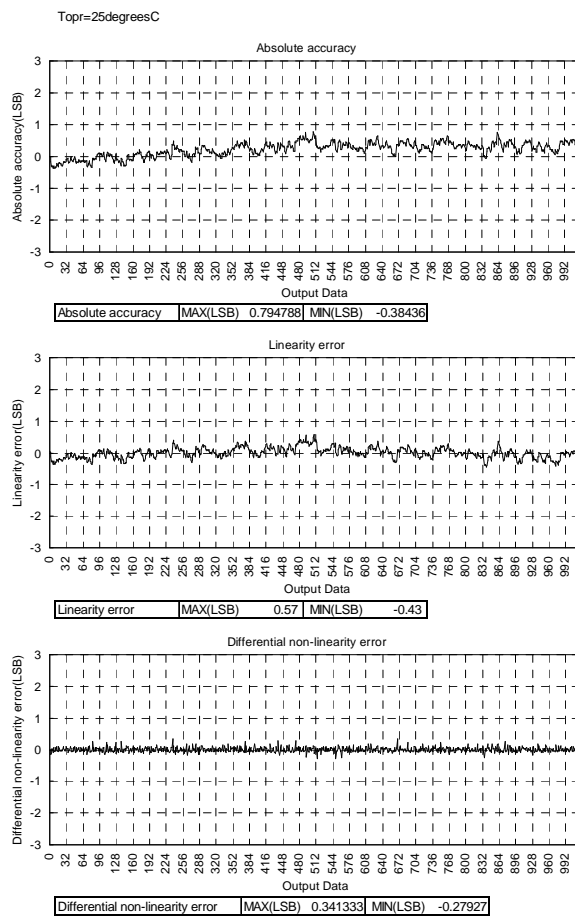


Figure19. Topr=25(degrees C)

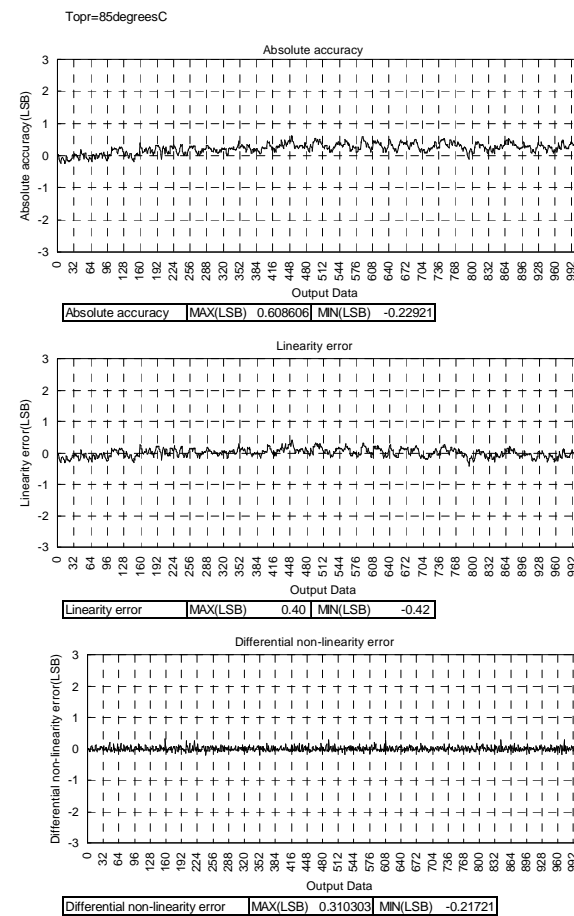


Figure20. Topr=85(degrees C)

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

5.A/D Accuracy(3)

■ Related Pin

AN0-AN7、 AN0_0-AN0_7、 AN2_0-AN2_7、 ANEX0、 ANEX1

■ Operating Condition

Topr=-40,25,85 (degrees C)

Vcc=AVcc=VREF =3.072V

φAD=10MHz

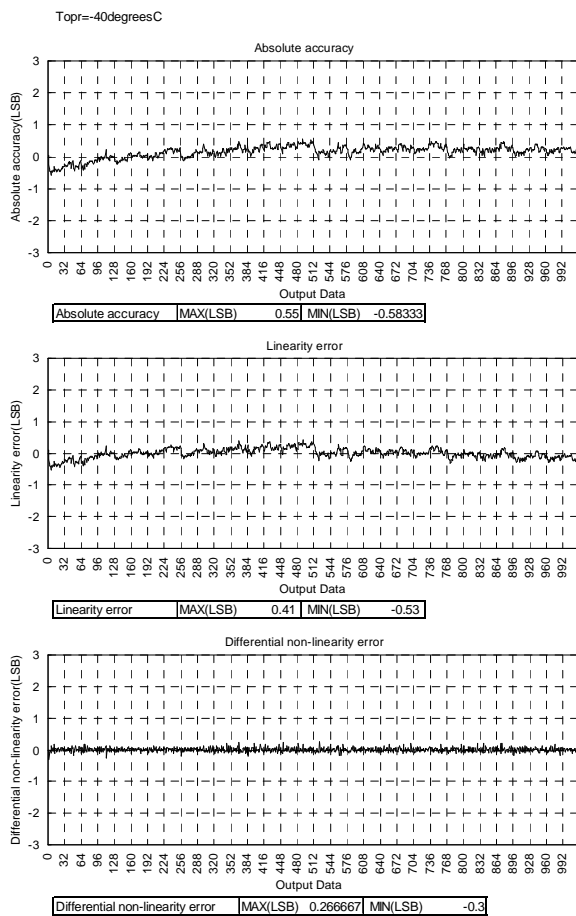


Figure21. Topr=-40(degrees C)

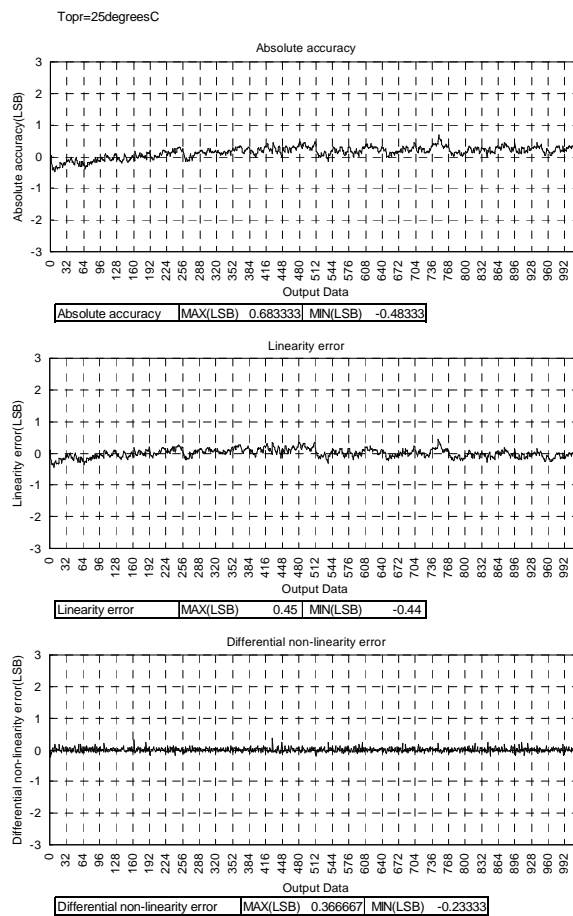


Figure22. Topr=25(degrees C)

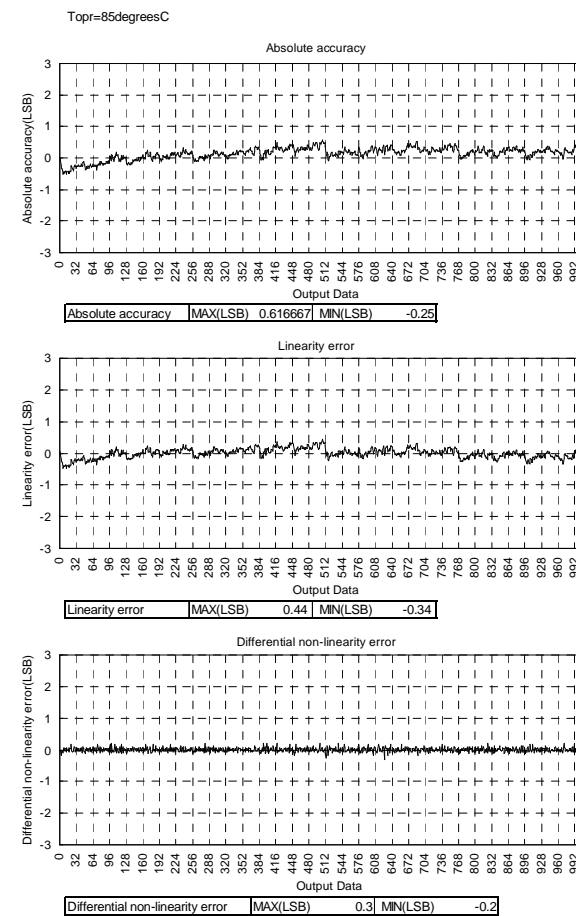


Figure23. Topr=85(degrees C)

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

5.A/D Accuracy(4)

■ Related Pin

AN0-AN7、 AN0_0-AN0_7、 AN2_0-AN2_7、 ANEX0、 ANEX1

■ Operating Condition

Topr=-40,25,85 (degrees C)

Vcc=AVcc=VREF =5.12V

φAD=20MHz (FOCO40M divided by 2)

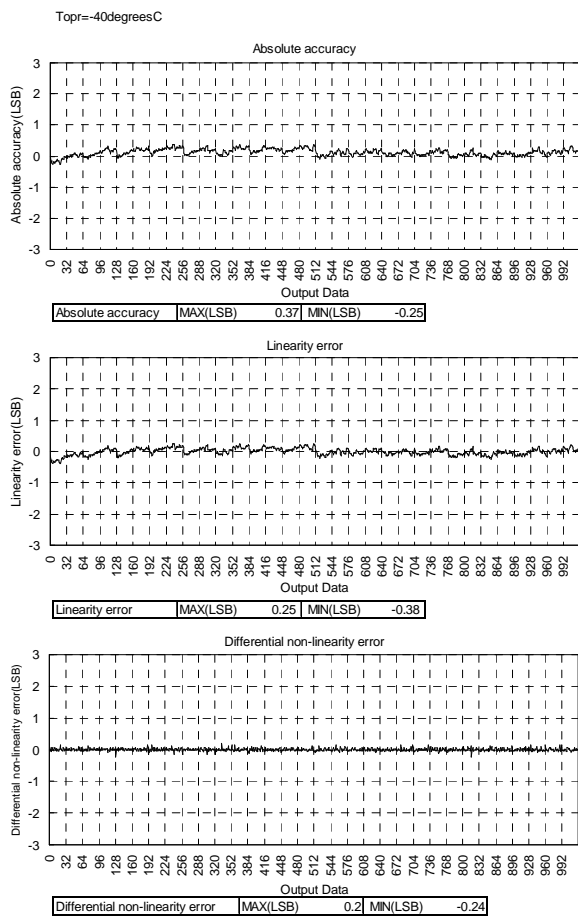


Figure24. Topr=-40(degrees C)

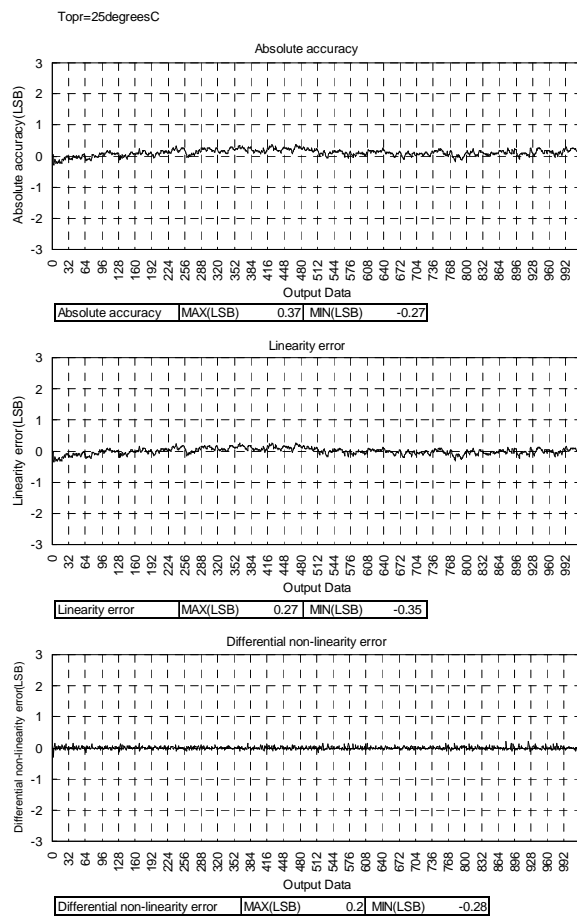


Figure25. Topr=25(degrees C)

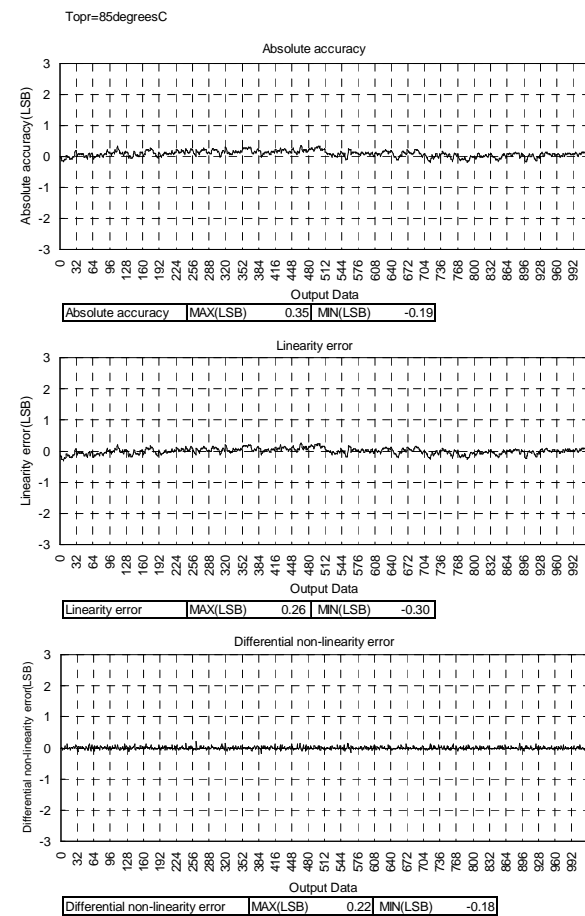


Figure26. Topr=85(degrees C)

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

5.A/D Accuracy(5)

■ Related Pin

AN0-AN7、 AN0_0-AN0_7、 AN2_0-AN2_7、 ANEX0、 ANEX1

■ Operating Condition

Topr=-40,25,85 (degrees C)

Vcc=AVcc=VREF =3.3V

φAD=13.3MHz (FOCO40M divided by 3)

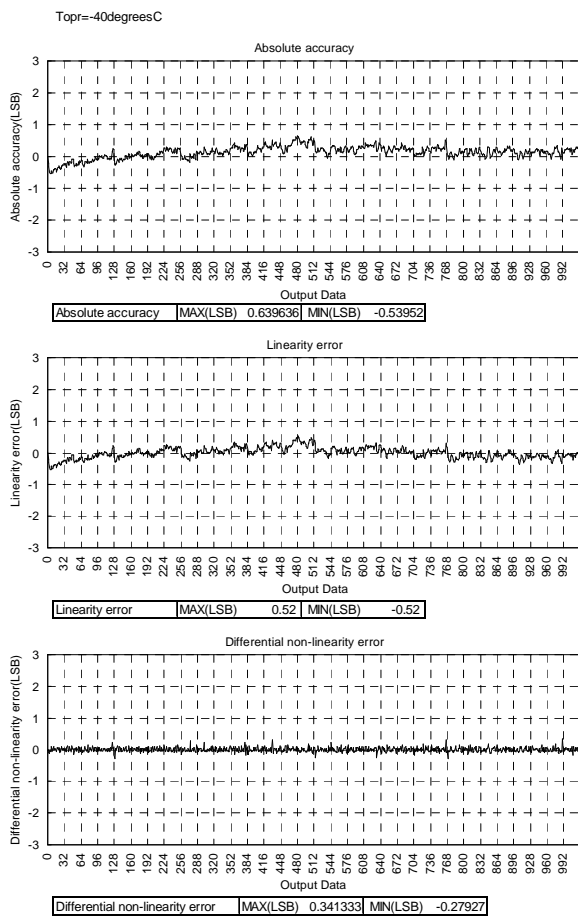


Figure27. Topr=-40(degrees C)

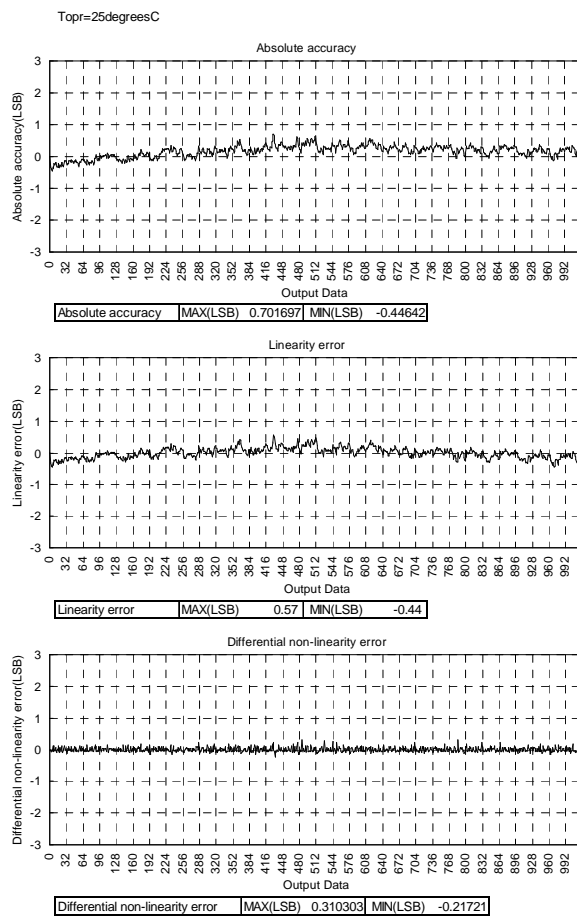


Figure28. Topr=25(degrees C)

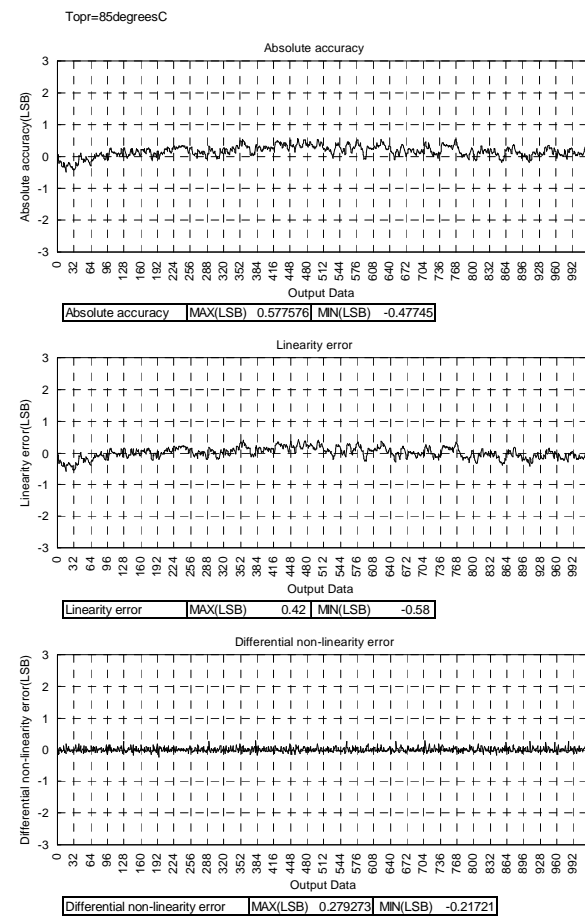


Figure29. Topr=85(degrees C)

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

5.A/D Accuracy(6)

■ Related Pin

AN0-AN7、 AN0_0-AN0_7、 AN2_0-AN2_7、 ANEX0、 ANEX1

■ Operating Condition

Topr=-40,25,85 (degrees C)

Vcc=AVcc=VREF =3.072V

φAD=10MHz (FOCO40M divided by 4)

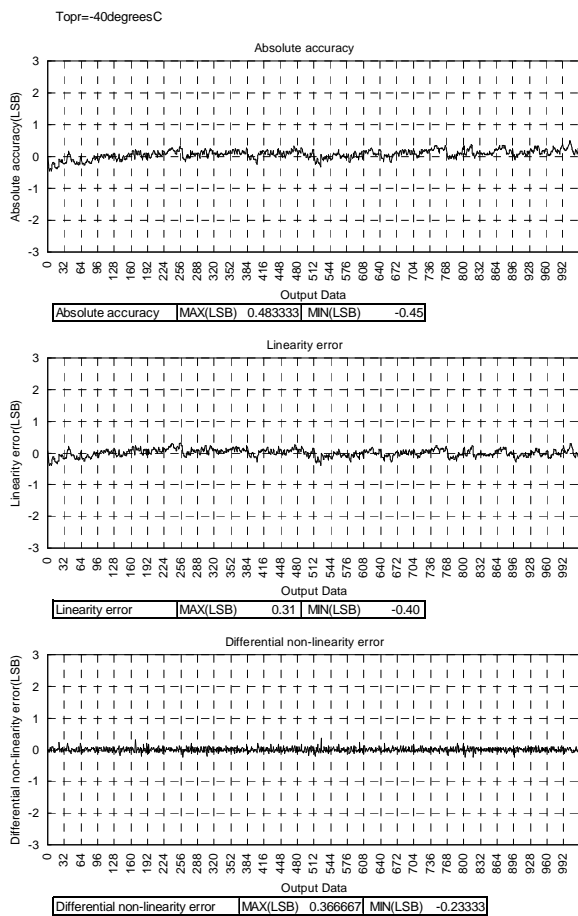


Figure30. Topr=-40(degrees C)

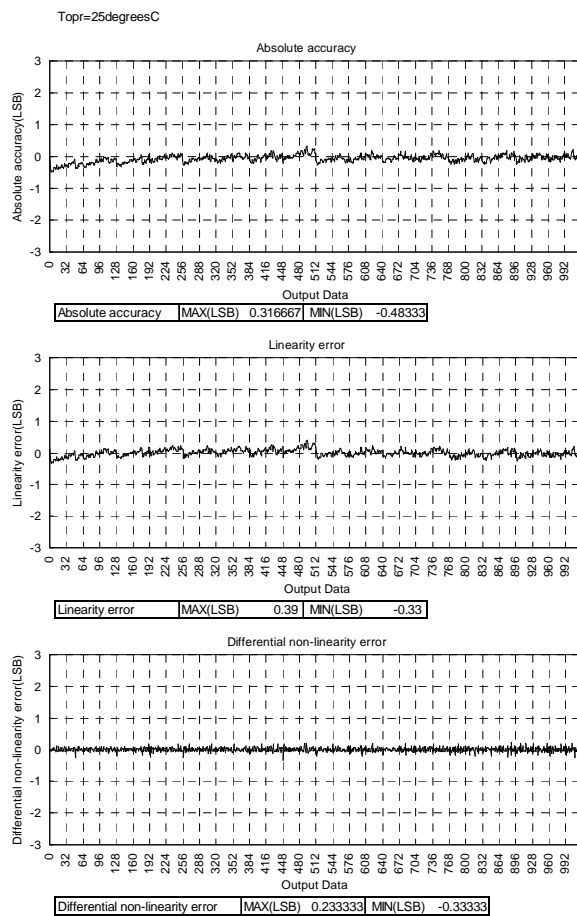


Figure31. Topr=25(degrees C)

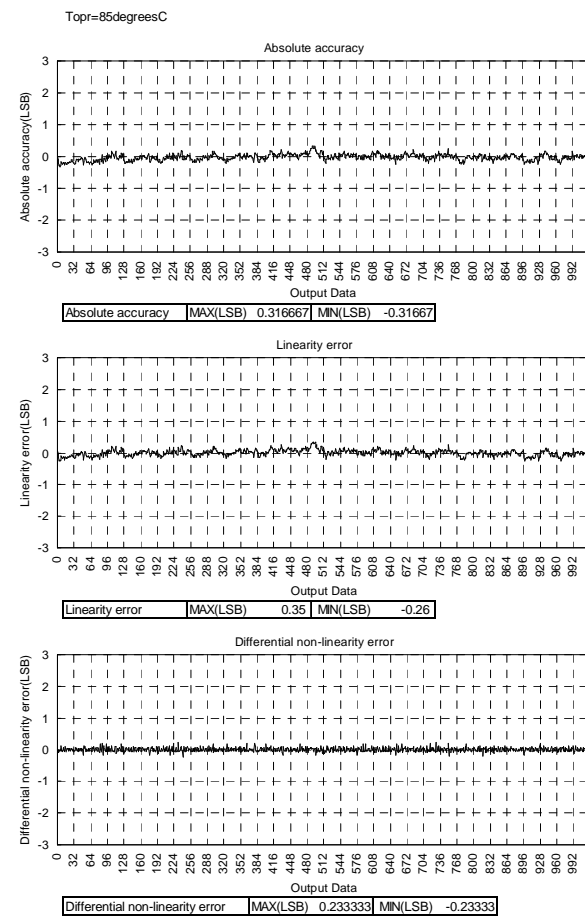


Figure32. Topr=85(degrees C)

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

6.D/A Accuracy(1)

- Related Pin
DA0, DA1

- Operating Condition
Topr=-40,25,85 (degrees C)
PLL CLOCK : 24MHz
Vcc=5.12V

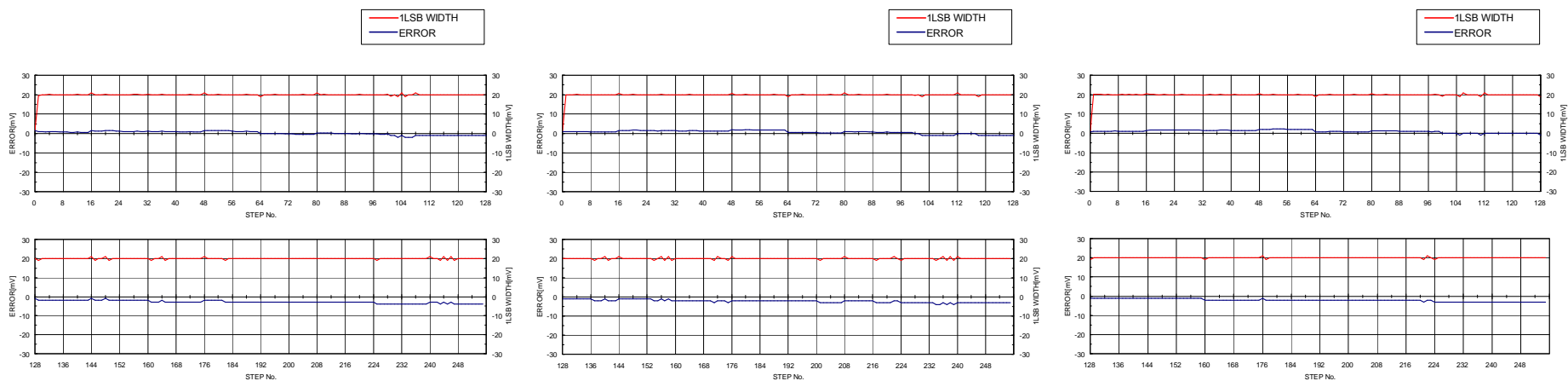


Figure33. Topr=-40(degrees C)

Figure34. Topr=25(degrees C)

Figure35. Topr=85(degrees C)

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

6.D/A Accuracy(2)

- Related Pin
DA0, DA1

- Operating Condition
Topr=-40,25,85 (degrees C)
XIN CLOCK : 10MHz
Vcc=3.072V

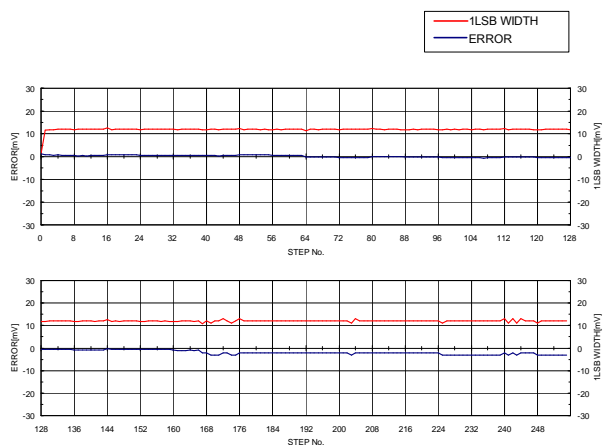


Figure36. Topr=-40(degrees C)

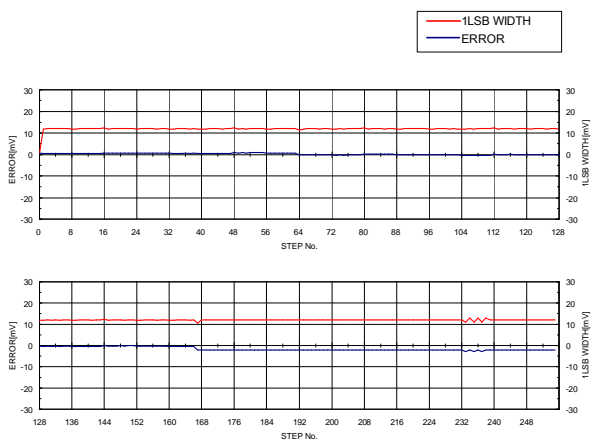


Figure37. Topr=25(degrees C)

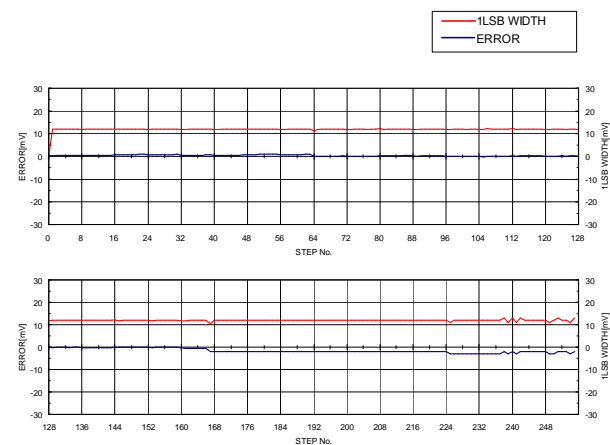


Figure38. Topr=85(degrees C)

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

7. Power supply current(1)

(1) High-speed mode $f(XIN)$

1. I_{CC} vs $f(XIN)$

■ Operating Condition

$XIN = 4$ to 16 MHz (square wave)

125 kHz on-chip oscillator stop

$T_{opr} = 25$ (degrees C)

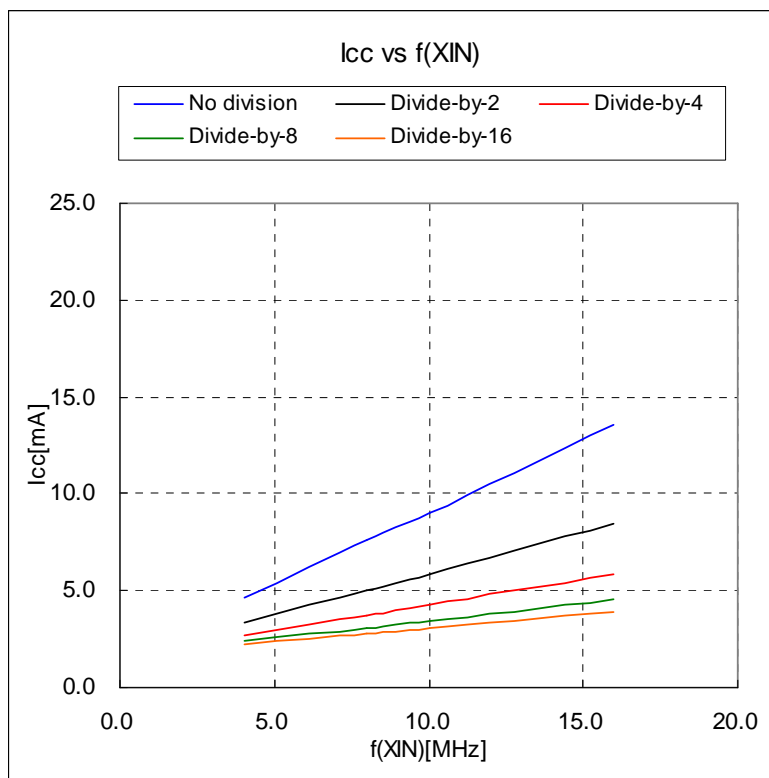


Figure39. I_{CC} vs $f(XIN)$ ($V_{CC}=3.0V$)

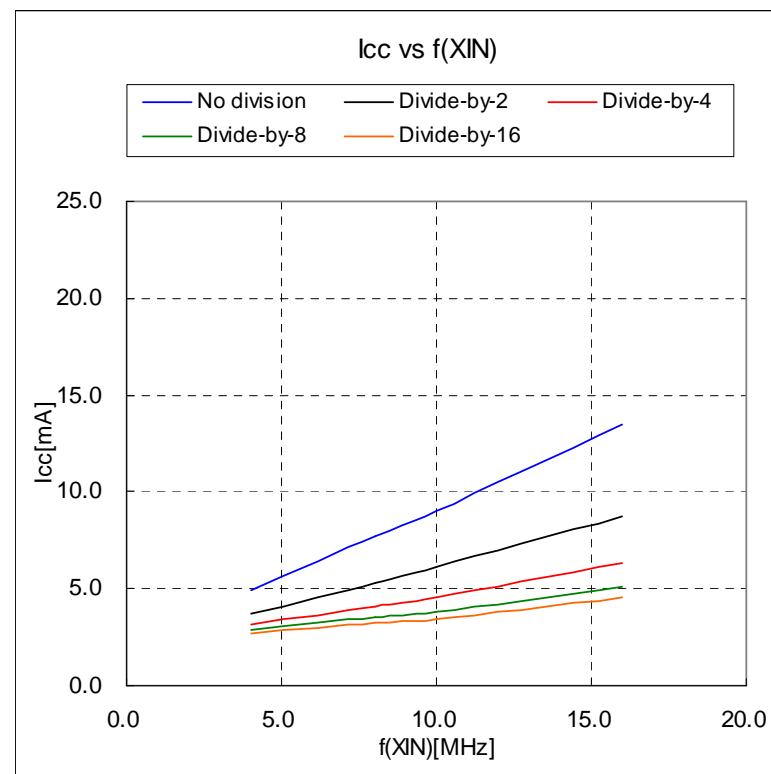


Figure40. I_{CC} vs $f(XIN)$ ($V_{CC}=5.0V$)

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

7. Power supply current(2)

(1) High-speed mode f(Xin)

2. I_{cc} vs V_{cc}

■ Operating Condition

XIN = 10 MHz (square wave)
125 kHz on-chip oscillator stop
Topr = -40, 25, 85 (degrees C)
No division

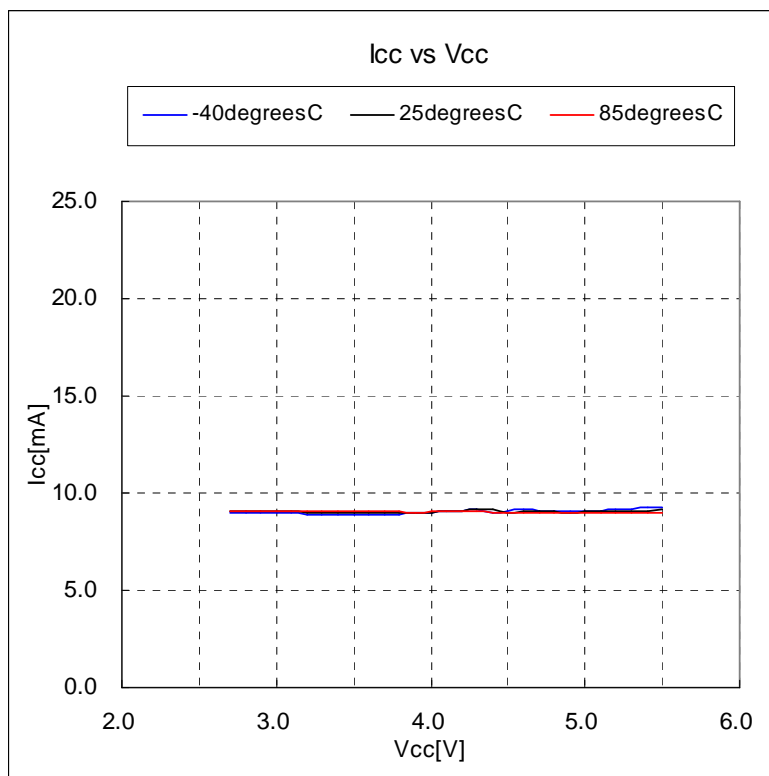


Figure41. I_{cc} vs V_{cc} (f(Xin)=10MHz)

■ Operating Condition

XIN = 16 MHz (square wave)
125 kHz on-chip oscillator stop
Topr = -40, 25, 85 (degrees C)
No division

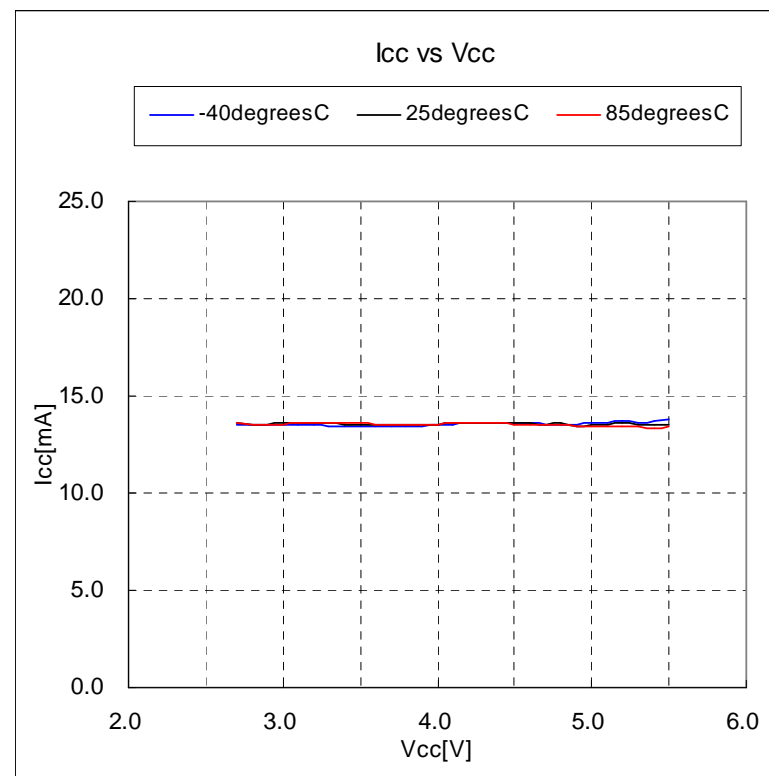


Figure42. I_{cc} vs V_{cc} (f(Xin)=16MHz)

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

7. Power supply current(3)

(1) High-speedmode f(Xin)

3. Icc vs Topr

■ Operating Condition

XIN = 4, 8, 10, 16 MHz (square wave)

125 kHz on-chip oscillator stop

Topr = -40 to 85(degrees C)

No division

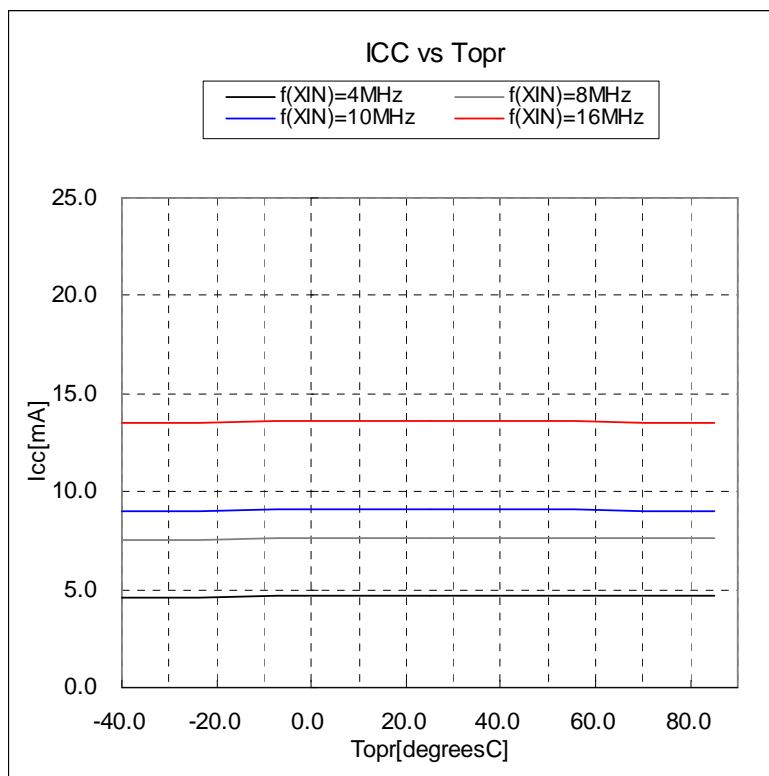


Figure43. Icc vs Topr (Vcc=3.0V)

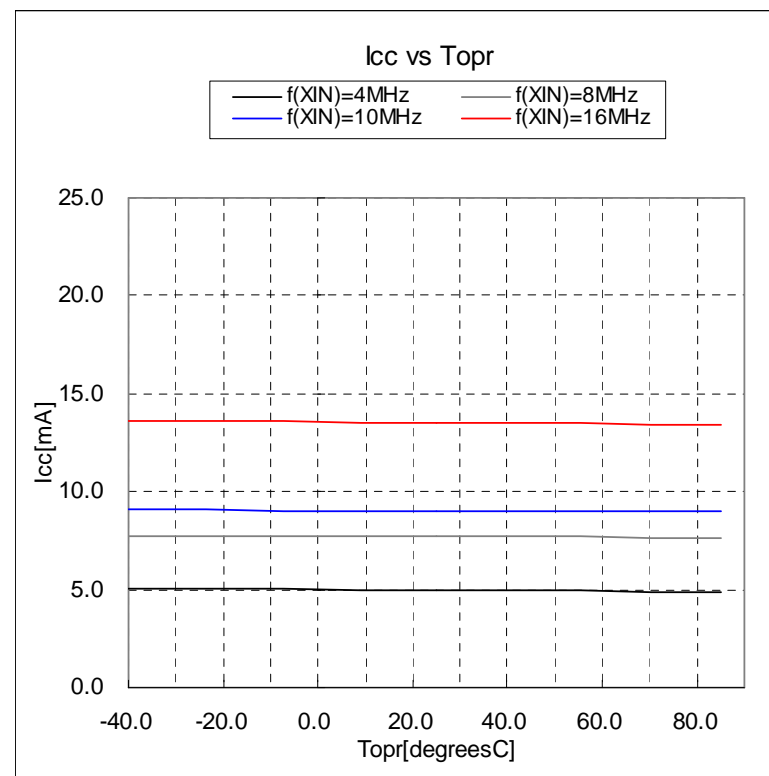


Figure44. Icc vs Topr (Vcc=5.0V)

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

7. Power supply current(4)

(2) High-speed mode f(PLL)

1. I_{CC} vs f(PLL)

■ Operating Condition

f(PLL) = 10 to 32 MHz (square wave)

125 kHz on-chip oscillator stop

Topr = 25(degrees C)

No division

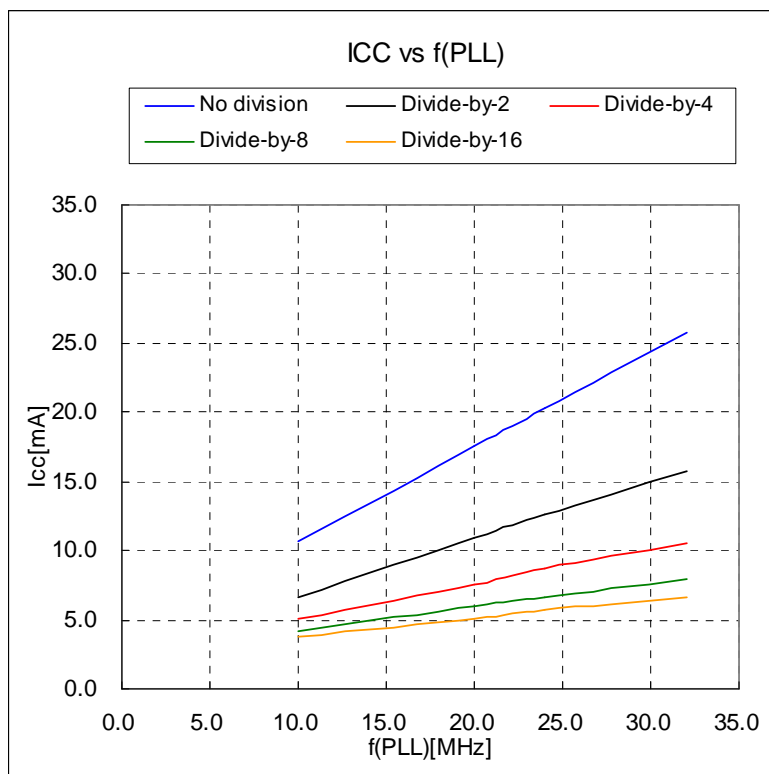


Figure45. I_{CC} vs f(PLL) (V_{CC}=3.0V)

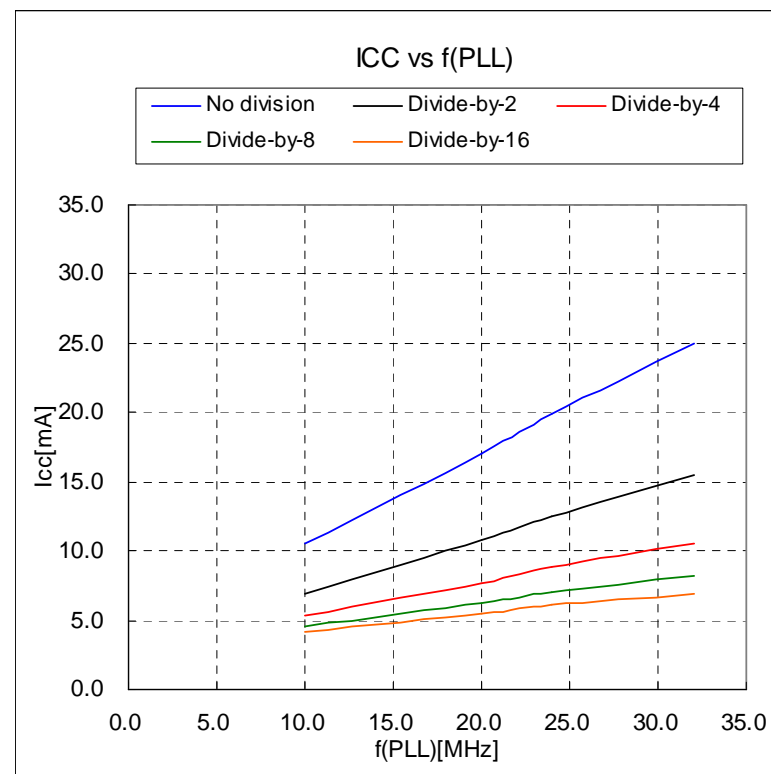


Figure46. I_{CC} vs f(PLL) (V_{CC}=5.0V)

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

7. Power supply current(5)

(2) High-speed mode f(PLL)

2. Icc vs Vcc

■ Operating Condition

f(PLL) = 20MHz

XIN = 5 MHz (square wave)

PLL multiplied by 4

125 kHz on-chip oscillator stop

Topr = -40,25,85(degrees C)

No division

■ Operating Condition

f(PLL) = 32MHz

XIN = 4 MHz (square wave)

PLL multiplied by 8

125 kHz on-chip oscillator stop

Topr = -40,25,85(degrees C)

No division

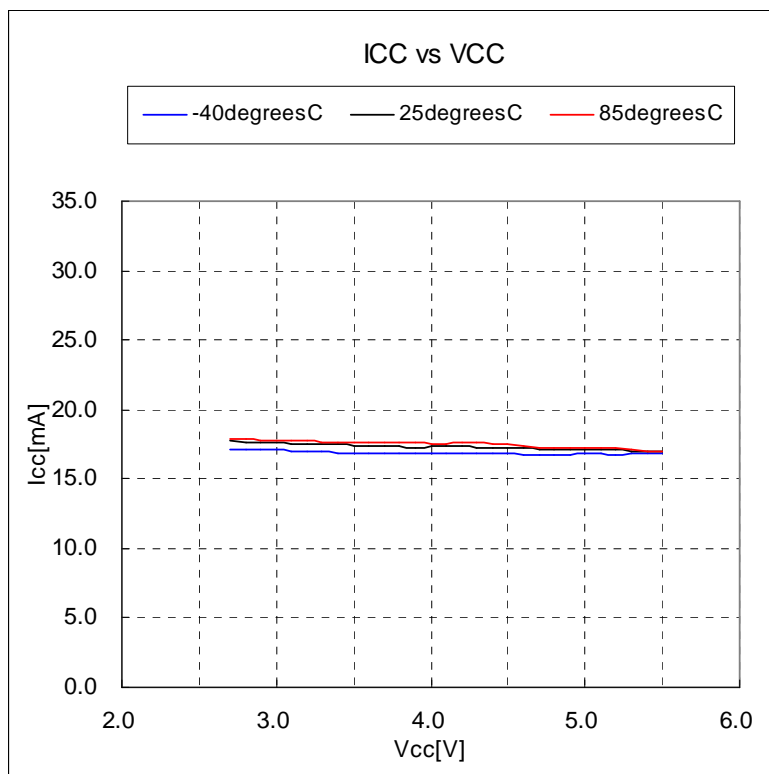


Figure47. Icc vs Vcc (f(PLL)=20MHz)

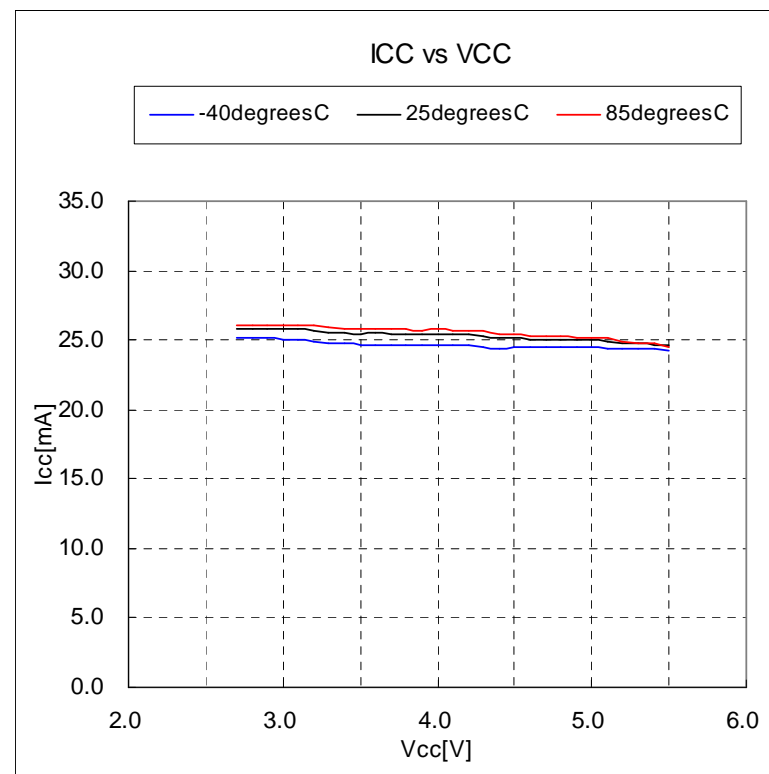


Figure48. Icc vs Vcc (f(PLL)=32MHz)

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

7. Power supply current(6)

(2) High-speed mode f(PLL)

3. Icc vs Topr

■ Operating Condition

f(PLL) = 20, 24, 32 MHz (square wave)

125 kHz on-chip oscillator stop

Topr = -40 to 85 (degrees C)

No division

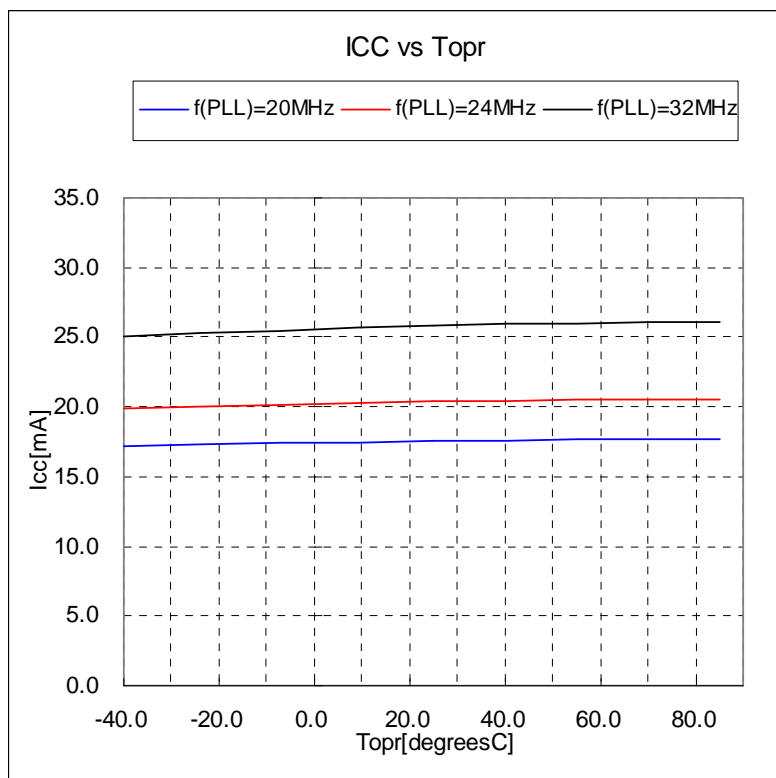


Figure49. Icc vs Topr (Vcc=3.0V)

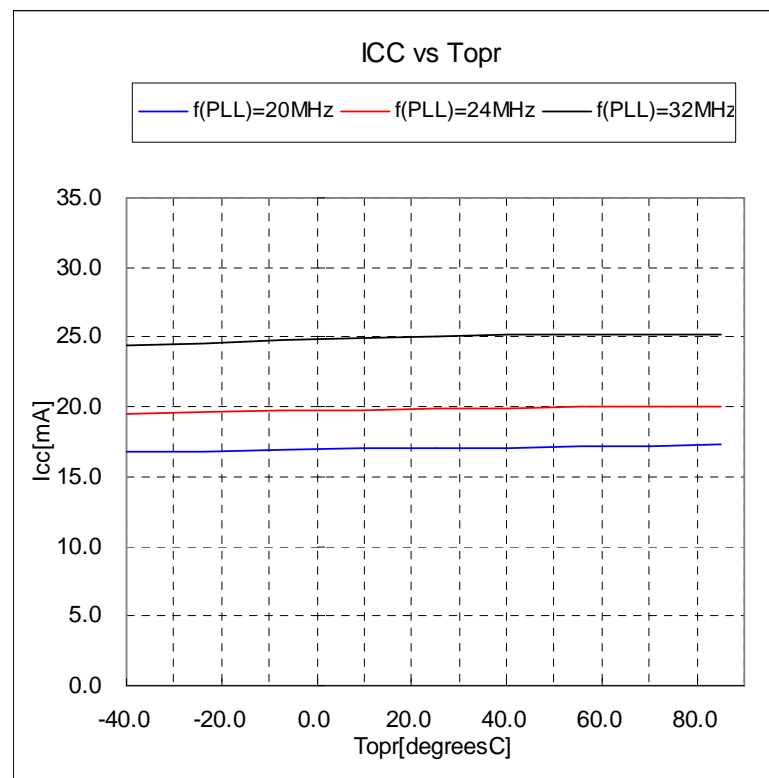


Figure50. Icc vs Topr (Vcc=5.0V)

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

7. Power supply current(7)

(3) 40MHz on-chip Oscillator mode

1. Icc vs Vcc

■ Operating Condition

40MHz on-chip oscillator on

f(BCLK)=5,10MHz

Main clock stop

125kHz on-chip oscillator stop

Topr = 25(degrees C)

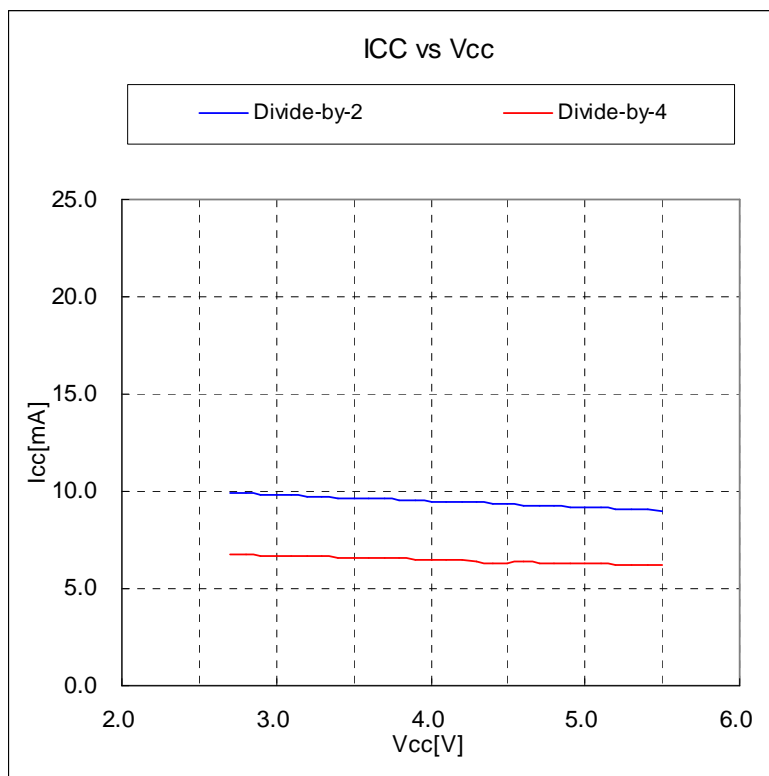


Figure51. Icc vs Vcc

2. Icc vs Topr

■ Operating Condition

40MHz on-chip oscillator on

Divide-by-2, f(BCLK)=10MHz

Main clock stop

125kHz on-chip oscillator stop

Topr = -40 to 85(degrees C)

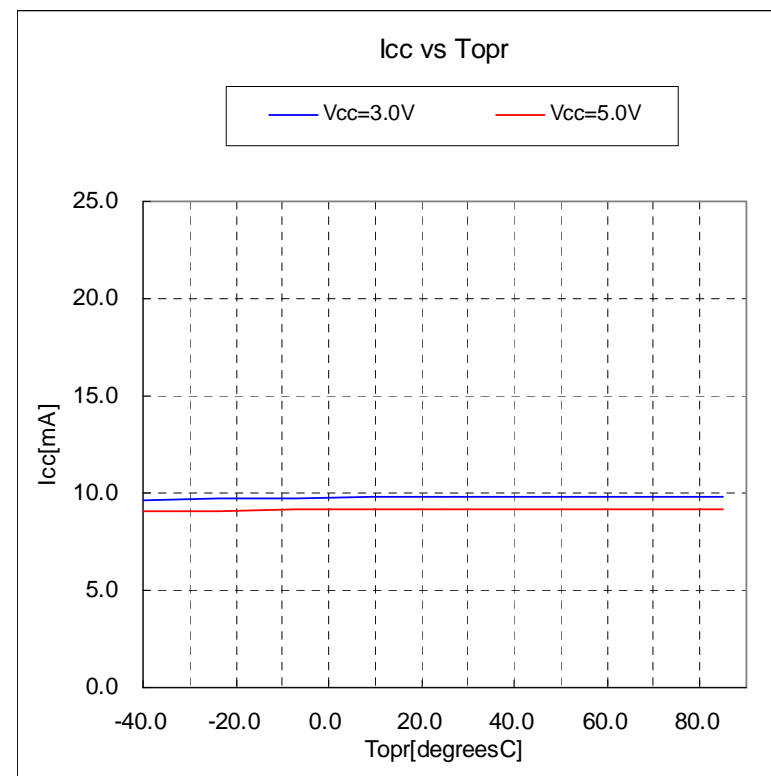


Figure52. Icc vs Topr

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

7. Power supply current(8)

(4) 125kHz on-chip Oscillator mode

1. Icc vs Vcc

■ Operating Condition

Main clock stop

125kHz on-chip oscillator on, no division

Topr = 25(degrees C)

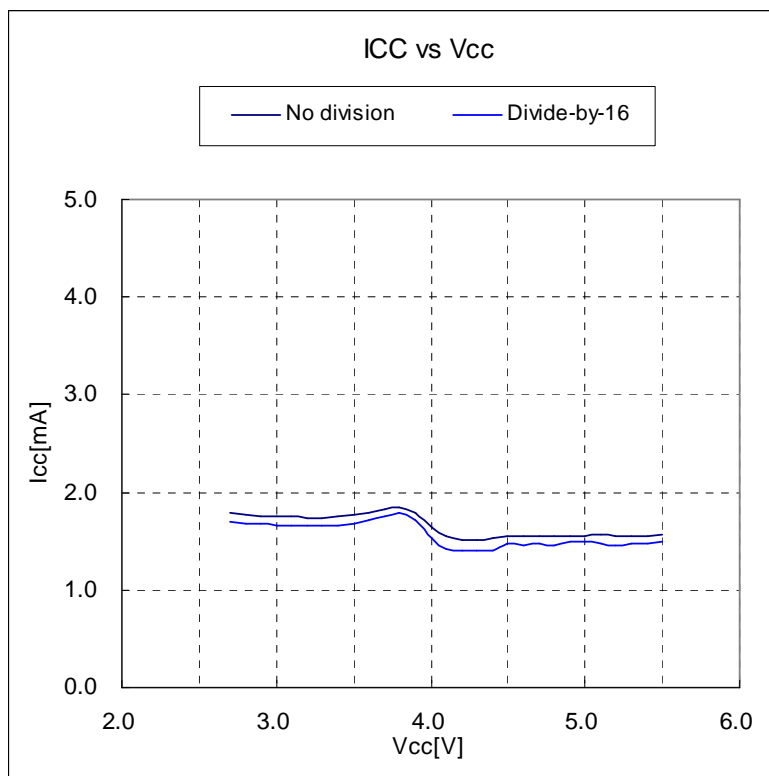


Figure53. Icc vs Vcc

■ Operating Condition

Main clock stop

125kHz on-chip oscillator on, no division

FMR22=1

Topr = 25(degrees C)

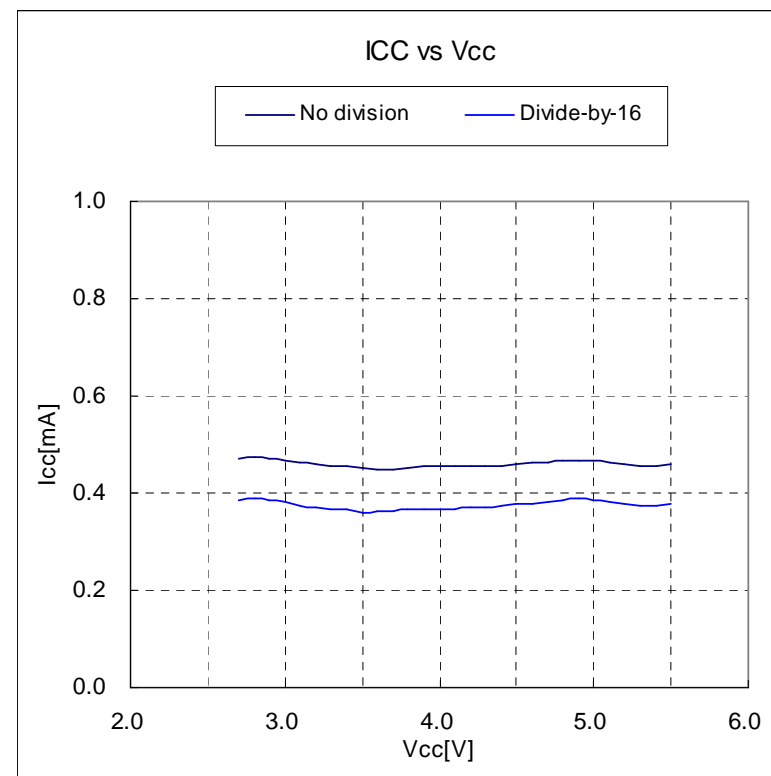


Figure54. Icc vs Vcc

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

7. Power supply current(9)

(4) 125kHz on-chip Oscillator mode

2. Icc vs Topr

■ Operating Condition

Main clock stop

125kHz on-chip oscillator on, no division

Topr = -40 to 85(degrees C)

■ Operating Condition

Main clock stop

125kHz on-chip oscillator on, no division

FMR22=1

Topr = -40 to 85(degrees C)

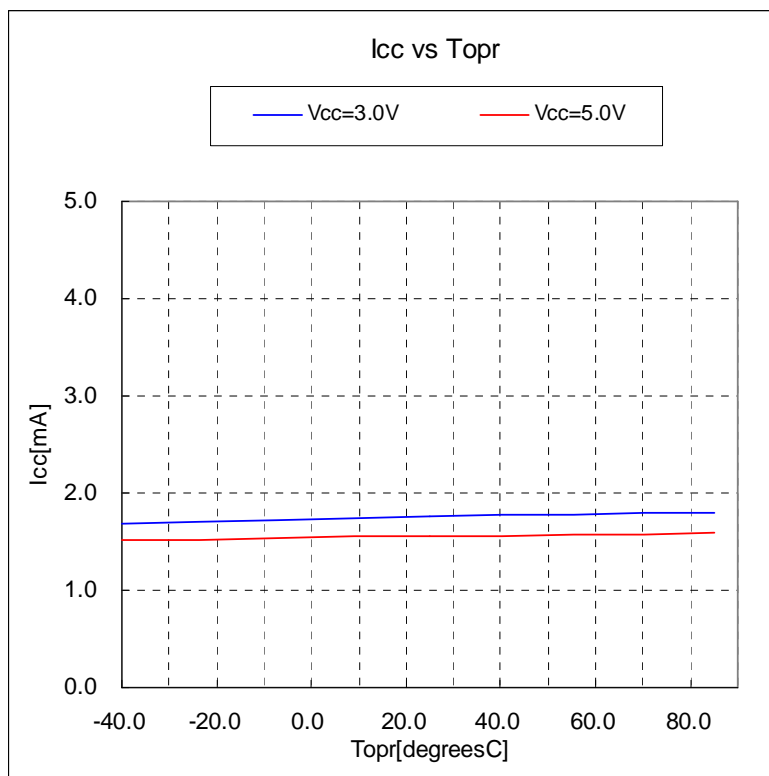


Figure55. Icc vs Topr

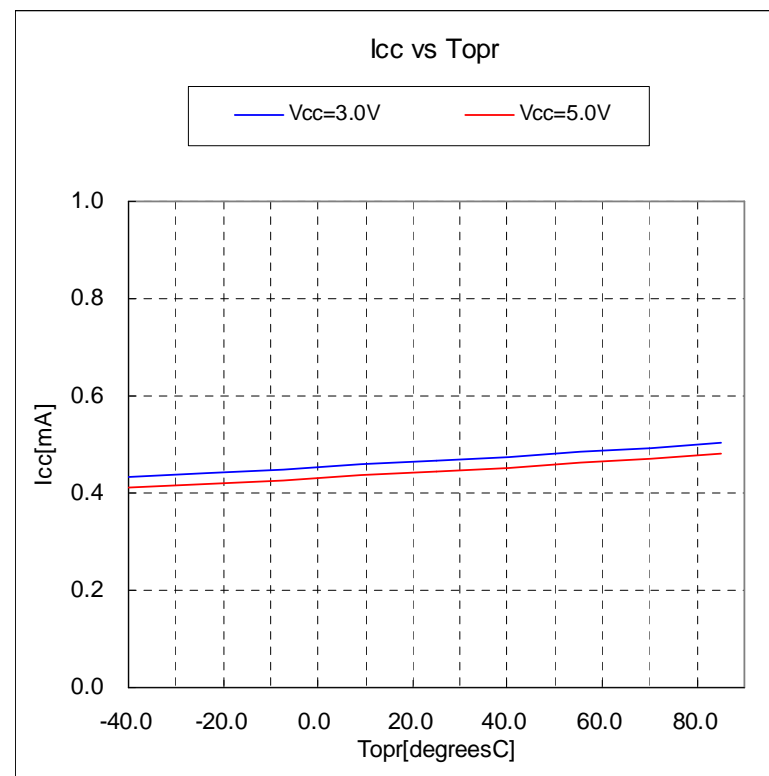


Figure56. Icc vs Topr

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

7. Power supply current(10)

(5) Low-Power mode f(Xcin)

1. I_{cc} vs V_{cc}

■ Operating Condition

f(Xcin) = 32kHz

In low-power mode

FMR22 = FMR23 = 1

on flash memory

Topr = -40,25,85(degrees C)

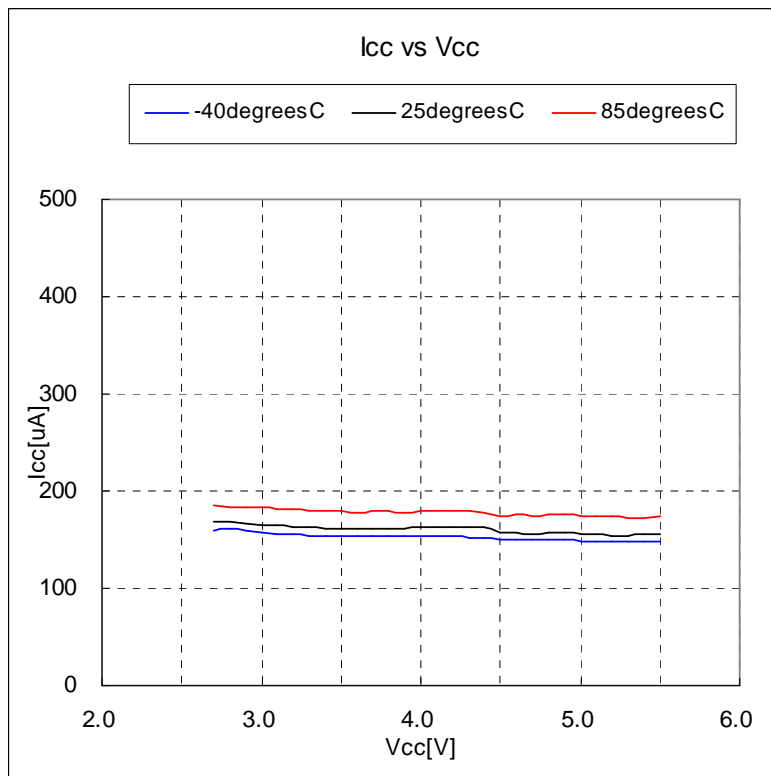


Figure57. I_{cc} vs V_{cc}

■ Operating Condition

f(Xcin) = 32kHz

In low-power mode

FMR22 = FMR23 = 1

on RAM

Topr = -40,25,85(degrees C)

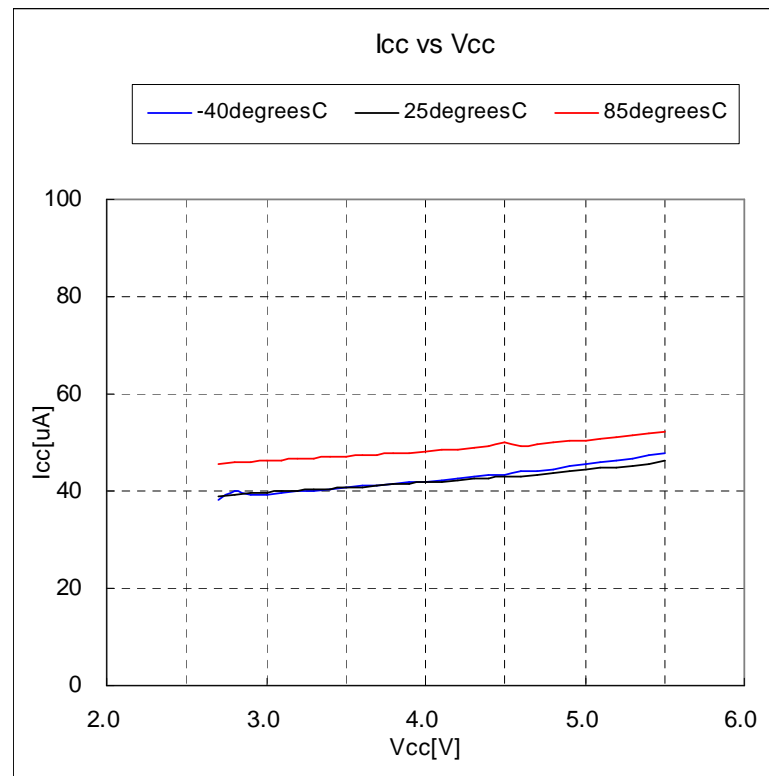


Figure58. I_{cc} vs V_{cc}

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

7. Power supply current(11)

(5) Low-Power mode f(Xcin)

2. Icc vs Topr

■ Operating Condition

f(Xcin) = 32kHz

In low-power mode,

FMR22 = FMR23 = 1

on flash memory

Topr = -40 to 85(degrees C)

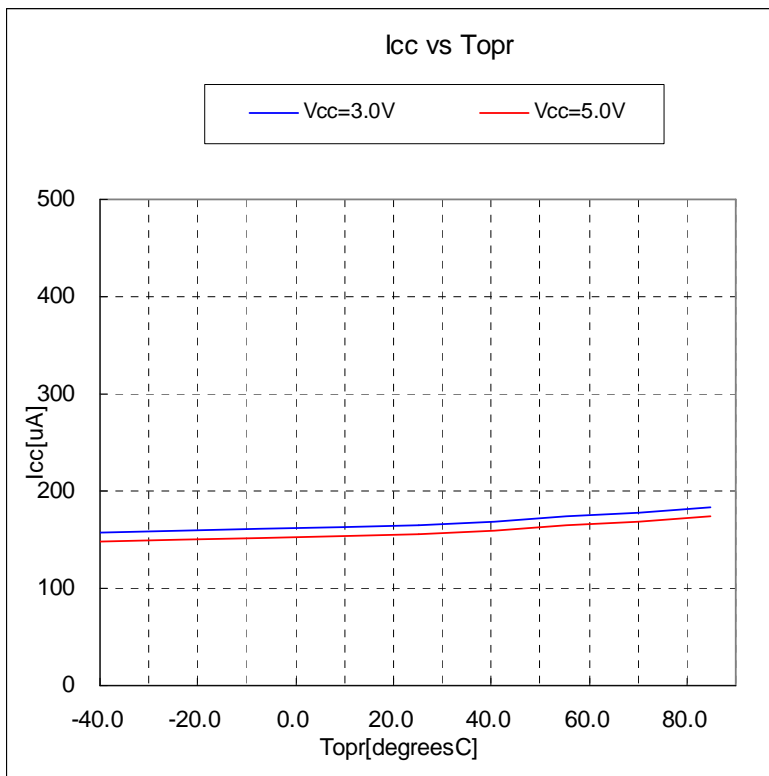


Figure59. Icc vs Topr

■ Operating Condition

f(Xcin) = 32kHz

In low-power mode

FMSTP = 1

on RAM

Topr = -40 to 85(degrees C)

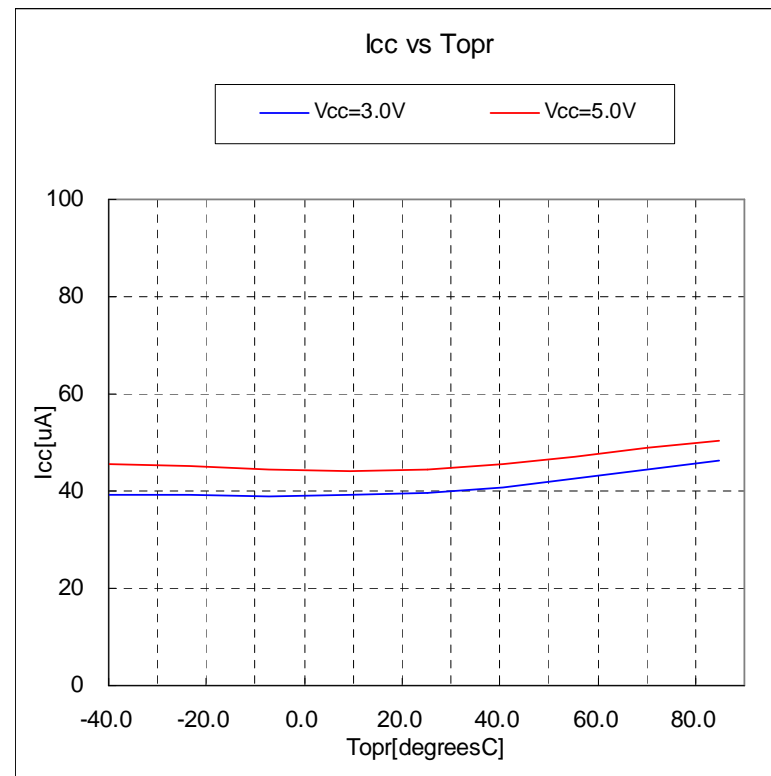


Figure60. Icc vs Topr

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

7. Power supply current(12)

(6) Wait mode f(Xcin)

1. I_{cc} vs V_{cc}

■ Operating Condition

f(Xcin) = 32kHz (oscillation capacity High)

40MHz on-chip oscillator stop

125kHz on-chip oscillator stop

Peripheral clock operation

Topr = -40, 25, 85(degrees C)

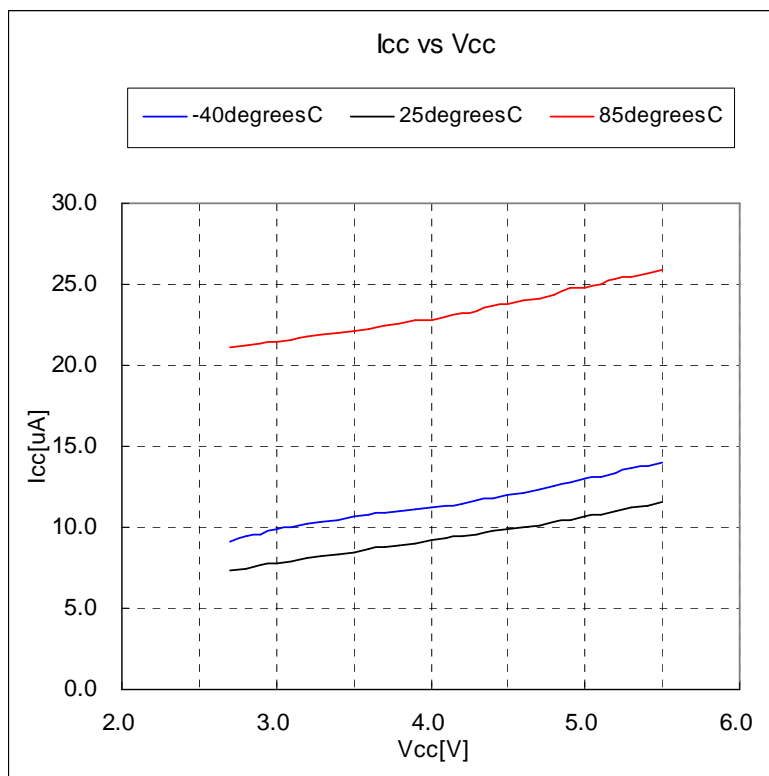


Figure61. I_{cc} vs V_{cc}

■ Operating Condition

f(Xcin) = 32kHz (oscillation capacity Low)

40MHz on-chip oscillator stop

125kHz on-chip oscillator stop

Peripheral clock operation

Topr = -40, 25, 85(degrees C)

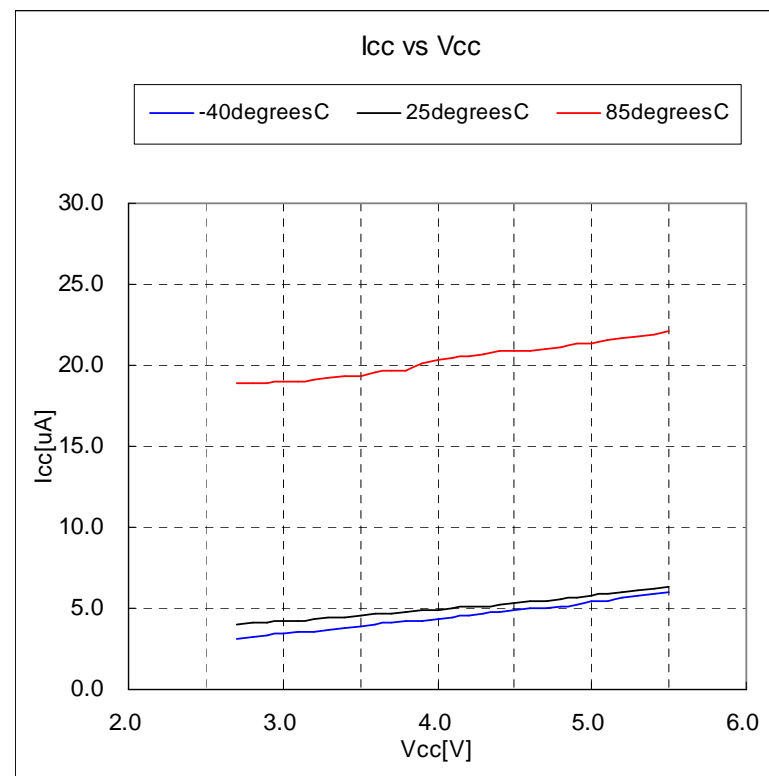


Figure62. I_{cc} vs V_{cc}

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

7. Power supply current(13)

(6) Wait mode f(Xcin)

2. Icc vs Topr

■ Operating Condition

f(Xcin) = 32kHz (oscillation capacity High)

40MHz on-chip oscillator stop

125kHz on-chip oscillator stop

Peripheral clock operation

Topr = -40 to 85(degrees C)

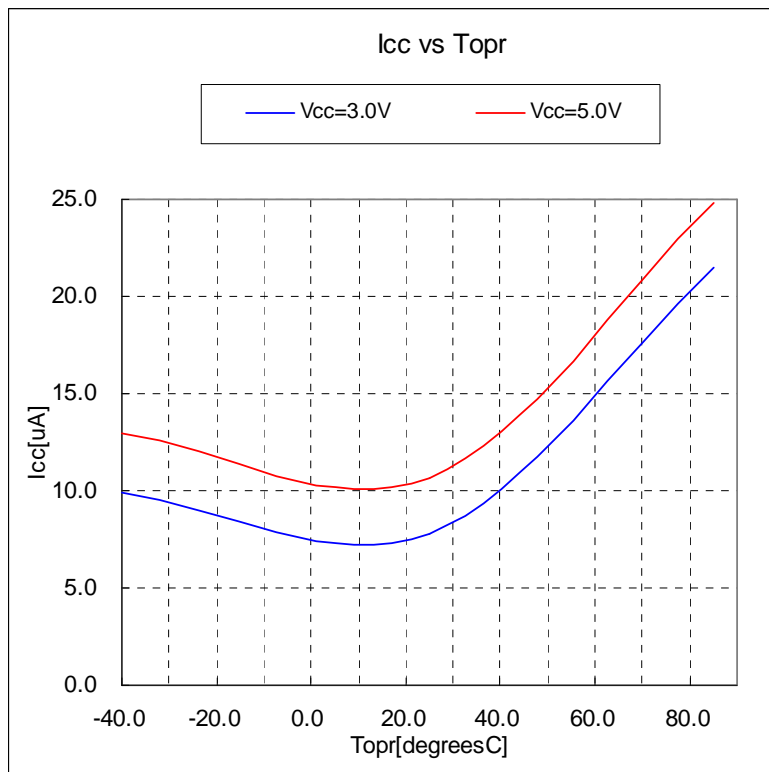


Figure63. Icc vs Topr

■ Operating Condition

f(Xcin) = 32kHz (oscillation capacity Low)

40MHz on-chip oscillator stop

125kHz on-chip oscillator stop

Peripheral clock operation

Topr = -40 to 85(degrees C)

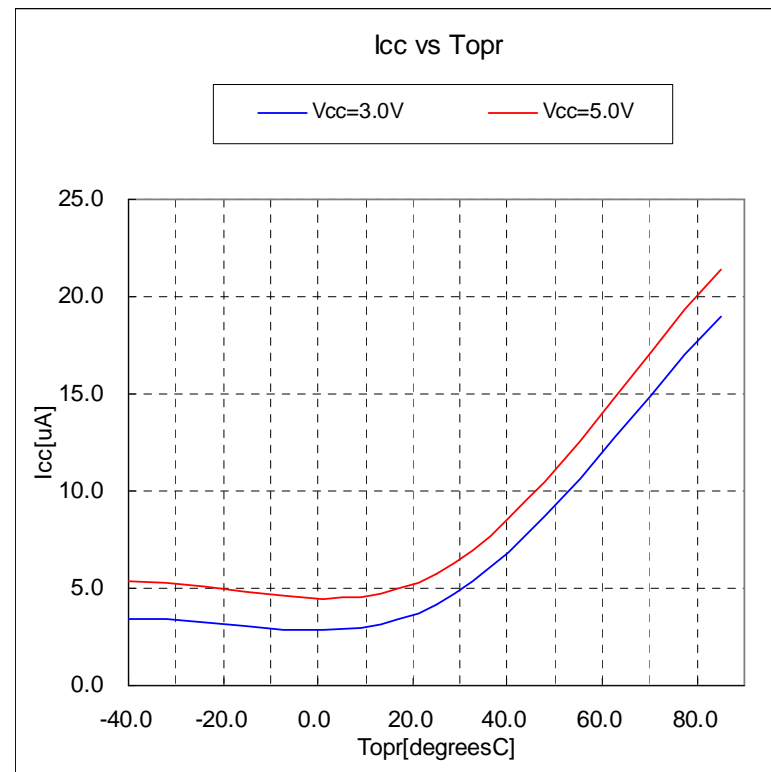


Figure64. Icc vs Topr

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

7. Power supply current(14)

(7) Wait mode f(OCO-S)

1. I_{cc} vs V_{cc}

■ Operating Condition

Main clock stop

40MHz on-chip oscillator stop

125kHz on-chip oscillator on

Peripheral clock operation

T_{opr} = -40,25,85(degrees C)

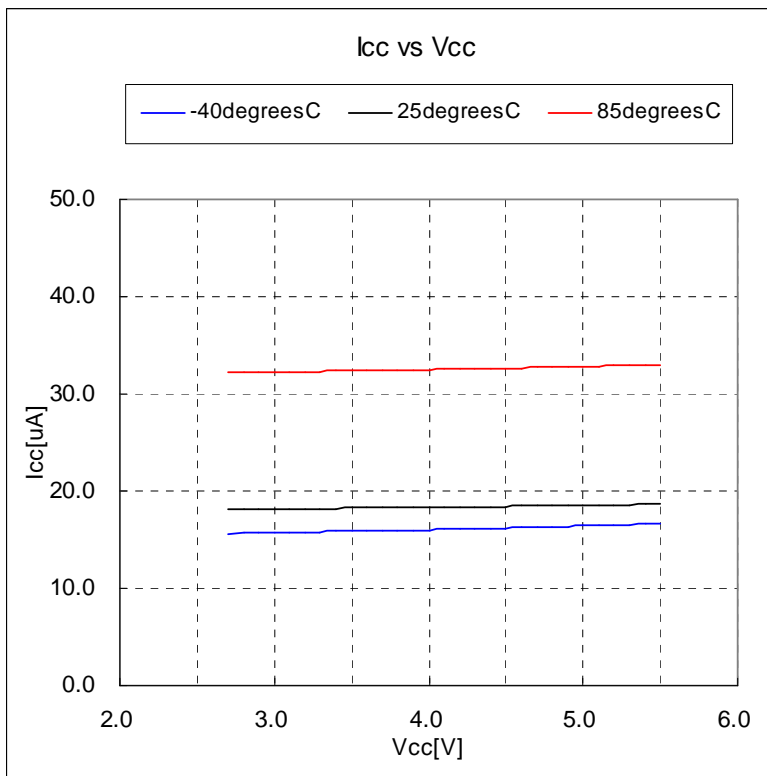


Figure65. I_{cc} vs V_{cc}

■ Operating Condition

Main clock stop

40MHz on-chip oscillator stop

125kHz on-chip oscillator on

Peripheral clock stop

T_{opr} = -40,25,85(degrees C)

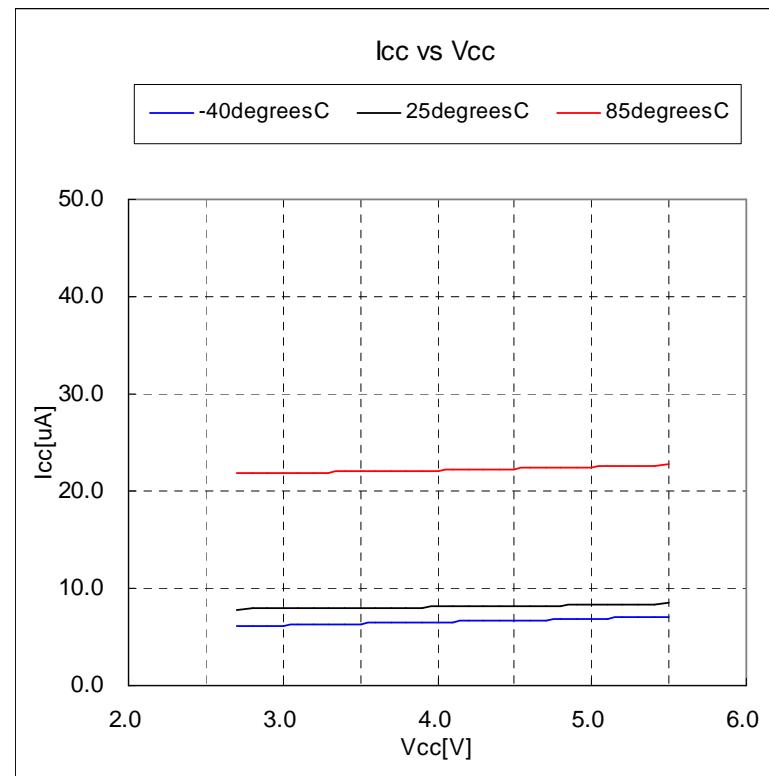


Figure66. I_{cc} vs V_{cc}

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

7. Power supply current(15)

(7) Wait mode f(OCO-S)

2. Icc vs Topr

■ Operating Condition

Main clock stop

40MHz on-chip oscillator stop

125kHz on-chip oscillator on

Peripheral clock operation

Topr = -40 to 85(degrees C)

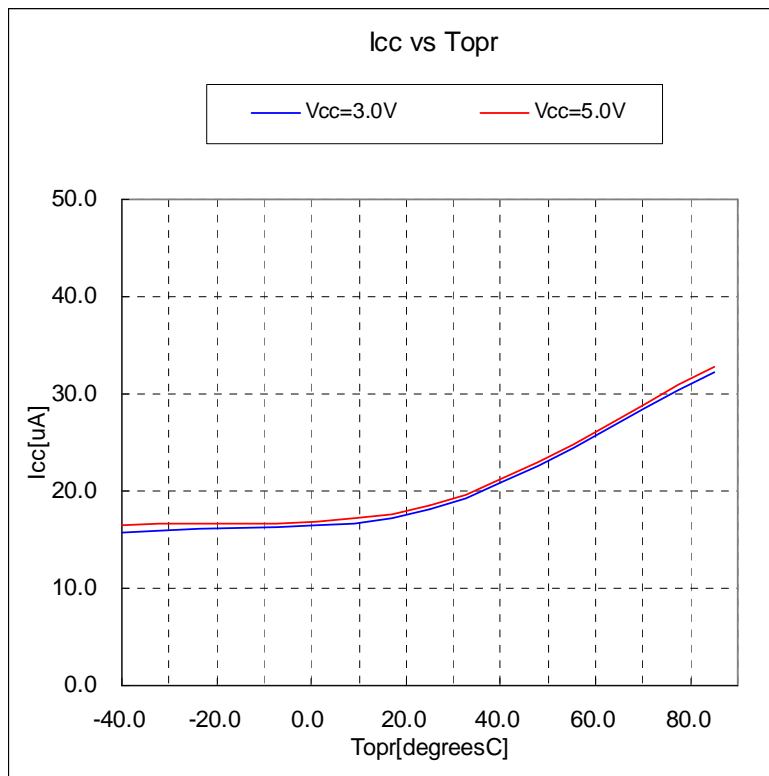


Figure67. Icc vs Topr

■ Operating Condition

Main clock stop

40MHz on-chip oscillator stop

125kHz on-chip oscillator on

Peripheral clock stop

Topr = -40 to 85(degrees C)

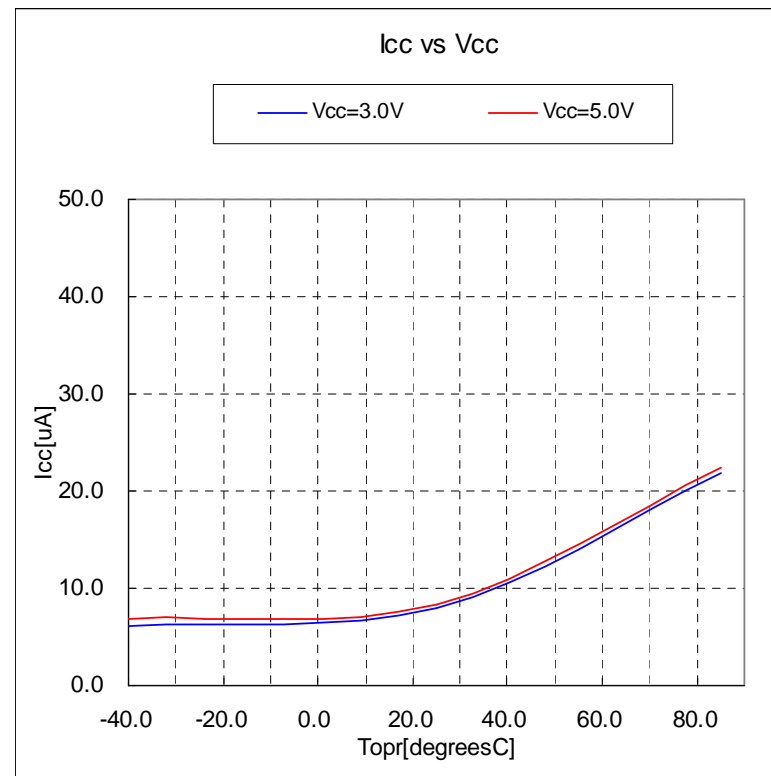


Figure68. Icc vs Topr

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

7. Power supply current(16)

(8) Stop mode

1. I_{cc} vs V_{cc}

■ Operating Condition

Main clock stop

40MHz on-chip oscillator stop

125kHz on-chip oscillator stop

Peripheral clock stop

Topr = -40,25,85(degrees C)

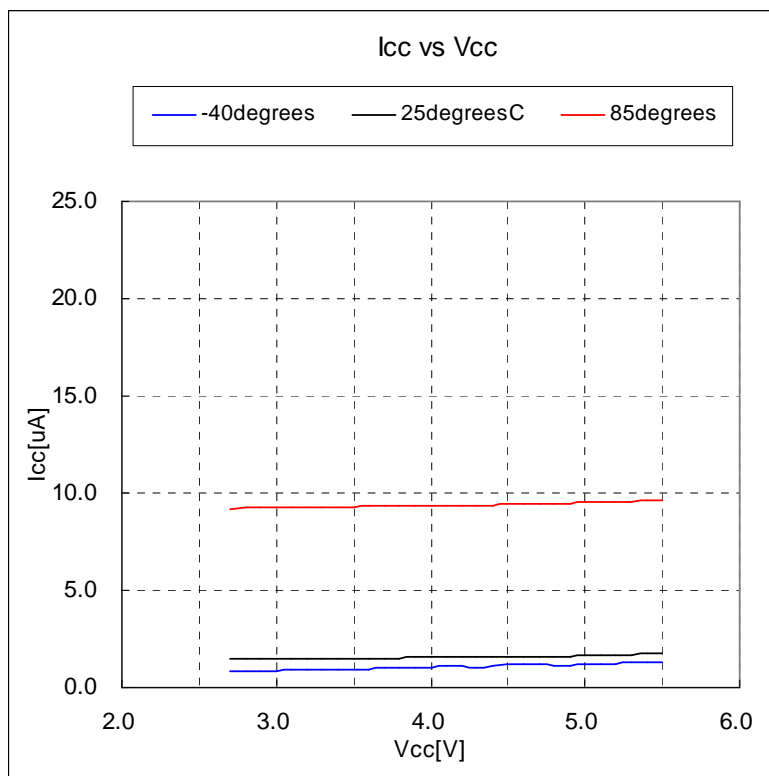


Figure69. I_{cc} vs V_{cc}

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

7. Power supply current(17)

(8) Stop mode

2. Icc vs Topr

■ Operating Condition

Main clock stop

40MHz on-chip oscillator stop

125kHz on-chip oscillator stop

Peripheral clock stop

Topr = -40 to 85(degrees C)

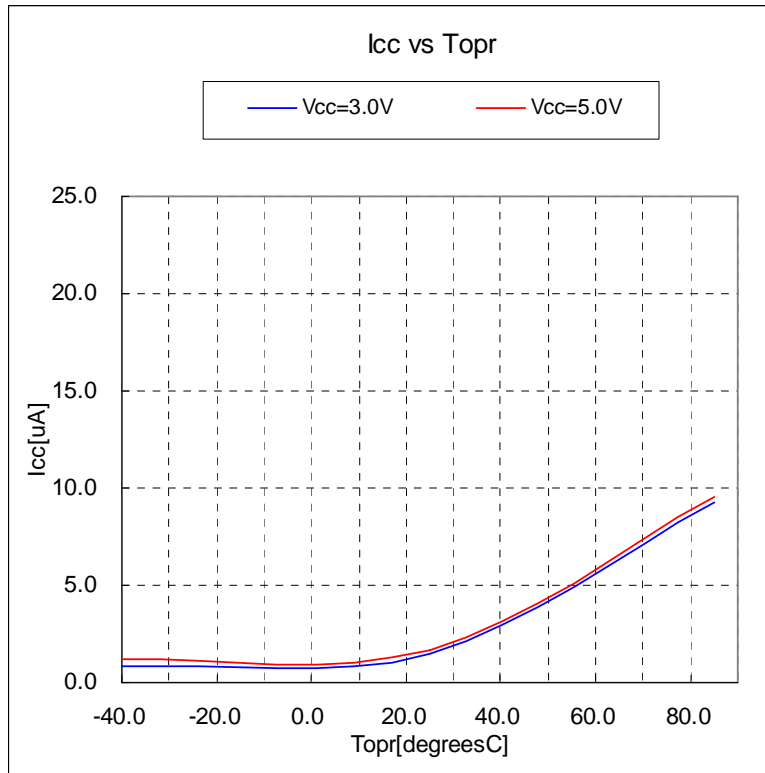


Figure70. Icc vs Topr

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

7. Power supply current(18) (9) During A/D conversion

■ Operating Condition
f(BCLK) = ϕ AD = 10MHz
Vcc1 = Vcc2 = VREF
Topr = 25(degrees C)
No division
This applies when using one A/D converter.

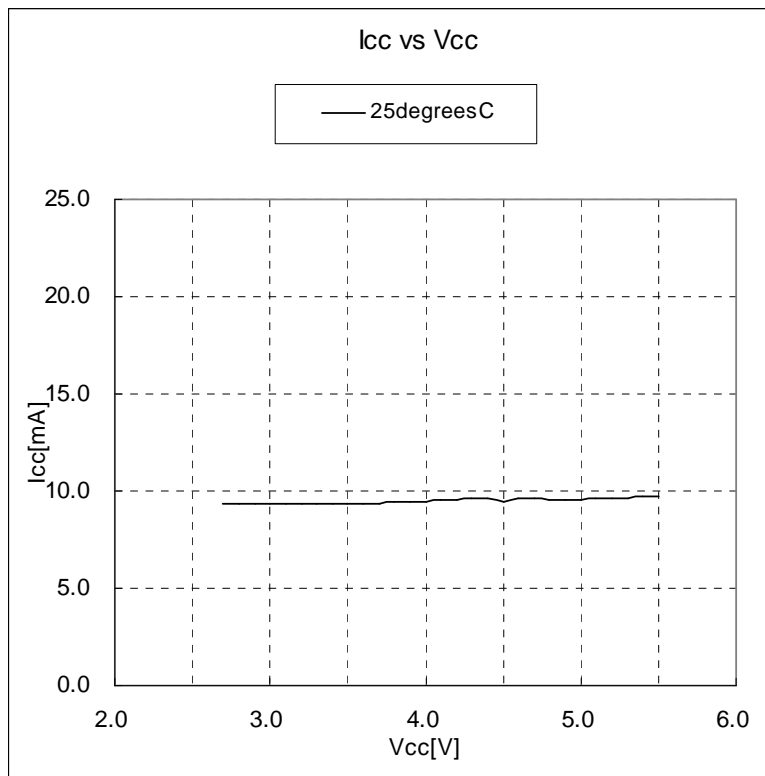


Figure71. Icc vs Vcc

■ Operating Condition
f(BCLK) = ϕ AD = 16MHz
Vcc1 = Vcc2 = VREF
Topr = 25(degrees C)
No division
This applies when using one A/D converter.

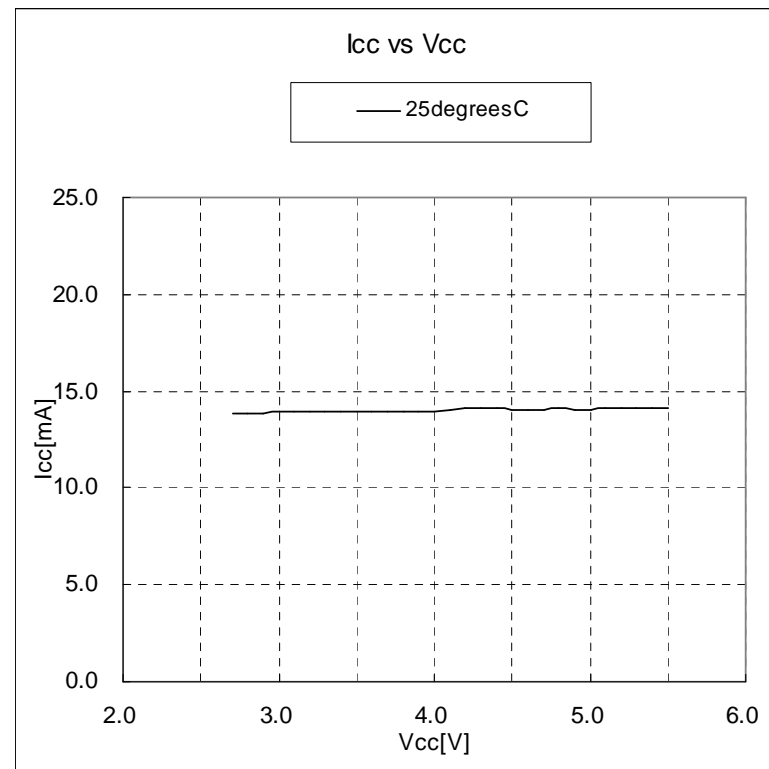


Figure72. Icc vs Vcc

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.

7. Power supply current(19)

(10) During flash memory erase / program

■ Operating Condition
f(BCLK) = 4 to 10MHz
PM17 = 1 (one wait)
Vcc=3.0V
Topr = 25(degrees C)
No division

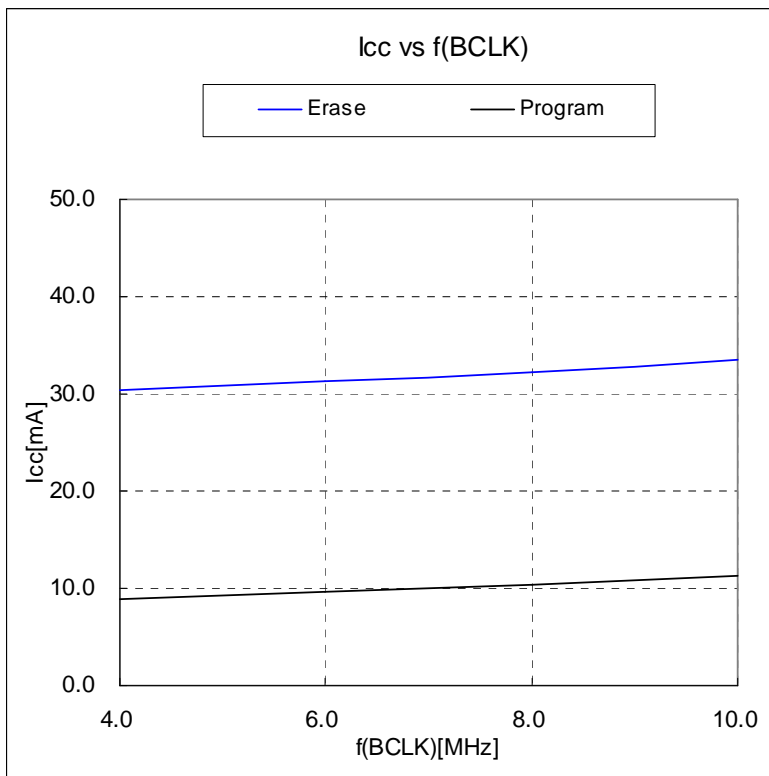


Figure73. Icc vs f(BCLK) (Vcc=3.0V)

■ Operating Condition
f(BCLK) = 4 to 10MHz
PM17 = 1 (one wait)
Vcc=5.0V
Topr = 25(degrees C)
No division

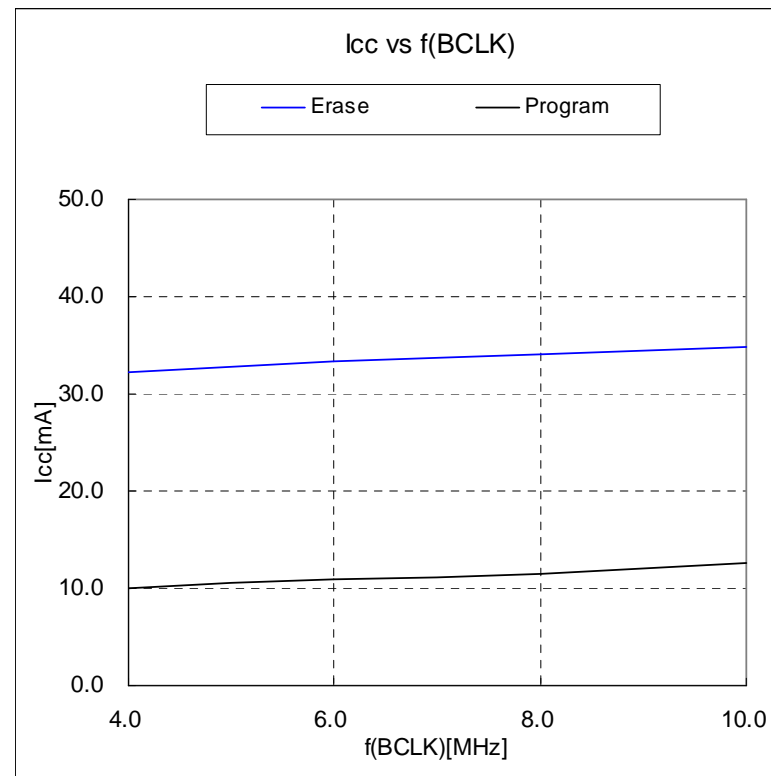


Figure74. Icc vs f(BCLK) (Vcc=5.0V)

The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics.



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