

■Renesas Flash Driver and EEPROM Emulation Software for RL78 Target MCU List

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R20UT5631EJ0101

- This list is a compilation of the target MCUs of the RL78 family Renesas Flash Driver and EEPROM Emulation Software.
RFD and EES can be used with the target MCUs in this list.
The items [R-x] ([R-1], [R-2], etc.) in the list are items used in RFD and EES.
For details, refer to the user's manual of the target software.
- RFD and EES cannot be used on MCUs other than those listed in the "Target MCUs".
- RL78/x1x (RL78/G1x, and RL78/F1x, and ...etc.) products cannot use RFD RL78 Type 0x and EES RL78 Type 0x.
For the target flash libraries of RL78/x1x, see the self RAM List (R20UT2944).

Target Software: **Renesas Flash Driver RL78 Type 01**
 EEPROM Emulation Software RL78 Type 01

Target MCUs

MCU Group	Code Flash memory		User RAM		Data Flash memory		[R-1]	[R-2]	[R-3]	[R-4]	[R-5]	[R-6]	[R-7]	[R-8]	Target MCU name
	Size (bytes)	Start/End Address	Size (bytes)	Start/End Address	Size (bytes)	Start/End Address	RAM Start Address	ROM End Address 1	ROM End Address 2	Data Flash End Address	OCD_ROM	Trace_RAM	END_BLOCK	CATEGORY	
RL78/G21 note1	128K	0x00000 - 0x1FFFF	8K	0xFDF00 - 0xFFEFFF	4K	0xF1000 - 0xF1FFF	0xFDF00	0x0FFFF	0x1FFFF	0xF1FFF	0x1FE00	0xFE300	64	01	R7F103GxG (x = 6, B, F, G, L)
	256K	0x00000 - 0x3FFFF	8K	0xFDF00 - 0xFFEFFF	4K	0xF1000 - 0xF1FFF	0xFDF00	0x0FFFF	0x3FFFF	0xF1FFF	0x3FE00	0xFE300	128	01	R7F103GxJ (x = 6, B, F, G, L)
RL78/G22	32K	0x00000 - 0x07FFF	4K	0xFE000 - 0xFFEFFF	2K	0xF1000 - 0xF17FF	0xFE000	0x07FFF	-	0xF17FF	0x07E00	0xFF300	16	01	R7F102GxC (x = 4, 6, 7, 8, A, B, C, E, F, G)
	64K	0x00000 - 0x0FFFF	4K	0xFE000 - 0xFFEFFF	2K	0xF1000 - 0xF17FF	0xFE000	0x0FFFF	-	0xF17FF	0x0FE00	0xFF300	32	01	R7F102GxE (x = 4, 6, 7, 8, A, B, C, E, F, G)
RL78/G23	96K	0x00000 - 0x17FFF	12K	0xFC000 - 0xFFEFFF	8K	0xF1000 - 0xF2FFF	0xFC000	0x0FFFF	0x17FFF	0xF2FFF	0x17E00	0xFD300	48	01	R7F100GxF (x = A, B, C, E, F, G, J, L)
	128K	0x00000 - 0x1FFFF	16K	0xFBF00 - 0xFFEFFF	8K	0xF1000 - 0xF2FFF	0xFBF00	0x0FFFF	0x1FFFF	0xF2FFF	0x1FE00	0xFC300	64	01	R7F100GxG (x = A, B, C, E, F, G, J, L, M, P)
	128K	0x00000 - 0x1FFFF	16K	0xFBF00 - 0xFFEFFF	8K	0xF1000 - 0xF2FFF	0xFBF00	0x0FFFF	0x1FFFF	0xF2FFF	0x1FE00	0xFC300	64	01	R7F104GxG (x = B)
	192K	0x00000 - 0x2FFFF	20K	0xFA000 - 0xFFEFFF	8K	0xF1000 - 0xF2FFF	0xFA000	0x0FFFF	0x2FFFF	0xF2FFF	0x2FE00	0xFB300	96	01	R7F100GxH (x = A, B, C, E, F, G, J, L, M, P)
	256K	0x00000 - 0x3FFFF	24K	0xF9000 - 0xFFEFFF	8K	0xF1000 - 0xF2FFF	0xF9000	0x0FFFF	0x3FFFF	0xF2FFF	0x3FE00	0xFA300	128	01	R7F100GxJ (x = A, B, C, E, F, G, J, L, M, P, S)
	256K	0x00000 - 0x3FFFF	24K	0xF9000 - 0xFFEFFF	8K	0xF1000 - 0xF2FFF	0xF9000	0x0FFFF	0x3FFFF	0xF2FFF	0x3FE00	0xFA300	128	01	R7F104GxJ (x = F, G)
	384K	0x00000 - 0x5FFFF	32K	0xF7000 - 0xFFEFFF	8K	0xF1000 - 0xF2FFF	0xF7000	0x0FFFF	0x5FFFF	0xF2FFF	0x5FE00	0xF8300	192	01	R7F100GxK (x = F, G, J, L, M, P, S)
	512K	0x00000 - 0x7FFFF	48K	0xF3000 - 0xFFEFFF	8K	0xF1000 - 0xF2FFF	0xF3000	0x0FFFF	0x7FFFF	0xF2FFF	0x7FE00	0xF4300	256	01	R7F100GxL (x = F, G, J, L, M, P, S)
RL78/G24	768K	0x00000 - 0xBFFFF	48K	0xF3000 - 0xFFEFFF	8K	0xF1000 - 0xF2FFF	0xF3000	0x0FFFF	0xBFFFF	0xF2FFF	0xBFE00	0xF4300	384	01	R7F100GxN (x = F, G, J, L, M, P, S)
	64K	0x00000 - 0x0FFFF	12K	0xFC000 - 0xFFEFFF	4K	0xF1000 - 0xF1FFF	0xFC000	0x0FFFF	-	0xF1FFF	0x0FE00	0xFD300	32	02	R7F101GxE (x = 6, 7, 8, A, B, E, F, G, J, L)
	128K	0x00000 - 0x1FFFF	12K	0xFC000 - 0xFFEFFF	4K	0xF1000 - 0xF1FFF	0xFC000	0x0FFFF	0x1FFFF	0xF1FFF	0x1FE00	0xFD300	64	02	R7F101GxG (x = 6, 7, 8, A, B, E, F, G, J, L)

note1 RL78/G21 do not have a function of "XTSTOP" bit. Therefore, be sure to modify into a comment the description line of "XTSTOP".
/* XTSTOP = 1u; CSC(Oscillation circuit stop XT1) */
[The files which described "XTSTOP".]
 CC-RL compiler : hdwinit.c / IAR compiler : low_level_init.c / LLVM compiler : hwinit.c

Target Software: **Renesas Flash Driver RL78 Type 11**
 EEPROM Emulation Software RL78 Type 11

Target MCUs

MCU Group	Code Flash memory		User RAM		Data Flash memory		[R-1]	[R-2]	[R-3]	[R-4]	[R-5]	[R-6]	[R-7]	—	Target MCU name
	Size (bytes)	Start/End Address	Size (bytes)	Start/End Address	Size (bytes)	Start/End Address	RAM Start Address	ROM End Address 1	ROM End Address 2	Data Flash End Address	OCD_ROM	Trace_RAM	END_BLOCK	—	
RL78/L23	64K	0x00000 - 0x0FFFF	16K	0xFBF00 - 0xFFEFFF	8K	0xF1000 - 0xF2FFF	0xFBF00	0x0FFFF	-	0xF2FFF	0x0FE00	0xFC300	32	-	R7F100LxE (x = F, G, J, L)
	128K	0x00000 - 0x1FFFF	16K	0xFBF00 - 0xFFEFFF	8K	0xF1000 - 0xF2FFF	0xFBF00	0x0FFFF	0x1FFFF	0xF2FFF	0x1FE00	0xFC300	64	-	R7F100LxG (x = F, G, J, L, M, P)
	256K	0x00000 - 0x3FFFF	32K	0xF7000 - 0xFFEFFF	8K	0xF1000 - 0xF2FFF	0xF7000	0x0FFFF	0x3FFFF	0xF2FFF	0x3FE00	0xF8300	128	-	R7F100LxJ (x = F, G, J, L, M, P)
	512K	0x00000 - 0x7FFFF	32K	0xF7000 - 0xFFEFFF	8K	0xF1000 - 0xF2FFF	0xF7000	0x0FFFF	0x7FFFF	0xF2FFF	0x7FE00	0xF8300	256	-	R7F100LxL (x = F, G, J, L, M, P)

Target Software: **Renesas Flash Driver RL78 Type 02**
 EEPROM Emulation Software RL78 Type 02

Target MCUs

MCU Group	Code Flash memory		User RAM		Data Flash memory		[R-1]	[R-2]	[R-3]	[R-4]	[R-5]	[R-6]	[R-7]	[R-8]	Target MCU name
	Size (bytes)	Start/End Address	Size (bytes)	Start/End Address	Size (bytes)	Start/End Address	RAM Start Address	ROM End Address 1	ROM End Address 2	Data Flash End Address	OCD_ROM	Trace_RAM	Hot plug-in	END_BLOCK	
RL78/F23	128K	0x00000 - 0x1FFFF	12K	0xFCF00 - 0xFFEFF	8K	0xF1000 - 0xF2FFF	0xFCF00	0x0FFFF	0x1FFFF	0xF2FFF	0x1FE00	0xFD300	0xFD500	128	R7F123FxG (x = B, G, L, M)
RL78/F24	256K	0x00000 - 0x3FFFF	24K	0xF9F00 - 0xFFEFF	16K	0xF1000 - 0xF4FFF	0xF9F00	0x0FFFF	0x3FFFF	0xF4FFF	0x3FE00	0xFA300	0xFA500	256	R7F124FxJ (x = B, G, L, M, P)

Target Software: **Renesas Flash Driver RL78 Type 03**
 EEPROM Emulation Software RL78 Type 03

Target MCUs

MCU Group	Code Flash memory		User RAM		Data Flash memory		[R-1]	[R-2]	[R-3]	[R-4]	[R-5]	[R-6]	[R-7]	[R-8]	Target MCU name
	Size (bytes)	Start/End Address	Size (bytes)	Start/End Address	Size (bytes)	Start/End Address	RAM Start Address	ROM End Address 1	ROM End Address 2	Data Flash End Address	OCD_ROM	Trace_RAM	Hot plug-in	END_BLOCK	
RL78/F22	128K	0x00000 - 0x1FFFF	12K	0xFCF00 - 0xFFEFF	8K	0xF1000 - 0xF2FFF	0xFCF00	0x0FFFF	0x1FFFF	0xF2FFF	0x1FE00	0xFD300	0xFD500	64	R7F122FxG (x = 7, B, G)
RL78/F25	512K	0x00000 - 0x7FFFF	40K	0xF5F00 - 0xFFEFF	16K	0xF1000 - 0xF4FFF	0xF5F00	0x0FFFF	0x7FFFF	0xF4FFF	0x7FE00	0xF6300	0xF6500	256	R7F125FxL (x = G, L, M, P)

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