

HS-OP470ARHEV1Z

Evaluation Board

AN1719 Rev.1.00 March 14, 2013

Introduction

The HS-OP470ARHEV1Z evaluation platform is designed to evaluate the HS-OP470ARH. The evaluation board contains all the circuitry needed to critique the performance of the HS-OP470ARH amplifier. The HS-OP470ARH is a radiation hardened, monolithic quad operational amplifier that provides highly reliable performance in harsh radiation environments. Its excellent noise characteristics coupled with an unique array of dynamic specifications make this amplifier well-suited for a variety of satellite system applications. Dielectrically isolated, bipolar process makes this device immune to Single Event Latch-up.

Reference Documents

- HS-0P470ARH Data Sheet FN4471
- HS-0P470ARH SMD 5962-98533
- HS-OP40ARH ELDRS Test Report

Evaluation Board Key Features

- Dual Supply Operation: ±5V to ±15V
- Singled-Ended or Differential Input Operation with Gain (G = 10V/V)
- External VREF input
- Banana Jack Connectors for Power Supply and VREF Inputs
- BNC Connectors for Op Amp Input and Output Terminals
- Convenient PCB Pads for Op Amp Input/Output Impedance Loading

Power Supply Connections

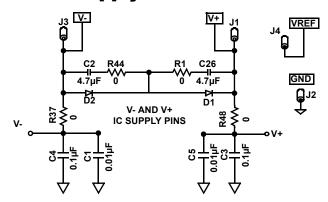


FIGURE 1. POWER SUPPLY CIRCUIT

Figure 1 demonstrates the power supply connections, decoupling and protection circuitry. External power connections are made through the V+, V-, VREF, and GND banana jack connections on the evaluation board. De-coupling capacitors C2 and C26 provide low-frequency power-supply filtering, while additional capacitors, C1, C3, C4 and C5, which are connected close to the part, filter out high frequency noise and are connected to their respective supplies through R37 and R48 resistors. These resistors are 0Ω but can be changed by the user to provide additional power supply filtering, or to reduce the supply voltage rate-of-rise time. Anti-reverse diodes D1 and D2 protect the circuit in the momentary case of accidentally reversing the power supplies to the evaluation board. The VREF pin can be connected to ground to establish a ground referenced input for split supply operation.

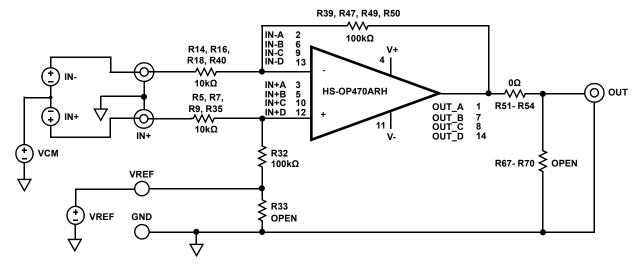


FIGURE 2. BASIC DIFFERENTIAL AMPLIFIER CONFIGURATION

Amplifier Configuration

A simplified schematic of the evaluation board is shown in Figure 2. The input stage with the components supplied is shown in Figure 3. The circuit implements a Hi-Z differential input with unbalanced common mode impedance. The differential amplifier gain is expressed in Equation 1:

$$V_{OUT} = (V_{IN+} - V_{IN-}) \bullet (R_F / R_{IN}) + V_{REF}$$
 (EQ. 1)

For single-ended input with an inverting gain G = -10V/V, the IN+ input is grounded and the signal is supplied to the IN- input. VREF must be connected to a reference voltage between the V+ and V-supply rails. For non-inverting operation with G = 11V/V, the negative input (IN-) is grounded and the signal is supplied to the positive input (IN+). The non-inverting gain is strongly dependent on any resistance from IN- to GND. For good gain accuracy, a 0Ω resistor should be installed on the empty R11 pad.

User-Selectable Options

Component pads are included to enable a variety of user-selectable circuits to be added to the amplifier inputs, the VREF input, outputs and the amplifier feedback loops.

A voltage divider can be added to establish a power supply-tracking common mode reference using the VREF input. The inverting and non-inverting inputs have additional resistor and capacitor placements for adding input attenuation or feedback capacitors (Figure 3).

The outputs (Figure 4) also have additional resistor and capacitor placements for filtering and loading.

NOTE: Operational amplifiers are sensitive to output capacitance and may oscillate. In the event of oscillation, reduce output capacitance by using shorter cables, or add a resistor in series with the output.

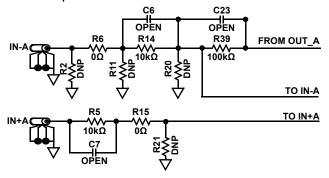


FIGURE 3. INPUT STAGE

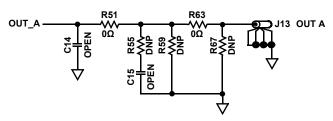


FIGURE 4. OUTPUT STAGE

TABLE 1. HS-OP470ARHEV1Z COMPONENTS PARTS LIST

DEVICE #	DESCRIPTION	COMMENTS
C1, C5	CAP, SMD, 0805, 0.01µF, 50V, 10%, X7R, ROHS	Power Supply Decoupling
C2, C26	CAP, SMD, 1210, 4.7µF, 50V, 10%, X7R, ROHS	Power Supply Decoupling
C3, C4	CAP, SMD, 0805, 0.1µF, 25V, 10%, X7R, ROHS	Power Supply Decoupling
C6-C26	CAP, SMD, 0603, Open Place Holder, ROHS	User Selectable Capacitors - Not Populated
D1, D2	40V Schottky Barrier Diode	Reverse Power Protection
J 1 -J4	Johnson Components Standard Type Banana Jack, 108-0740-001	Power Supply and Reference Voltage Connector
J5-J16	AMPHENOL BNC Connector, 31-5329-52RFX	Connections for Input and Output
R32	Resistor, SMD, 0603, 100kΩ, 1%, ROHS	VREF Resistor Divider
R5, R7, R9, R14, R16, R18, R35, R40	Resistor, SMD, 0603, 10kΩ, 1%, 1/16W, ROHS	Gain Setting Resistor
R39, R47, R49, R50	Resistor, SMD, 0603, 100kΩ, 1%, 1/16W, ROHS	Gain Setting Feedback Resistor
R2, R3, R4, R11, R12, R13, R20, R21, R22, R23, R25, R26, R28, R30, R31, R33, R34, R38, R42, R43, R46, R55, R56, R57, R58, R59, R60, R61, R62, R62, R67, R68, R69, R70	Resistor, SMD, 0603, DNP-Place Holder, ROHS	User Selectable Resistors - Not Populated
U1	HS-OP470ARH, Radiation Hardened, Very Low Noise Quad Operational Amplifier	

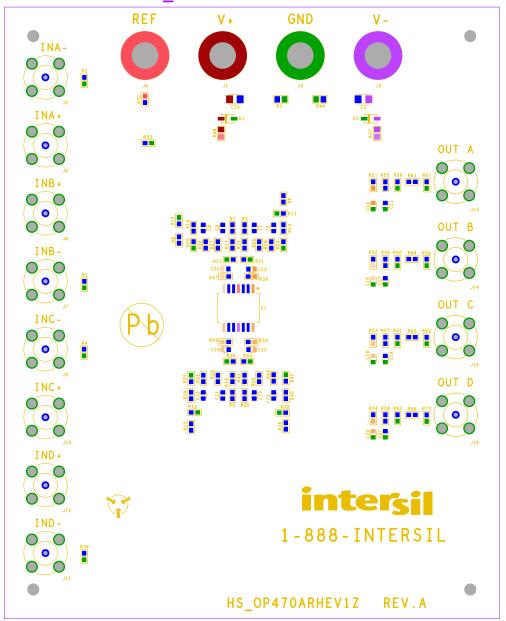


HS-OP470ARHEV1Z Top View



HS_OP470ARHEV1Z REV.A





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SILKSCREEN TOP
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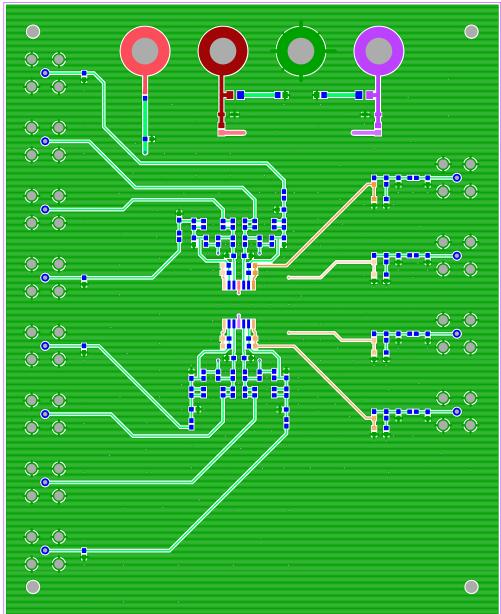
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HS-OP470ARHEV1Z Top Layer







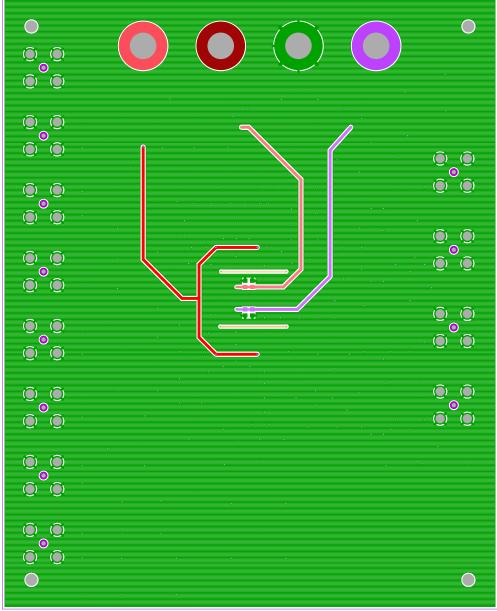
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HS-OP470ARHEV1Z Bottom Layer



HS_OP470ARHEV1Z REV.A





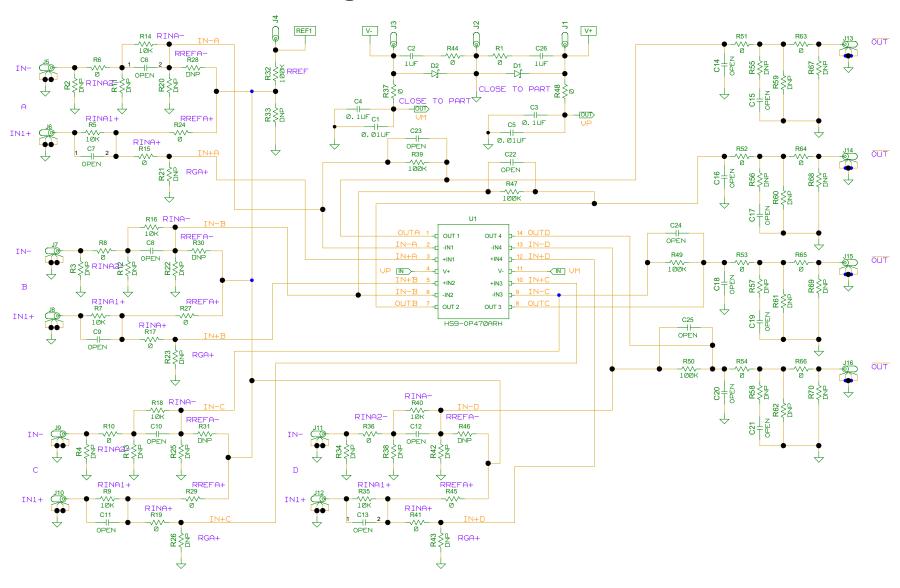


BOTTOM LAYER

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HS-OP470ARHEV1Z Schematic Diagram



HS-OP470ARHEV1Z

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Renesas Electronics America Inc. 1001 Murphy Ranch Road, Milpitas, CA 95035, U.S.A. Tel: +1-408-432-8888, Fax: +1-408-434-5351

Renesas Electronics Canada Limited 9251 Yonge Street, Suite 8309 Richmond Hill, Ontario Canada L4C 9T3 Tel: +1-905-237-2004

Renesas Electronics Europe Limited Dukes Meadow, Milliboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K Tel: +44-1628-651-700, Fax: +44-1628-651-804

Renesas Electronics Europe GmbH

Arcadiastrasse 10, 40472 Düsseldorf, German Tel: +49-211-6503-0, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
Room 1709 Quantum Plaza, No.27 ZhichunLu, Haidian District, Beijing, 100191 P. R. China Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 301, Tower A, Central Towers, 555 Langao Road, Putuo District, Shanghai, 200333 P. R. China Tel: +86-21-2226-0898, Fax: +86-21-2226-0999

Renesas Electronics Hong Kong Limited

Unit 1601-1611, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong Tel: +852-2265-6688, Fax: +852 2886-9022

Renesas Electronics Taiwan Co., Ltd.

13F, No. 363, Fu Shing North Road, Taipei 10543, Taiwan Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd. 80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre, Singapore 339949 Tel: +65-6213-0200, Fax: +65-6213-0300

Renesas Electronics Malaysia Sdn.Bhd. Unit 1207, Block B, Menara Amcorp, Amco Amcorp Trade Centre, No. 18, Jin Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia

Unit 1207, Block B, Menara Amcorp, Amcorp Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics India Pvt. Ltd. No.777C, 100 Feet Road, HAL 2nd Stage, Indiranagar, Bangalore 560 038, India Tel: +91-80-67208700, Fax: +91-80-67208777

Renesas Electronics Korea Co., Ltd. 17F, KAMCO Yangiae Tower, 262, Gangnam-daero, Gangnam-gu, Seoul, 06265 Korea Tel: +82-2-558-3737, Fax: +82-2-558-5338