

To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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(Note 2) “Renesas Electronics product(s)” means any product developed or manufactured by or for Renesas Electronics.

PFESiP® EP-1 Development Evaluation Kit

NEC Electronics' PFESiP EP-1^{Note} Development Evaluation Kit supports the development of EP-1 Series products in terms of both software development of the microcontroller and hardware development of the logic, reduces development risks, and accelerates development.

This development evaluation kit is suitable for evaluating the use of EP-1 Series products with easily and quickly emulate the EP-1 Series functions.

Note PFESiP : Platform for Embedded System in a Package
EP-1 : Embedded Processor -1

Features of the development evaluation kit

EP-1 evaluation environment (EP-1 Evaluation Board Lite)

- EP-1 dedicated microcontroller (V850E2 core: 200 MHz, max.) mounted
- FPGA mounted as a customizable area
 - Altera Stratix II EP2S15 used as the FPGA
 - Logic of up to 130 K gates can be provided
- USB 2.0 FS ports provided (host x 2, device x 1)
- LAN evaluation environment (LAN controller made by SMSC: LAN9115) provided
- Connectors allowing expandability equipped
 - 80 signals each for the EP-1 dedicated microcontroller ports and FPGA GPIO

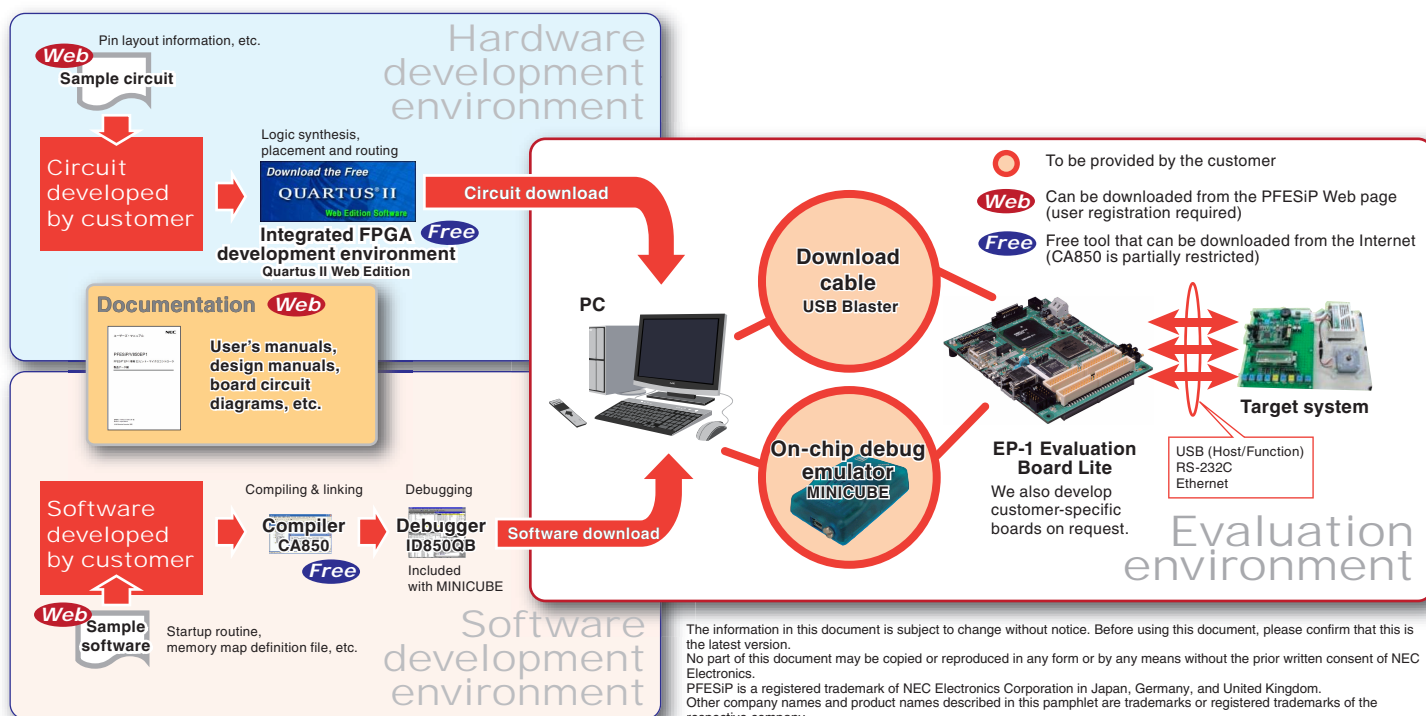
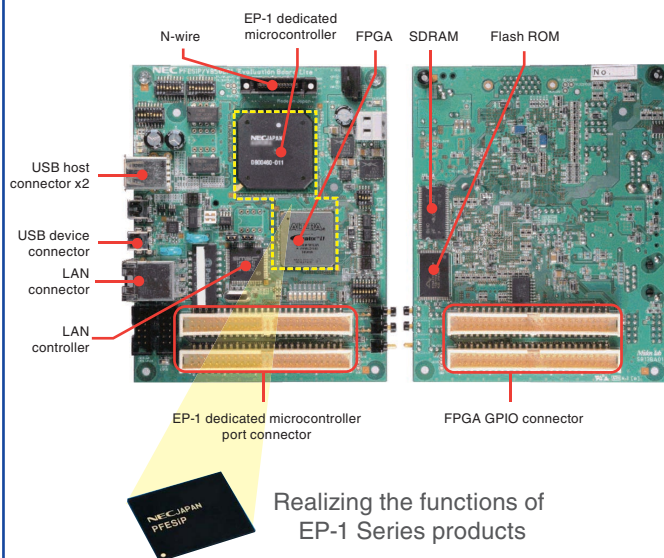
Microcontroller block software development

- Development can be started right away by using the debugger and compiler included with the on-chip debug emulator (MINICUBE).
- USB sample programs can be downloaded from the Internet.

Logic block hardware development

- Quartus II Web Edition (free) can be used
- Complicated pin layout information is not required, because sample circuits can be downloaded from the Internet.

EP-1 Evaluation Board Lite



PFESiP Web site: <http://www.necel.com/pfesip/en/>