

GaAs INTEGRATED CIRCUIT μ PG2406T6R

0.01 to 3.0 GHz SPDT SWITCH

DESCRIPTION

The μ PG2406T6R is a GaAs MMIC for L, S-band SPDT (Single Pole Double Throw) switch which were designed for mobile phone and another L, S-band application.

This device can operate 2 control switching by control voltage 1.8 to 3.3 V. This device can operate frequency from 0.01 to 3.0 GHz, having the low insertion loss and high isolation.

This device is housed in a 6-pin plastic TSSON (Thin Shrink Small Out-line Non-leaded) (T6R) package. And this package is able to high-density surface mounting.

FEATURES

- Switch control voltage : $V_{\text{cont}}(\text{H}) = 1.8 \text{ to } 3.3 \text{ V}$ (2.7 V TYP.)
: $V_{\text{cont}}(\text{L}) = -0.2 \text{ to } +0.2 \text{ V}$ (0 V TYP.)
- Low insertion loss : $L_{\text{ins}} = 0.40 \text{ dB TYP. @ } f = 1.0 \text{ GHz, } V_{\text{cont}}(\text{H}) = 2.7 \text{ V, } V_{\text{cont}}(\text{L}) = 0 \text{ V}$
: $L_{\text{ins}} = 0.47 \text{ dB TYP. @ } f = 2.5 \text{ GHz, } V_{\text{cont}}(\text{H}) = 2.7 \text{ V, } V_{\text{cont}}(\text{L}) = 0 \text{ V}$
- High isolation : $\text{ISL} = 27 \text{ dB TYP. @ } f = 1.0 \text{ GHz, } V_{\text{cont}}(\text{H}) = 2.7 \text{ V, } V_{\text{cont}}(\text{L}) = 0 \text{ V}$
: $\text{ISL} = 17 \text{ dB TYP. @ } f = 2.5 \text{ GHz, } V_{\text{cont}}(\text{H}) = 2.7 \text{ V, } V_{\text{cont}}(\text{L}) = 0 \text{ V}$
- Handling power : $P_{\text{in}}(0.1 \text{ dB}) = +29.0 \text{ dBm TYP. @ } f = 2.0/2.5 \text{ GHz, } V_{\text{cont}}(\text{H}) = 2.7 \text{ V, } V_{\text{cont}}(\text{L}) = 0 \text{ V}$
: $P_{\text{in}}(1 \text{ dB}) = +30.5 \text{ dBm TYP. @ } f = 0.5 \text{ to } 3.0 \text{ GHz, } V_{\text{cont}}(\text{H}) = 2.7 \text{ V, } V_{\text{cont}}(\text{L}) = 0 \text{ V}$
- High-density surface mounting : 6-pin plastic TSSON (T6R) package ($1.0 \times 1.0 \times 0.37 \text{ mm}$)

APPLICATIONS

- L, S-band digital cellular or cordless telephone
- W-LAN, WLL and Bluetooth™ etc.

ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Supplying Form
μ PG2406T6R-E2	μ PG2406T6R-E2-A	6-pin plastic TSSON (T6R) (Pb-Free)	G9	<ul style="list-style-type: none"> • Embossed tape 8 mm wide • Pin 1, 6 face the perforation side of the tape • Qty 5 kpcs/reel

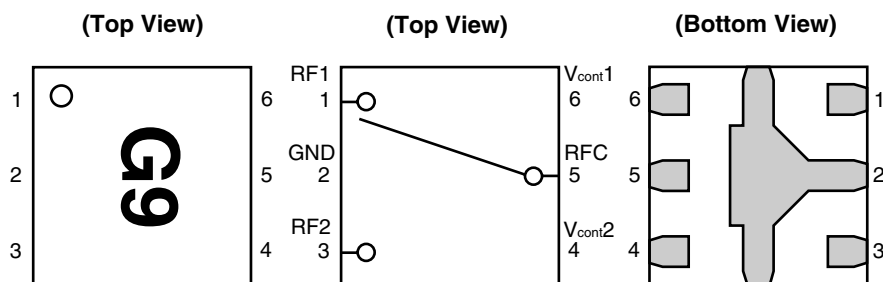
Remark To order evaluation samples, please contact your nearby sales office.

Part number for sample order: μ PG2406T6R

Caution Although this device is designed to be as robust as possible, ESD (Electrostatic Discharge) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions must be employed at all times.

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PIN CONNECTIONS AND INTERNAL BLOCK DIAGRAM



Pin No.	Pin Name
1	RF1
2	GND
3	RF2
4	V _{cont2}
5	RFC
6	V _{cont1}

SW TRUTH TABLE

ON Path	V _{cont1}	V _{cont2}
RFC-RF1	High	Low
RFC-RF2	Low	High

ABSOLUTE MAXIMUM RATINGS (T_A = +25°C, unless otherwise specified)

Parameter	Symbol	Ratings	Unit
Switch Control Voltage	V _{cont}	+6.0 ^{Note}	V
Input Power	f = 0.01 to 0.5 GHz	P _{in1}	+24.0
	f = 0.5 to 3.0 GHz	P _{in2}	+31.0
Operating Ambient Temperature	T _A	−45 to +85	°C
Storage Temperature	T _{stg}	−55 to +150	°C

Note |V_{cont1} − V_{cont2}| ≤ 6.0 V

RECOMMENDED OPERATING RANGE (T_A = +25°C, unless otherwise specified)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Switch Control Voltage (H)	V _{cont (H)}	1.8	2.7	3.3	V
Switch Control Voltage (L)	V _{cont (L)}	−0.2	0	0.2	V
Control Voltage Difference	ΔV _{cont (H)} , ΔV _{cont (L)} ^{Note}	−0.1	0	0.1	V

Note ΔV_{cont (H)} = V_{cont1 (H)} − V_{cont2 (H)}

ΔV_{cont (L)} = V_{cont1 (L)} − V_{cont2 (L)}

ELECTRICAL CHARACTERISTICS

(T_A = +25°C, V_{cont} (H) = 2.7 V, V_{cont} (L) = 0 V, DC blocking capacitors = 56 pF, unless otherwise specified)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Insertion Loss 1	L _{ins1}	f = 0.01 to 0.05 GHz ^{Note 1}	–	0.40	–	dB
Insertion Loss 2	L _{ins2}	f = 0.05 to 0.5 GHz ^{Note 2}	–	0.40	0.45	dB
Insertion Loss 3	L _{ins3}	f = 0.5 to 1.0 GHz	–	0.40	0.45	dB
Insertion Loss 4	L _{ins4}	f = 1.0 to 2.0 GHz	–	0.45	0.50	dB
Insertion Loss 5	L _{ins5}	f = 2.0 to 2.5 GHz	–	0.47	0.55	dB
Insertion Loss 6	L _{ins6}	f = 2.5 to 3.0 GHz	–	0.53	0.60	dB
Isolation 1	ISL1	f = 0.01 to 0.05 GHz ^{Note 1}	–	27	–	dB
Isolation 2	ISL2	f = 0.05 to 0.5 GHz ^{Note 2}	23	27	–	dB
Isolation 3	ISL3	f = 0.5 to 1.0 GHz	23	27	–	dB
Isolation 4	ISL4	f = 1.0 to 2.0 GHz	16	19	–	dB
Isolation 5	ISL5	f = 2.0 to 2.5 GHz	14	17	–	dB
Isolation 6	ISL6	f = 2.5 to 3.0 GHz	14	17	–	dB
Input Return Loss 1	RL _{in1}	f = 0.01 to 0.05 GHz ^{Note 1}	–	20	–	dB
Input Return Loss 2	RL _{in2}	f = 0.05 to 0.5 GHz ^{Note 2}	15	20	–	dB
Input Return Loss 3	RL _{in3}	f = 0.5 to 3.0 GHz	15	20	–	dB
Output Return Loss 1	RL _{out1}	f = 0.01 to 0.05 GHz ^{Note 1}	–	20	–	dB
Output Return Loss 2	RL _{out2}	f = 0.05 to 0.5 GHz ^{Note 2}	15	20	–	dB
Output Return Loss 3	RL _{out3}	f = 0.5 to 3.0 GHz	15	20	–	dB
0.1 dB Loss Compression	P _{in} (0.1 dB)	f = 2.0/2.5 GHz	+26.0	+29.0	–	dBm
Input Power ^{Note 3}		f = 0.5 to 3.0 GHz	–	+29.0	–	dBm
1 dB Loss Compression	P _{in} (1 dB)	f = 0.5 to 3.0 GHz	–	+30.5	–	dBm
Input Power ^{Note 4}						
2nd Harmonics	2f ₀	f = 2.0/2.5 GHz, P _{in} = +20 dBm	65	75	–	dBc
3rd Harmonics	3f ₀	f = 2.0/2.5 GHz, P _{in} = +20 dBm	65	75	–	dBc
Intermodulation Intercept Point	IIP ₃	f = 0.5 to 3.0 GHz, 2 tone, 5 MHz spicing	–	+60	–	dBm
Switch Control Current	I _{cont}	No RF input	–	0.2	20	μA
Switch Control Speed	t _{sw}	50% CTL to 90/10% RF	–	50	500	ns

Notes 1. DC blocking capacitors = 10 000 pF at f = 0.01 to 0.05 GHz

2. DC blocking capacitors = 1 000 pF at f = 0.05 to 0.5 GHz

3. P_{in} (0.1 dB) is measured the input power level when the insertion loss increases more 0.1 dB than that of linear range.

4. P_{in} (1 dB) is measured the input power level when the insertion loss increases more 1 dB than that of linear range.

Caution This device is used it is necessary to use DC blocking capacitors.

The value of DC blocking capacitors should be chosen to accommodate the frequency of operation, bandwidth, switching speed and the condition with actual board of your system. The range of recommended DC blocking capacitor value is less than 56 pF.

ELECTRICAL CHARACTERISTICS

(T_A = +25°C, V_{cont} (H) = 1.8 V, V_{cont} (L) = 0 V, DC blocking capacitors = 56 pF, unless otherwise specified)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Insertion Loss 7	L _{ins7}	f = 0.01 to 0.05 GHz ^{Note 1}	–	0.40	–	dB
Insertion Loss 8	L _{ins8}	f = 0.05 to 0.5 GHz ^{Note 2}	–	0.40	0.46	dB
Insertion Loss 9	L _{ins9}	f = 0.5 to 1.0 GHz	–	0.40	0.47	dB
Insertion Loss 10	L _{ins10}	f = 1.0 to 2.0 GHz	–	0.46	0.52	dB
Insertion Loss 11	L _{ins11}	f = 2.0 to 2.5 GHz	–	0.48	0.57	dB
Insertion Loss 12	L _{ins12}	f = 2.5 to 3.0 GHz	–	0.54	0.62	dB
Isolation 7	ISL7	f = 0.01 to 0.05 GHz ^{Note 1}	–	27	–	dB
Isolation 8	ISL8	f = 0.05 to 0.5 GHz ^{Note 2}	23	27	–	dB
Isolation 9	ISL9	f = 0.5 to 1.0 GHz	23	27	–	dB
Isolation 10	ISL10	f = 1.0 to 2.0 GHz	16	19	–	dB
Isolation 11	ISL11	f = 2.0 to 2.5 GHz	14	17	–	dB
Isolation 12	ISL12	f = 2.5 to 3.0 GHz	14	17	–	dB
Input Return Loss 4	RL _{in4}	f = 0.01 to 0.05 GHz ^{Note 1}	–	20	–	dB
Input Return Loss 5	RL _{in5}	f = 0.05 to 0.5 GHz ^{Note 2}	15	20	–	dB
Input Return Loss 6	RL _{in6}	f = 0.5 to 3.0 GHz	15	20	–	dB
Output Return Loss 4	RL _{out4}	f = 0.01 to 0.05 GHz ^{Note 1}	–	20	–	dB
Output Return Loss 5	RL _{out5}	f = 0.05 to 0.5 GHz ^{Note 2}	15	20	–	dB
Output Return Loss 6	RL _{out6}	f = 0.5 to 3.0 GHz	15	20	–	dB
0.1 dB Loss Compression Input Power ^{Note 3}	P _{in (0.1 dB)}	f = 2.0/2.5 GHz	+19.0	+22.0	–	dBm
		f = 0.5 to 3.0 GHz	–	+22.0	–	dBm
1 dB Loss Compression Input Power ^{Note 4}	P _{in (1 dB)}	f = 0.5 to 3.0 GHz	–	+25.0	–	dBm
Switch Control Current	I _{cont}	No RF input	–	0.2	20	μA
Switch Control Speed	t _{sw}	50% CTL to 90/10% RF	–	50	500	ns

Notes 1. DC blocking capacitors = 10 000 pF at f = 0.01 to 0.05 GHz

2. DC blocking capacitors = 1 000 pF at f = 0.05 to 0.5 GHz

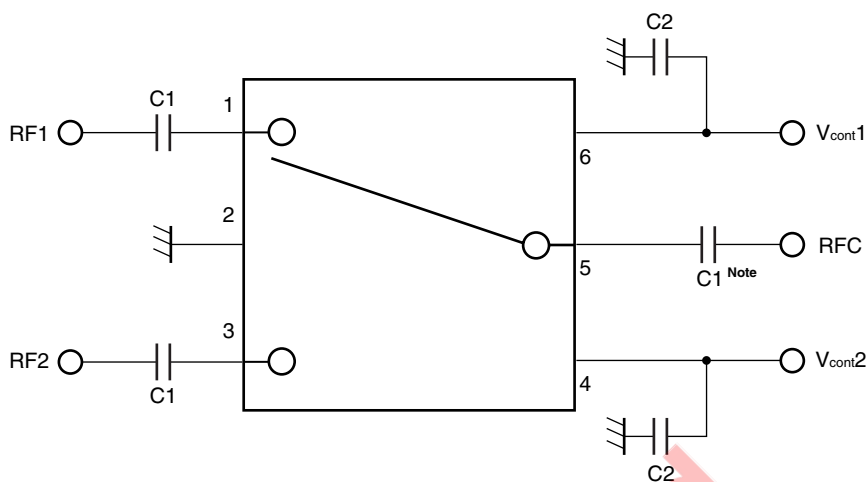
3. P_{in (0.1 dB)} is measured the input power level when the insertion loss increases more 0.1 dB than that of linear range.

4. P_{in (1 dB)} is measured the input power level when the insertion loss increases more 1 dB than that of linear range.

Caution This device is used it is necessary to use DC blocking capacitors.

The value of DC blocking capacitors should be chosen to accommodate the frequency of operation, bandwidth, switching speed and the condition with actual board of your system. The range of recommended DC blocking capacitor value is less than 56 pF.

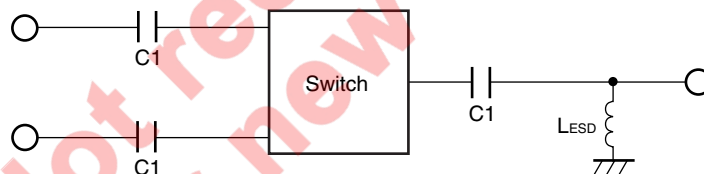
EVALUATION CIRCUIT



Note C1 : 0.01 to 0.05 GHz 10 000 pF
: 0.05 to 0.5 GHz 1 000 pF
: 0.5 to 3.0 GHz 56 pF
C2 : 1 000 pF

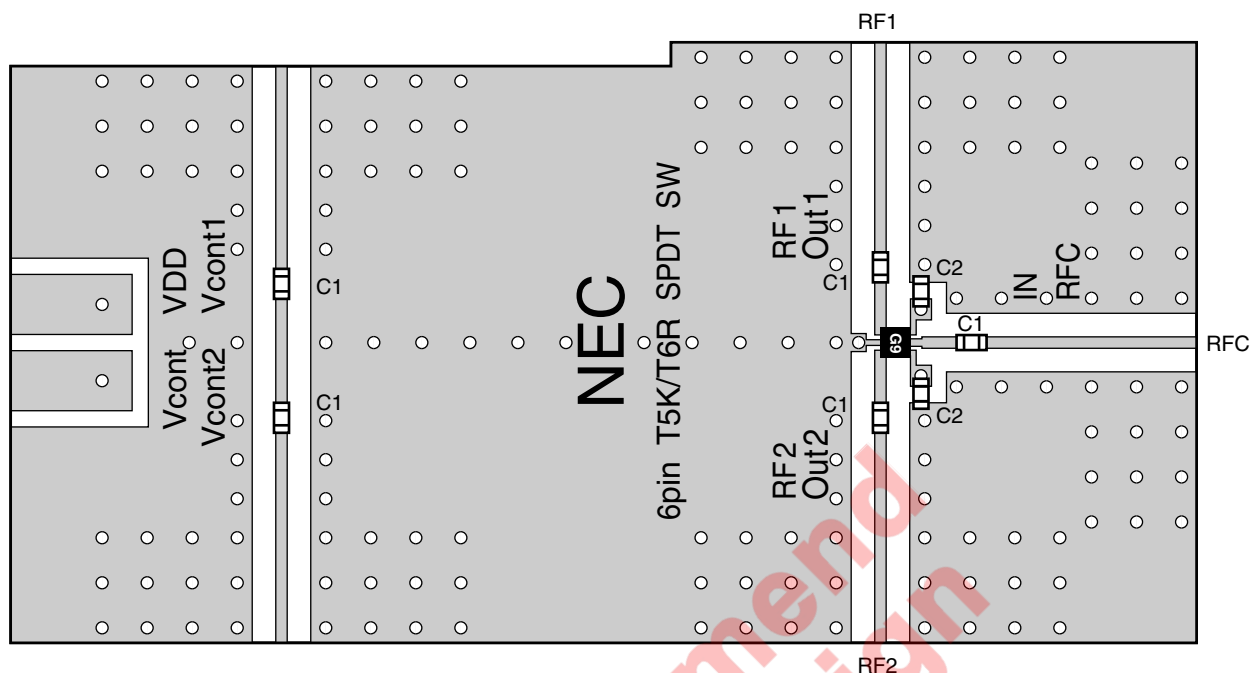
The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

APPLICATION INFORMATION



- L_{ESD} provides a means to increase the ESD protection on a specific RF port, typically the port attached to the antenna.
- The value may be tailored to provide specific electrical responses.
- The RF ground connections should be kept as short as possible and connected to directly to a good RF ground for best performance.

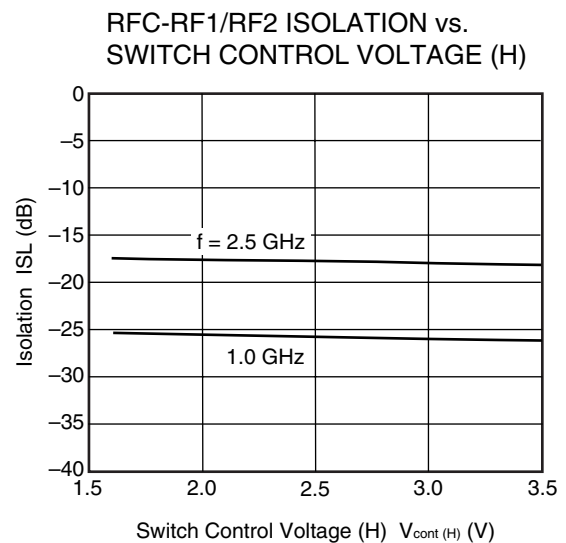
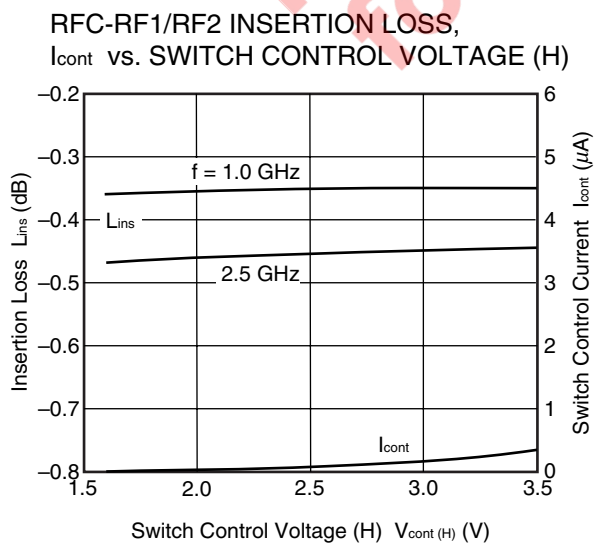
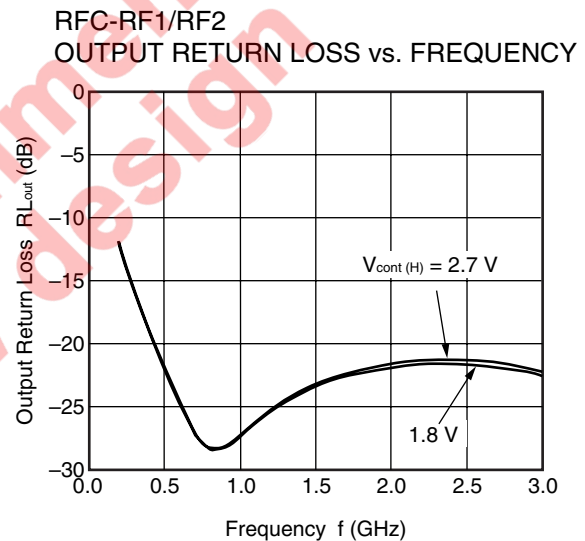
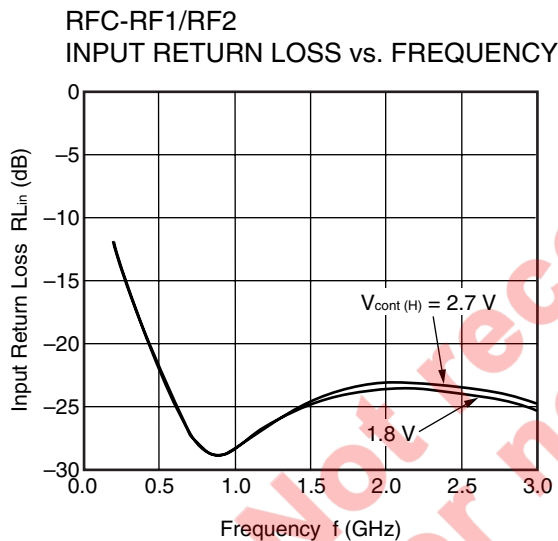
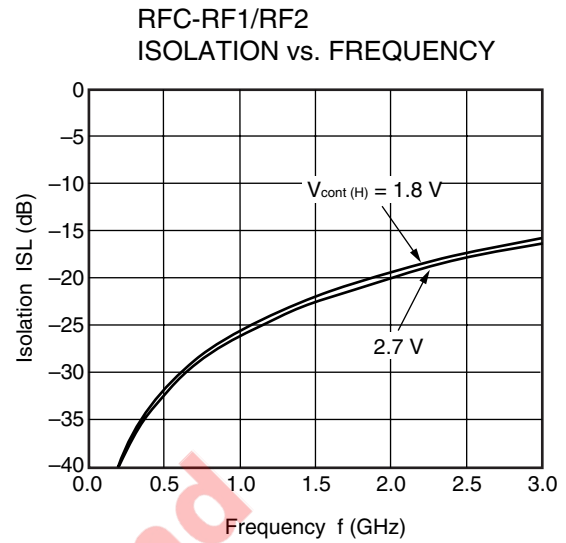
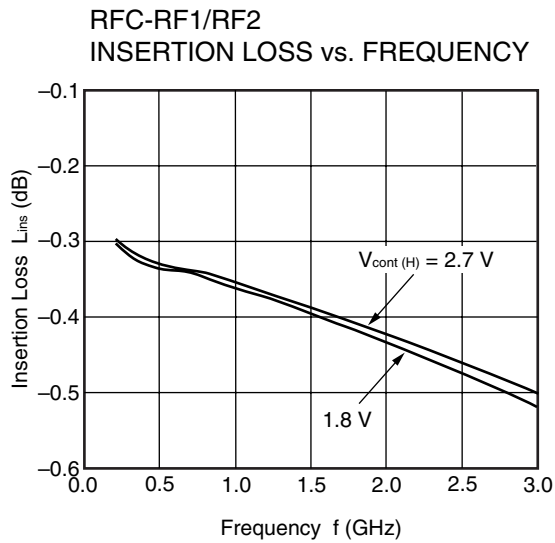
ILLUSTRATION OF THE TEST CIRCUIT ASSEMBLED ON EVALUATION BOARD



USING THE NEC EVALUATION BOARD

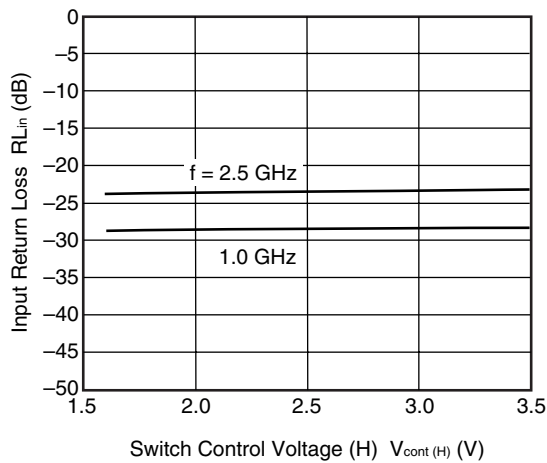
Symbol	Test Conditions	Values
C1	f = 0.01 to 0.05 GHz	10 000 pF
	f = 0.05 to 0.5 GHz	1 000 pF
	f = 0.5 to 3.0 GHz	56 pF
C2		1 000 pF

TYPICAL CHARACTERISTICS ($T_A = +25^\circ\text{C}$, DC blocking capacitors = 56 pF, unless otherwise specified)

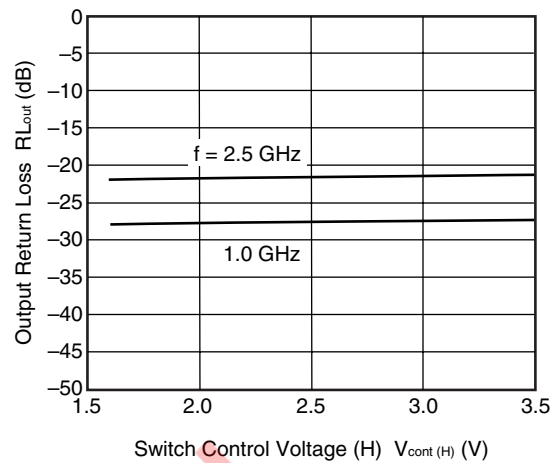


Remark The graphs indicate nominal characteristics.

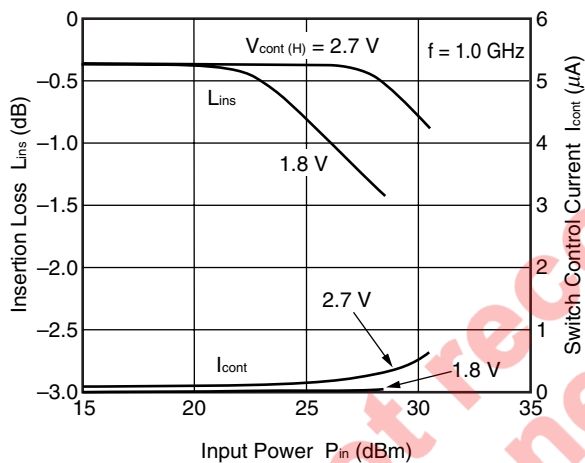
RFC-RF1/RF2 INPUT RETURN LOSS vs. SWITCH CONTROL VOLTAGE (H)



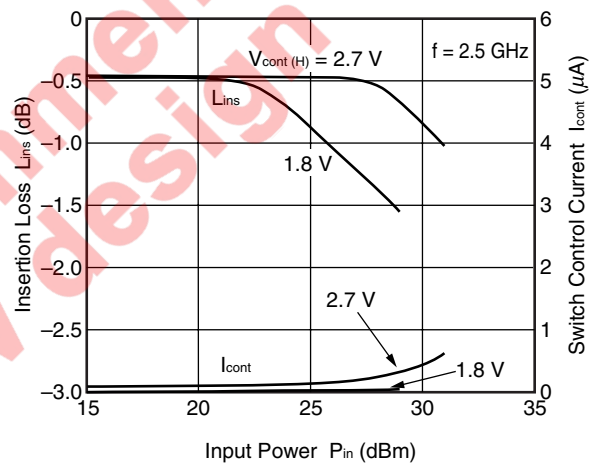
RFC-RF1/RF2 OUTPUT RETURN LOSS vs. SWITCH CONTROL VOLTAGE (H)



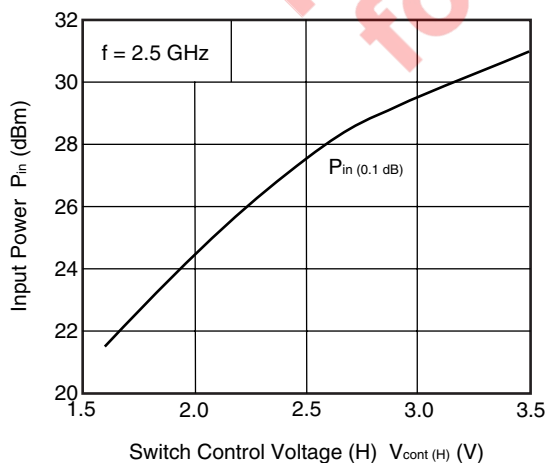
RFC-RF1/RF2 INSERTION LOSS, I_{cont} vs. INPUT POWER



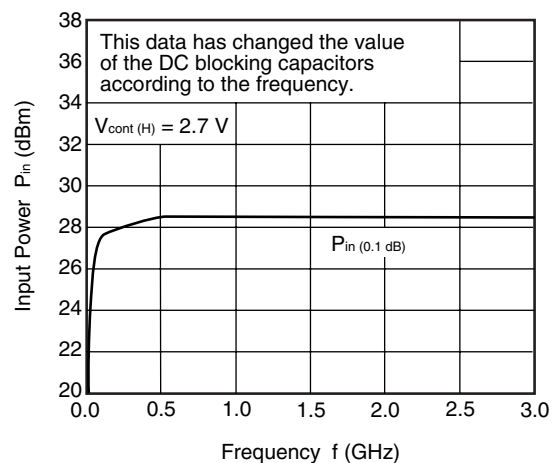
RFC-RF1/RF2 INSERTION LOSS, I_{cont} vs. INPUT POWER



RFC-RF1/RF2 INPUT POWER vs. SWITCH CONTROL VOLTAGE (H)



RFC-RF1/RF2 INPUT POWER vs. FREQUENCY

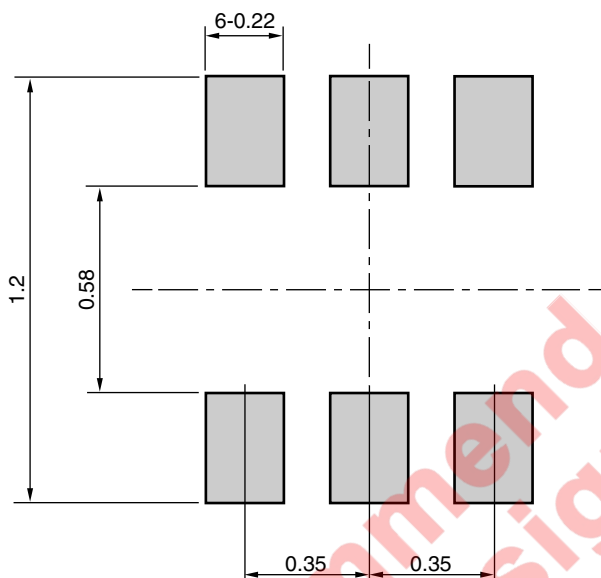


Remark The graphs indicate nominal characteristics.

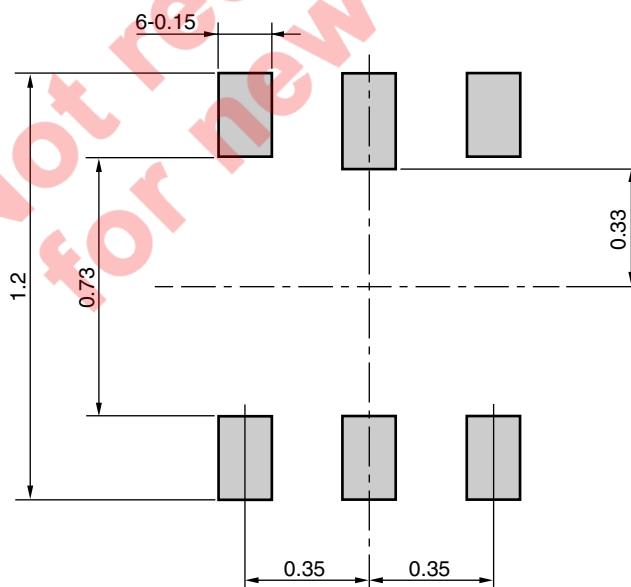
MOUNTING PAD AND SOLDER MASK LAYOUT DIMENSIONS

6-PIN PLASTIC TSSOP (T6R) (UNIT: mm)

MOUNTING PAD



SOLDER MASK

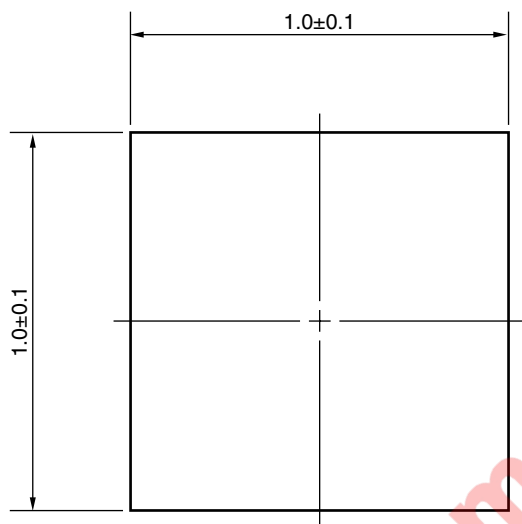


Remark The mounting pad and solder mask layouts in this document is for reference only.
When designing PCB, please consider workability of mounting, solder joint reliability, prevention of solder bridge and so on, in order to optimize the design.

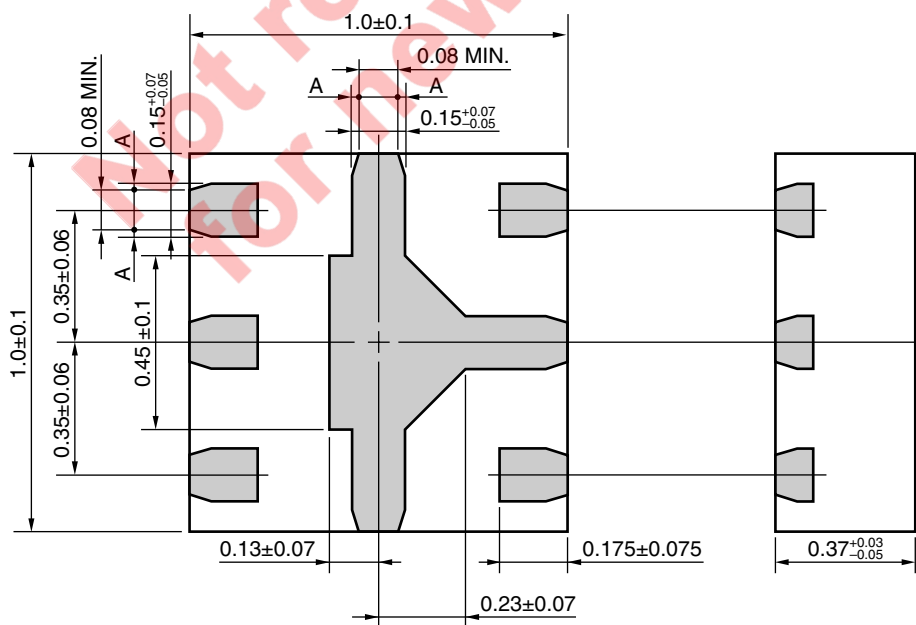
PACKAGE DIMENSIONS

6-PIN PLASTIC TSSOP (T6R) (UNIT: mm)

(Top View)



(Bottom View)



Remark A > 0

RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) : 260°C or below Time at peak temperature : 10 seconds or less Time at temperature of 220°C or higher : 60 seconds or less Preheating time at 120 to 180°C : 120±30 seconds Maximum number of reflow processes : 3 times Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	IR260
Partial Heating	Peak temperature (terminal temperature) : 350°C or below Soldering time (per side of device) : 3 seconds or less Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	HS350

Caution Do not use different soldering methods together (except for partial heating).

Not recommended
for new design

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M8E 02.11-1

Caution

GaAs Products

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
 1. Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or in any way allow it to enter the mouth.

Not recommend
for new design

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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