

# $\mu$ PC358MF-DAA

R03DS0005EJ0100

Rev.1.00

Aug 04, 2010

## Bipolar Analog Integrated Circuit

### Description

The  $\mu$  PC358MF-DAA is dual operational amplifier which is designed to operate for a single power supply. It includes features of low-voltage operation, a common-mode input voltage that range from  $V^-$  (GND) level, an output from a  $V^-$  (GND) level that is determined by the output stage of class C push-pull circuit and a  $50 \mu\text{A}$  (TYP.) constant current, and a low current consumption. In addition, this can operate at both positive and negative power supply and it can be extensively used in various amplifier circuits.

This package becomes smaller than  $\mu$  PC358G2 package in existence because the package adopts the narrow body SOP that is generally used abroad.

### Features

- The package is compliant with a JEDEC standard (MS-012).
- Thermal resistance was improved more than 30% from existing  $\mu$  PC358G2 by adopting copper-based lead material. ( $R_{\text{th(j-a)}} = 156^\circ\text{C/W}$ )
- Wider Operating Ambient Temperature range than  $\mu$  PC358G2  
 —  $\mu$  PC358MF-DAA ( $T_A = -40$  to  $+85^\circ\text{C}$ ),  $\mu$  PC358G2 ( $T_A = -20$  to  $+80^\circ\text{C}$ )
- Input Offset Voltage  $\pm 2$  mV (TYP.)
- Input Bias Current  $14$  nA (TYP.)
- Internal frequency compensation
- Output short-circuit protection
- Large Signal Voltage Gain  $100000$  (TYP.)

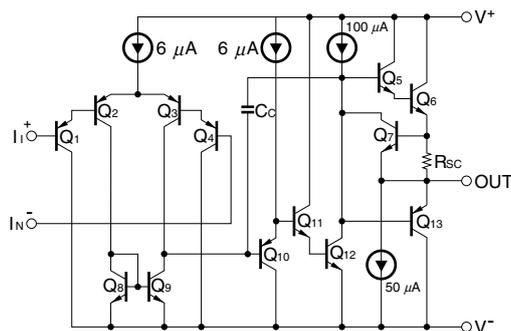
### Ordering Information

Part No.	Package	Package Type
$\mu$ PC358MF-DAA-E1-AT <sup>*1</sup>	8-pin plastic SOP (3.9 × 4.9)	<ul style="list-style-type: none"> <li>• 12 mm wide embossed taping</li> <li>• Pin 1 on draw-out side</li> <li>• 2500 p/reel</li> </ul>
$\mu$ PC358MF-DAA-E2-AT <sup>*1</sup>	8-pin plastic SOP (3.9 × 4.9)	<ul style="list-style-type: none"> <li>• 12 mm wide embossed taping</li> <li>• Pin 1 at take-up side</li> <li>• 2500 p/reel</li> </ul>

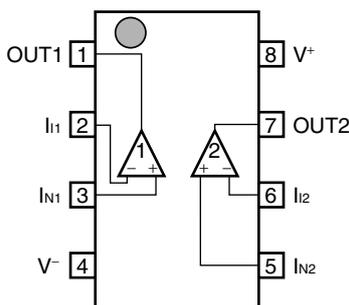
Note: \*1.Pb-free (This product does not contain Pb in the external electrode and other parts.)

**CAUTION** Do not use the products in applications such as the transportation equipment (a car, a train, a ship, etc.) where "Special quality grade" is required, because the products are placed in a quality grade "standard" to be required at general devices.

### Equivalent Circuit (1/2 Circuit)



### Pin Configuration (Marking side)



### Absolute Maximum Ratings (TA = 25°C)

Parameter	Symbol	Ratings	Unit
Voltage between V <sup>+</sup> and V <sup>-</sup> *1	V <sup>+</sup> -V <sup>-</sup>	-0.3 to +32	V
Differential Input Voltage	V <sub>ID</sub>	±32	V
Input Voltage *2	V <sub>I</sub>	V <sup>-</sup> - 0.3 to V <sup>-</sup> + 32	V
Output Applied Voltage *3	V <sub>O</sub>	V <sup>-</sup> - 0.3 to V <sup>+</sup> + 0.3	V
Total Power Dissipation *4	P <sub>T</sub>	440	mW
Output Short Circuit Duration (vs. GND) *5	t <sub>s</sub>	Indefinite	s
Operating Ambient Temperature	T <sub>A</sub>	-40 to +85	°C
Storage Temperature	T <sub>stg</sub>	-55 to +125	°C

Notes: \*1.Note that reverse connections of the power supply may damage ICs.

\*2.The input voltage is allowed to input without damage or destruction independent of the magnitude of V<sup>+</sup>.

Either input signal is not allowed to go negative by more than 0.3 V. In addition, the input voltage that operates normally as an operational amplifier is within the Common Mode Input Voltage range of an electrical characteristic.

\*3.A range where input voltage can be applied to an output pin externally with no deterioration or damage to the feature (characteristic). The input voltage can be applied regardless of the electric supply voltage. This specification which includes the transition state such as electric power ON/OFF must be kept.

\*4.This is the value in T<sub>A</sub> ≤ 56°C of when the glass epoxy substrate (size: 100 mm x 100 mm, thickness: 1 mm, 15% of the substrate area where only one side is copper foiled is filling wired) is mounted. Derate at -6.4 mW/°C when T<sub>A</sub> > 56°C. In the condition same as the above, Junction - ambient thermal resistance R<sub>th(J-A)</sub> = 156°C/W.

\*5.Only as for V<sup>+</sup> ≤ 15V and any 1 channel. Please use the product within the derating condition or Total Power Dissipation, which are showed in Note 4.

### Recommended Operating Conditions

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Power Supply Voltage (Split)	$V^{\pm}$	$\pm 1.5$		$\pm 15$	V
Power Supply Voltage ( $V^- = \text{GND}$ )	$V^+$	+3		+30	V

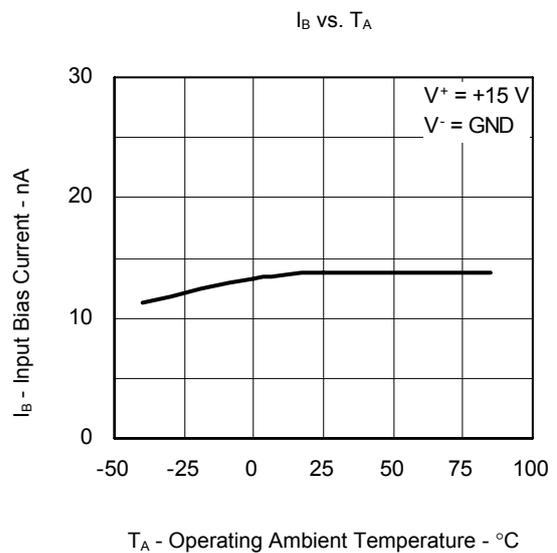
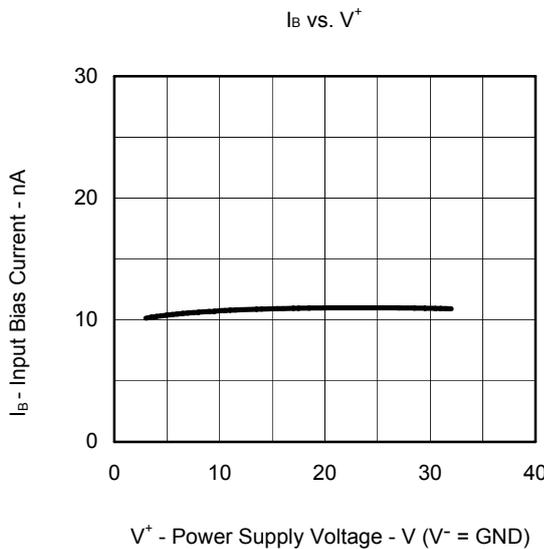
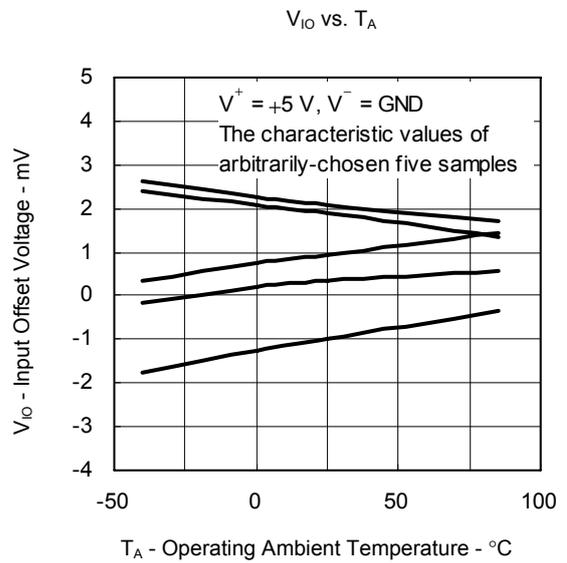
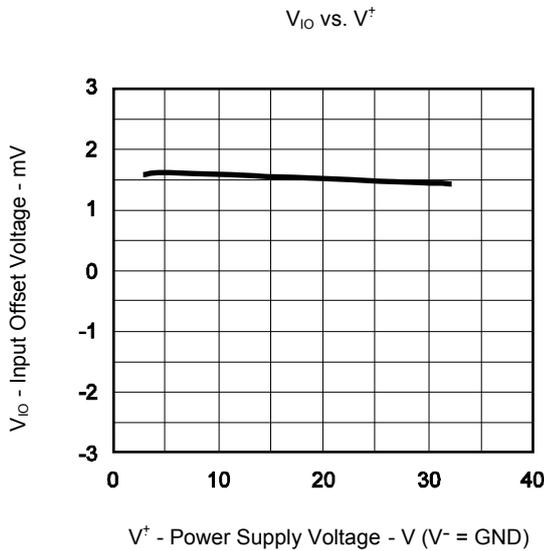
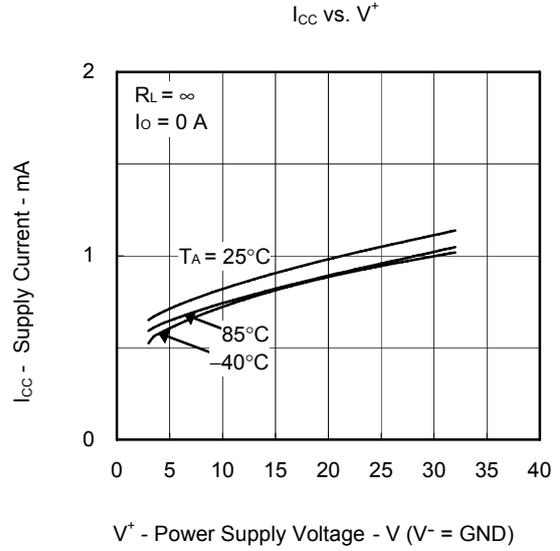
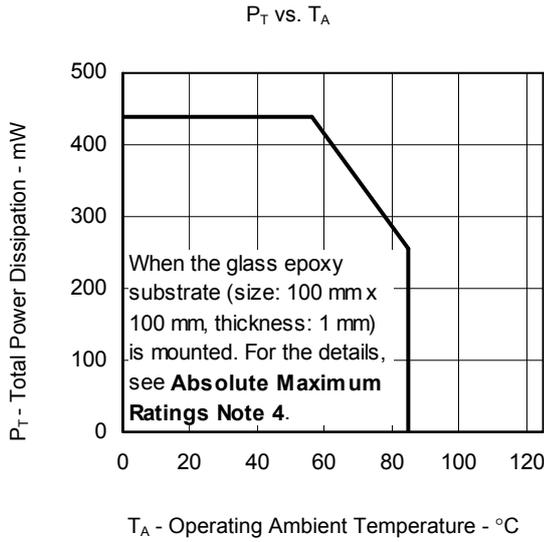
### Electrical Characteristics ( $T_A = 25^{\circ}\text{C}$ , $V^+ = +5\text{ V}$ , $V^- = \text{GND}$ )

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input Offset Voltage	$V_{IO}$	$R_S = 0\ \Omega$		$\pm 2$	$\pm 7$	mV
Input Offset Current	$I_{IO}$			$\pm 5$	$\pm 50$	nA
Input Bias Current *1	$I_B$			14	250	nA
Large Signal Voltage Gain	$A_V$	$R_L \geq 2\ \text{k}\Omega$	25000	100000		
Circuit Current *2	$I_{CC}$	$R_L = \infty$ , $I_O = 0\ \text{A}$		0.7	1.2	mA
Common Mode Rejection Ratio	CMR		65	70		dB
Supply Voltage Rejection Ratio	SVR		65	100		dB
Output Voltage Swing	$V_O$	$R_L = 2\ \text{k}\Omega$ (Connect to GND)	0		$V^+ - 1.5$	V
Common Mode Input Voltage Range	$V_{ICM}$		0		$V^+ - 1.5$	V
Output Source Current	$I_{O\ \text{SOURCE}}$	$V_{IN(+)} = +1\text{V}$ , $V_{IN(-)} = 0\ \text{V}$	20	40		mA
Output Sink Current	$I_{O\ \text{SINK1}}$	$V_{IN(-)} = +1\ \text{V}$ , $V_{IN(+)} = 0\ \text{V}$	10	20		mA
	$I_{O\ \text{SINK2}}$	$V_{IN(-)} = +1\ \text{V}$ , $V_{IN(+)} = 0\ \text{V}$ , $V_O = 200\ \text{mV}$	12	50		$\mu\text{A}$
Channel Separation		$f = 1\ \text{to}\ 20\ \text{kHz}$		120		dB

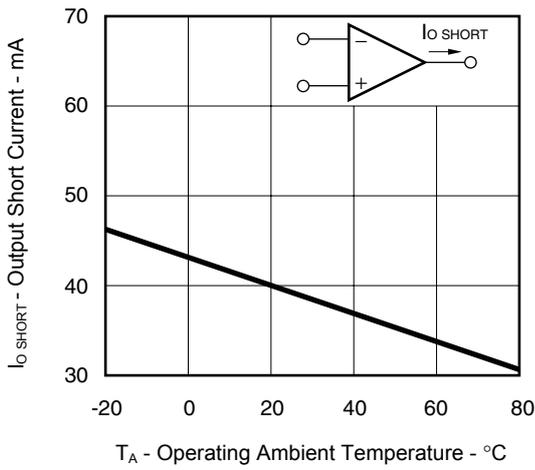
Notes: \*1. The input bias current flows in the direction where the IC flows out because the first stage is configured with a PNP transistor.

\*2. This is a current that flows in the internal circuit. This current will flow irrespective of the channel used.

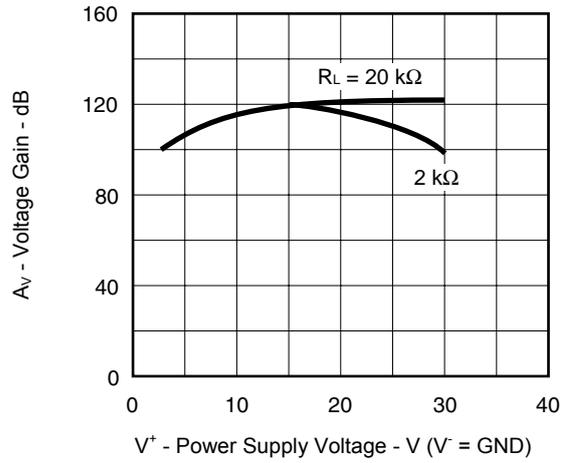
Typical Performance Characteristics (T<sub>A</sub> = 25°C, TYP.) (Reference value)



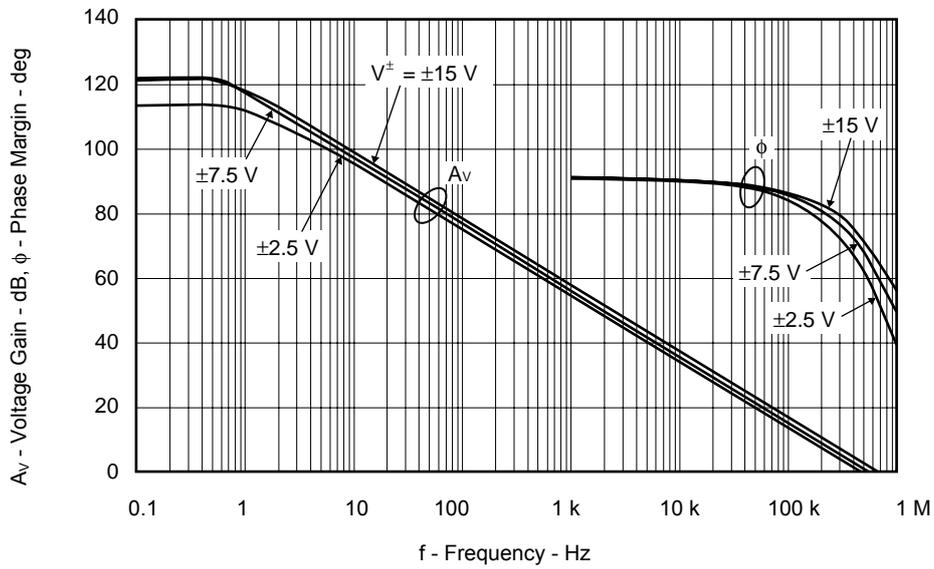
$I_{O\text{ SHORT}} \text{ vs. } T_A$



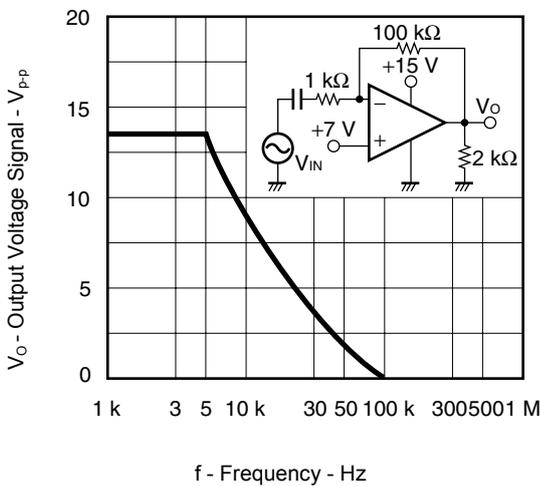
$A_v \text{ vs. } V^+$



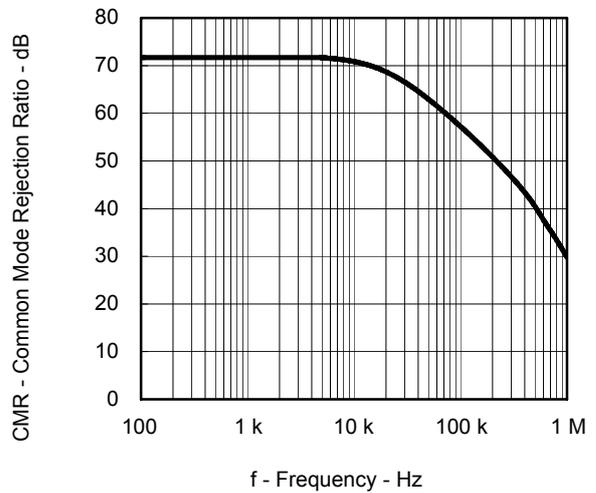
$A_v, \phi \text{ vs. } f$



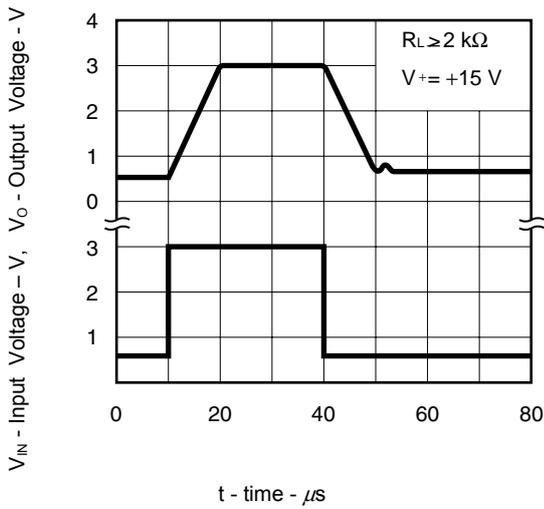
$V_o \text{ vs. } f$



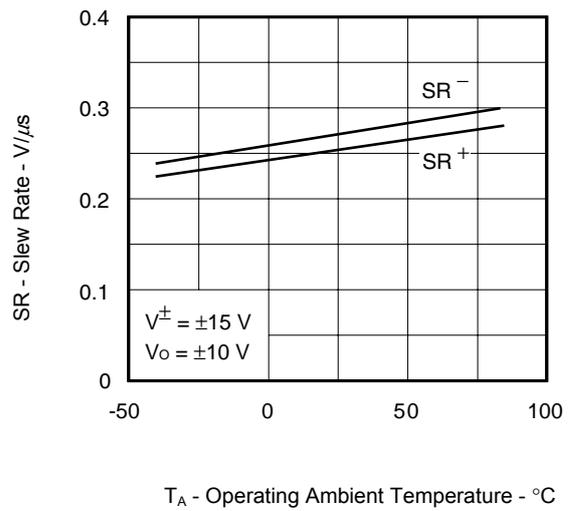
CMR vs. f



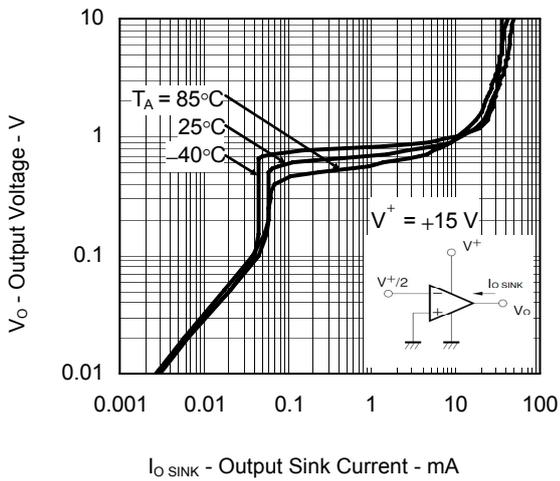
Pulse Response



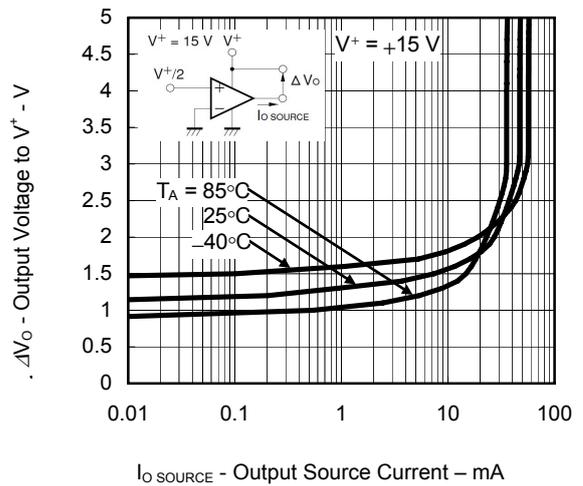
SR -  $T_A$



$V_O$  vs.  $I_{O \text{ SINK}}$

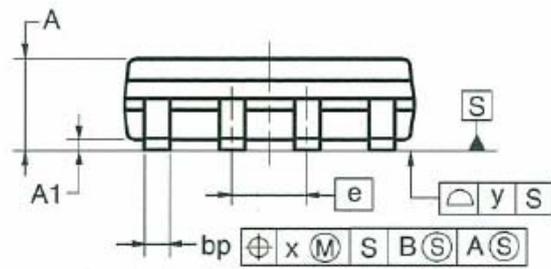
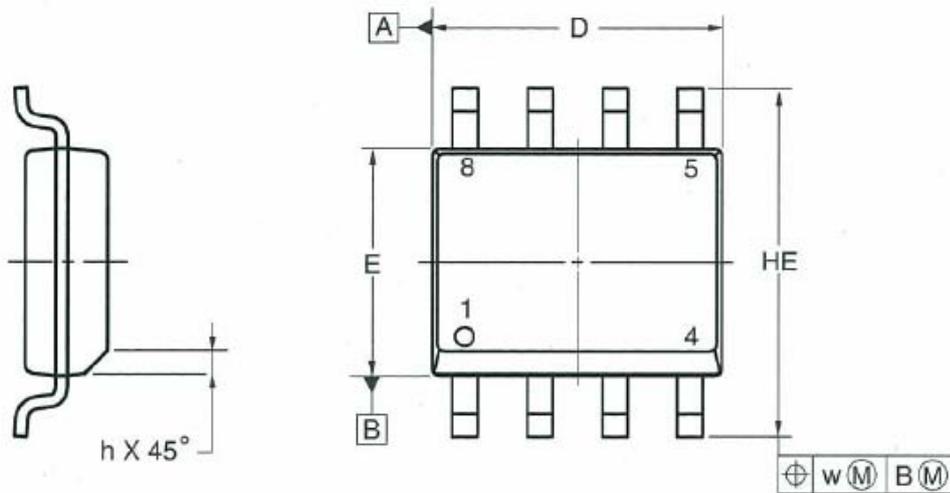


$\Delta V_O$  vs.  $I_{O \text{ SOURCE}}$

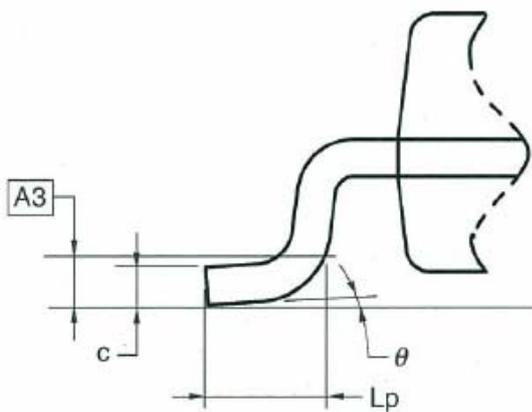


Package Drawings

8-pin Plastic SOP (3.9 × 4.9)



detail of lead end



(UNIT:mm)

ITEM	DIMENSIONS
D	4.80 to 5.00
E	3.80 to 4.00
HE	5.80 to 6.20
e	1.27
bp	0.35 to 0.49
A	1.35 to 1.75
A1	0.10 to 0.25
A3	0.25
c	0.19 to 0.25
Lp	0.40 to 1.25
h	0.25 to 0.50
w	0.25
x	0.25
y	0.10
θ	0° to 7°

## Recommended Soldering Conditions

The μPC358MF-DAA should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact our sales representative. For technical information, see the following website.

**Semiconductor Device Mount Manual (<http://www2.renesas.com/pkg/en/mount/index.html>)**

Recommended Soldering Conditions of Surface Mount Device

Process	Conditions	Symbol
Infrared ray reflow	Peak temperature: 260°C, Reflow time: 60 seconds or less (at 220°C or higher), Maximum number of reflow processes: 3 times.	IR60-00-3
Wave soldering	Solder temperature: 260°C or below, Flow time: 10 seconds or less, Maximum number of flow processes: 1 time, Pre-heating temperature: 120°C or below (Package surface temperature).	WS60-00-1
Partial heating method	Pin temperature: 350°C or below, Heat time: 3 seconds or less (Per each side of the device).	P350

**Caution** Apply only one kind of soldering condition to a device, except for “partial heating method”, or the device will be damaged by heat stress.

Remark Flux: Rosin flux with low chlorine (0.2 Wt% or below) recommended.

## Reference Documents

- Quality Grades on NEC Semiconductor Device C11531E
- Semiconductor Device Mount Manual <http://www2.renesas.com/pkg/en/mount/index.html>
- Review of Quality and Reliability Handbook C12769E
- NEC Semiconductor Device Reliability/Quality Control System C10983E

<b>Revision History</b>	<i>μ</i> PC358MF-DAA
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Rev.	Date	Description	
		Page	Summary
1.00	Aug 04, 2010	-	First Edition issued

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