

RNA62782LP

R03DS0054EJ0202

CMOS System Reset IC

Rev.2.02

Mar 01, 2013

Description

RNA62782 is microcomputer and system reset signal to be generated.

Delay time of reset is released at the internal circuit fixed to 100 ms, the external components unnecessary.

Features

- Reset detection voltage: 4.0 V \pm 4.0%
- Built-in long delay time: 100 ms
- Circuit current: 10 μ A
- Open drain output
- Wide supply voltage range: 1.8 V to 5.5 V

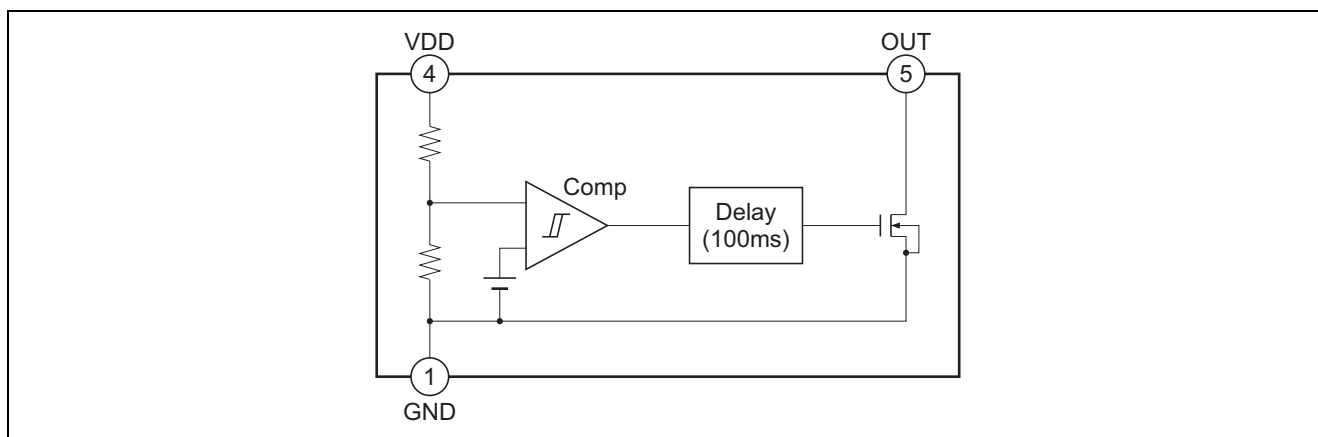
Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)	Surface Treatment
RNA62782LPH1	MPAK-5	PLSP0005ZB-A	LP	H (3,000 pcs/reel)	1 (Sn/Bi)

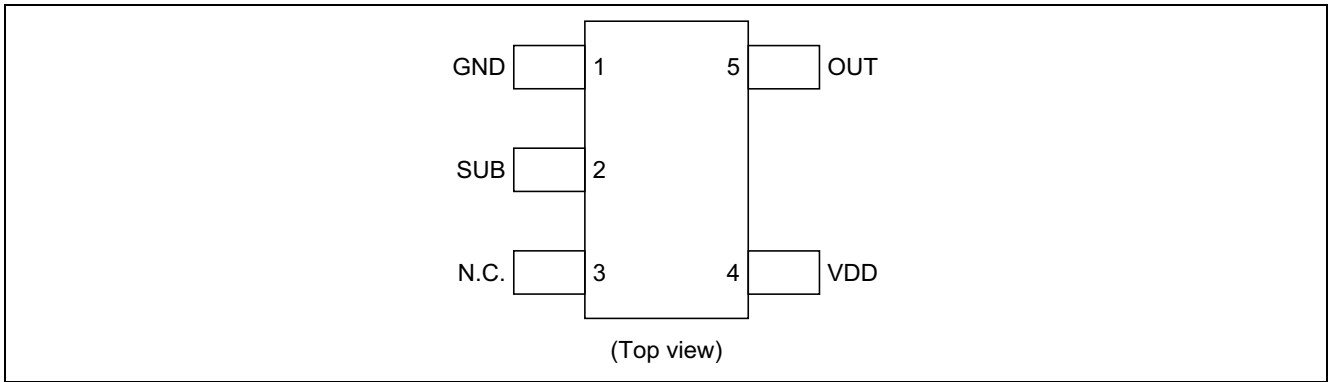
Application

- Power supply voltage monitoring for microprocessors
- Computers and notebook computers
- Digital still camera, digital video camera, and PDA
- Industrial equipment

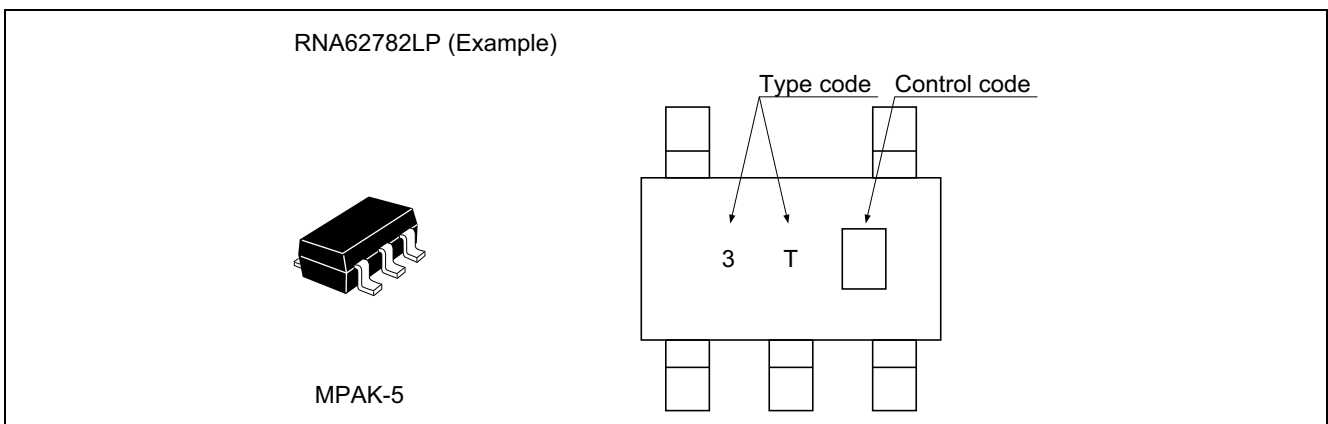
Block Diagram



Pin Arrangement



Mark Indication



Control code	Starting in January "A", "B", "C", "D", "E", "F", "G", "H", "J", "K", "L", "M"
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Pin Description

Pin No.	Pin Name	I/O	Function
1	GND	—	<ul style="list-style-type: none"> Ground
2	SUB	—	<ul style="list-style-type: none"> TAB is connected to inside. Same as 1 pin and ground.
3	N.C.	—	<ul style="list-style-type: none"> No Connection
4	VDD	—	<ul style="list-style-type: none"> Sourcing power-supply voltage. (To detect this voltage)
5	OUT	O	<ul style="list-style-type: none"> Power-supply voltage reaches the value below the detection voltage, low is output Open drain output Range pull-up resistance: 2.2(kΩ) to 100(kΩ)

Absolute Maximum Ratings

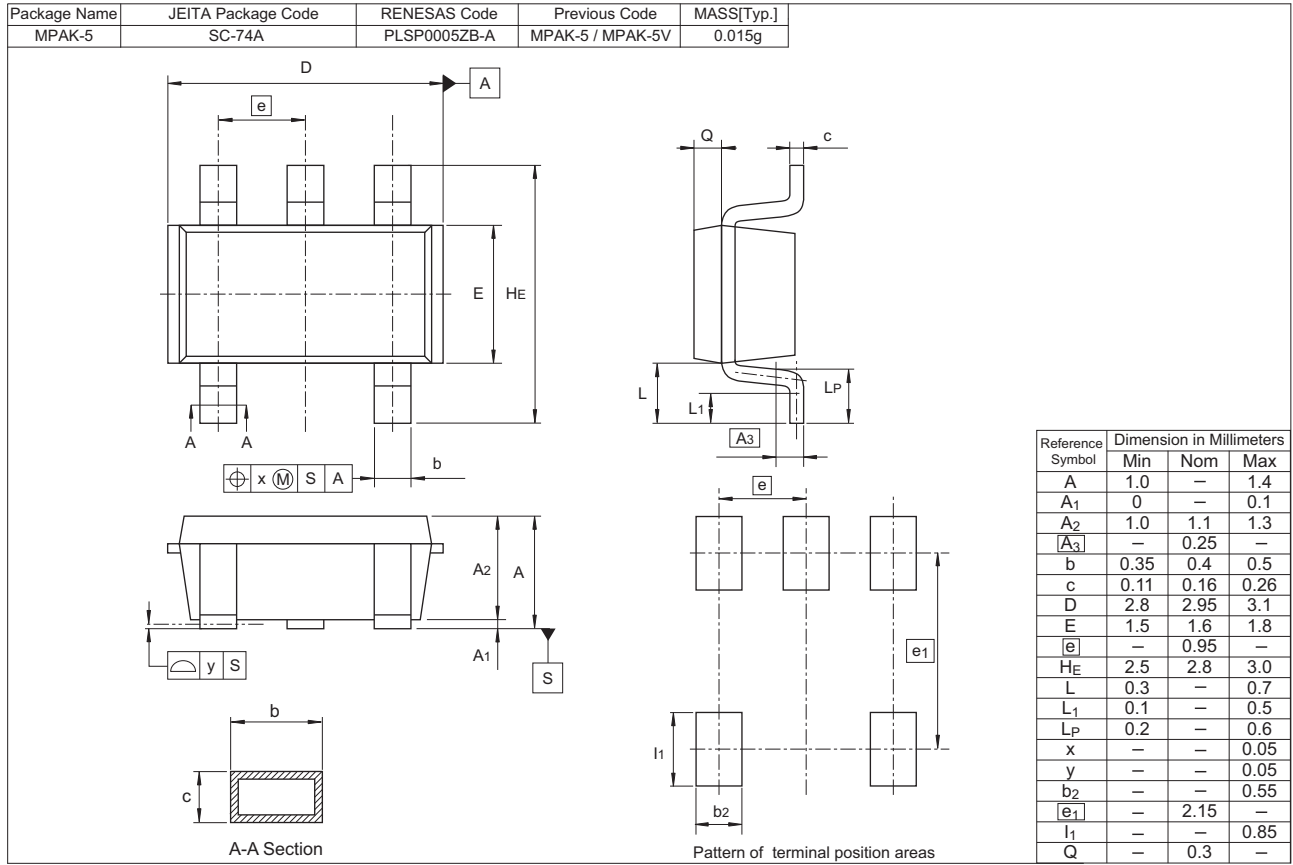
Item	Symbol	Ratings	Unit
Supply voltage	V_{DD}	6.5	V
Output voltage (open-drain type)	V_{OUT}	-0.3 to +6.5	V
Input voltage	V_{IN}	-0.3 to V_{DD}	V
Output current	I_{OUT}	6	mA
Power dissipation	P_d	120 ($T_a = 25^\circ\text{C}$)	mW
Operating temperature	T_{opr}	-30 to +85	$^\circ\text{C}$
Storage temperature	T_{stg}	-40 to +125	$^\circ\text{C}$

Electrical Characteristics

($V_{DD} = 5.0\text{ V}$, $T_a = 25^\circ\text{C}$, unless otherwise noted)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Threshold operating voltage	V_{OUL}	—	—	0.9	V	$R_L = 2.2\text{ (k}\Omega\text{)}, V_{OUT} < 0.4\text{ V}$
Circuit current	I_{DD}	—	10	20	μA	$V_{DD} = 5.0\text{ V}, V_{OUT} = \text{Hi (}V_{DD}\text{)}$
Detecting voltage	V_S	3.84	4.00	4.16	V	
Detecting voltage temperature coefficient	$V_S/\Delta T$	—	± 100	—	ppm/ $^\circ\text{C}$	
Hysteresis voltage	V_{HYS}	50	80	110	mV	
Output low voltage	V_{OL}	—	0.2	0.4	V	$I_{OUT} = 4\text{ mA}, V_{DD} = 3.5\text{ V}$
Output leakage current	I_{LK}	—	—	30	nA	$V_{OUT} = V_{DD} = 5.0\text{ V}$
Delay time	td	60	100	140	ms	

Package Dimensions



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Renesas Electronics America Inc.
2880 Scott Boulevard Santa Clara, CA 95050-2554, U.S.A.
Tel: +1-408-588-6000, Fax: +1-408-588-6130

Renesas Electronics Canada Limited
1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada
Tel: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: +44-1628-651-700, Fax: +44-1628-651-804

Renesas Electronics Europe GmbH
Arcadiastrasse 10, 40472 Düsseldorf, Germany
Tel: +49-211-65030, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
7th Floor, Quantum Plaza, No.27 Zhichunlu Haidian District, Beijing 100083, P.R.China
Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 204, 205, AZIA Center, No.1233 Lujiazui Ring Rd., Pudong District, Shanghai 200120, China
Tel: +86-21-5877-1818, Fax: +86-21-6887-7858 / -7898

Renesas Electronics Hong Kong Limited
Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2886-9318, Fax: +852 2886-9022/9044

Renesas Electronics Taiwan Co., Ltd.
13F, No. 363, Fu Shing North Road, Taipei, Taiwan
Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd.
80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre Singapore 339949
Tel: +65-6213-0200, Fax: +65-6213-0300

Renesas Electronics Malaysia Sdn.Bhd.
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: +60-3-7955-3390, Fax: +60-3-7955-9510

Renesas Electronics Korea Co., Ltd.
11F., Samik Laviel' or Bldg., 720-2 Yeoksam-Dong, Kangnam-Ku, Seoul 135-080, Korea
Tel: +82-2-558-3737, Fax: +82-2-558-5141