BCR3AM-12B

600V - 3A - Triac

Low Power Use

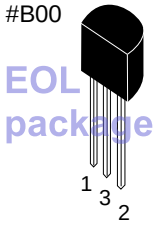
R07DS1562EJ0100
Rev.1.00
June 10, 2024

Features

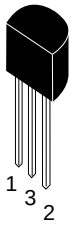
- $I_{T\,(RMS)}$: 3 A (non-continuous)
- V_{DRM}: 700 V (Tj = 125°C)
- I_{FGT}I, I_{RGT}I, I_{RGT} III: 15 mA
- Tj: 150 °C
- Planar Passivation Type
- RoHS Compliant
- Halogen-free (PRSS0003DJ-A)
- Completely Pb-free (PRSS0003DJ-A)

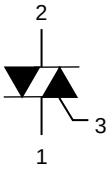
Outline

RENESAS Package code: PRSS0003EA-A (Package name: TO-92*)
Ordering code: #B00



PRSS0003DJ-A (Package name: TO-92)
#BD0





1. T_1 Terminal
2. T_2 Terminal
3. Gate Terminal

Application

Non-continuous motor control and other general purpose AC control applications.

Maximum Ratings

Parameter	Symbol	Voltage class	Unit	Conditions
		12		
Repetitive peak off-state voltage Note1	V_{DRM}	700	V	Tj = 125°C
		600	V	Tj = 150°C
Non-repetitive peak off-state voltage Note1	V_{DSM}	700	V	

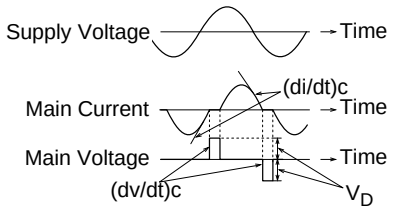
Notes: 1. Gate open.

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	$I_{T\,(RMS)}$	3	A	Commercial frequency, sine full wave 360° conduction, non-continuous
Surge on-state current	I_{TSM}	30	A	60 Hz sinewave 1 full cycle, peak value, non-repetitive
I^2t for fusing	I^2t	3.7	A^2s	Value corresponding to 1 cycle of half wave 60 Hz, surge on-state current
Peak gate power dissipation	P_{GM}	3	W	
Average gate power dissipation	$P_{G\,(AV)}$	0.3	W	
Peak gate voltage	V_{GM}	6	V	
Peak gate current	I_{GM}	0.5	A	
Junction Temperature	Tj	–40 to +150	°C	
Storage temperature	Tstg	–40 to +150	°C	

Electrical Characteristics

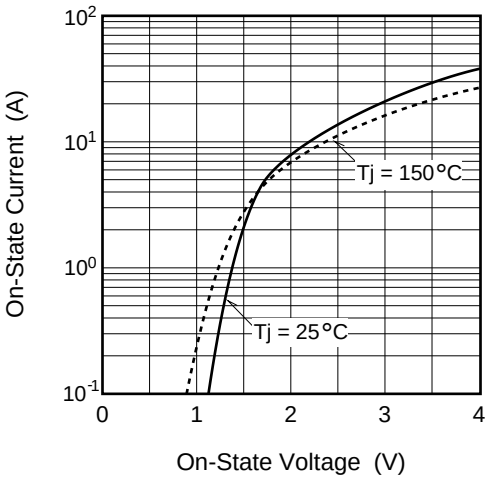
Parameter		Symbol	Min.	Typ.	Max.	Unit	Test conditions
Repetitive peak off-state current		I_{DRM}	—	—	2.0	mA	Tj = 150°C, V_{DRM} applied
On-state voltage		V_{TM}	—	—	1.7	V	Tc = 25°C, I_{TM} = 4.5 A, instantaneous measurement
Gate trigger voltage Note2	I	V_{FGTI}	—	—	1.5	V	Tj = 25°C, V_D = 6 V, R_L = 6 Ω, R_G = 330 Ω
	II	V_{RGTI}	—	—	1.5	V	
	III	V_{RGTIII}	—	—	1.5	V	
Gate trigger current Note2	I	I_{FGTI}	—	—	15	mA	Tj = 25°C, V_D = 6 V, R_L = 6 Ω, R_G = 330 Ω
	II	I_{RGTI}	—	—	15	mA	
	III	I_{RGTIII}	—	—	15	mA	
Gate non-trigger voltage		V_{GD}	0.2	—	—	V	Tj = 125°C, V_D = 1/2 V_{DRM}
			0.1	—	—	V	Tj = 150°C, V_D = 1/2 V_{DRM}
Thermal resistance		$R_{th\ (j-c)}$	—	—	50	°C/W	Junction to case Note3
Critical-rate of rise of off-state commutating voltage Note4		(dv/dt)c	5	—	—	V/μs	Tj = 125°C
			1	—	—	V/μs	Tj = 125°C

Notes: 2. Measurement using the gate trigger characteristics measurement circuit.
3. Case temperature is measured at the T_2 terminal 1.5 mm away from the molded case.
4. Test conditions of the critical-rate of rise of off-state commutating voltage is shown in the table below.

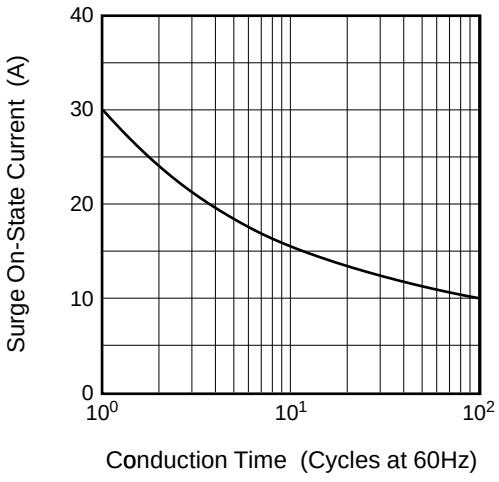
Test conditions	Commutating voltage and current waveforms (inductive load)
1. Junction temperature Tj = 125°C / 150°C 2. Rate of decay of on-state commutating current (di/dt)c = – 1.5 A/ms 3. Peak off-state voltage V_D = 400 V	

Performance Curves

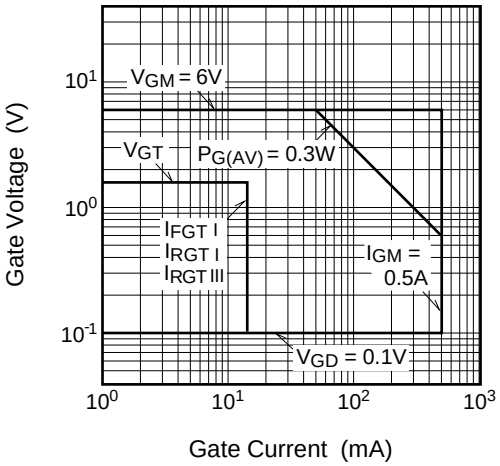
Maximum On-State Characteristics



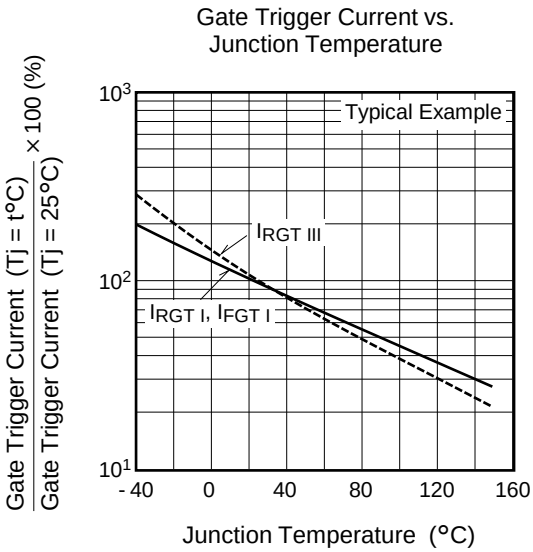
Rated Surge On-State Current



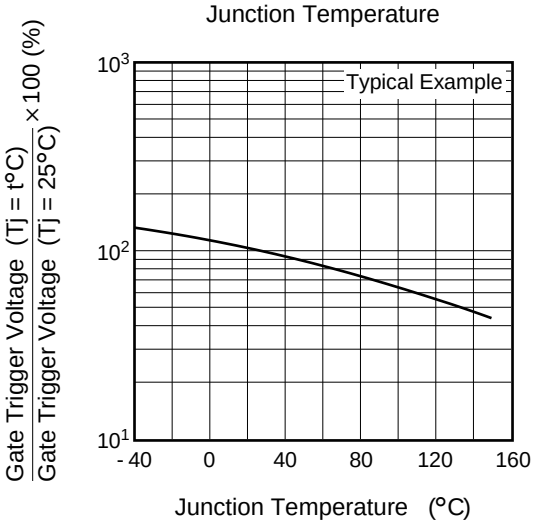
Gate Characteristics (I, II and III)



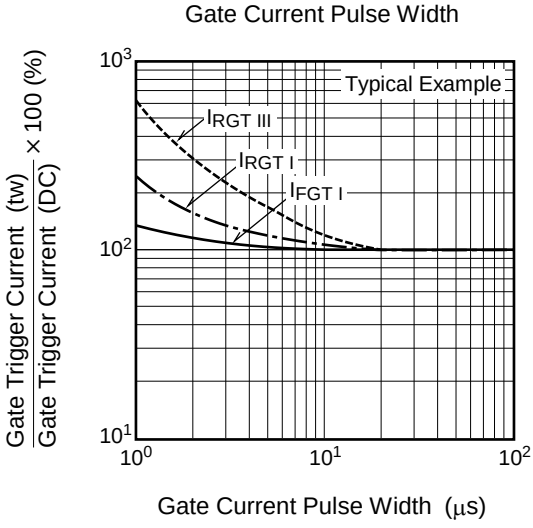
Gate Trigger Current vs. Junction Temperature



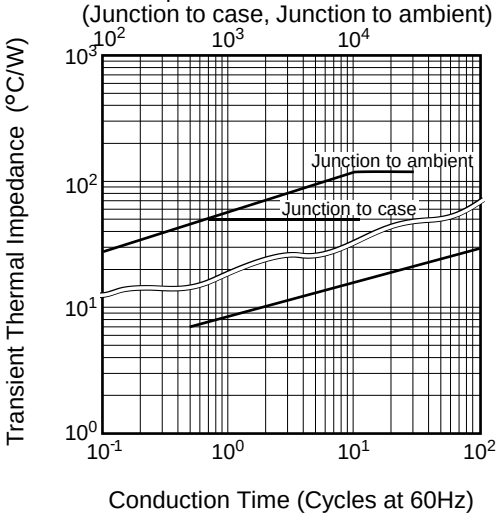
Gate Trigger Voltage vs. Junction Temperature



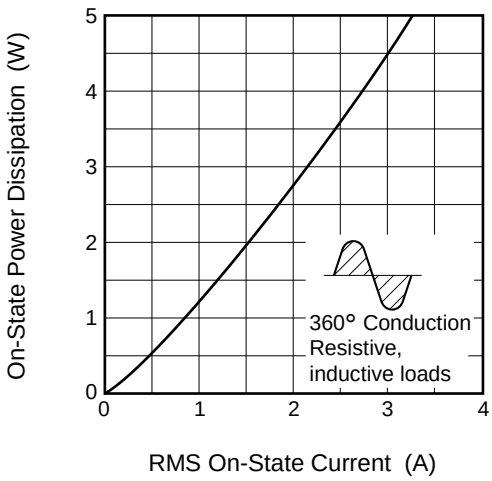
Gate Trigger Current vs. Gate Current Pulse Width



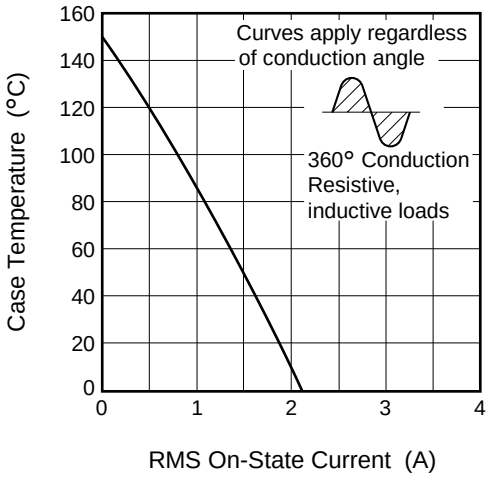
Maximum Transient Thermal Impedance Characteristics (Junction to case, Junction to ambient)



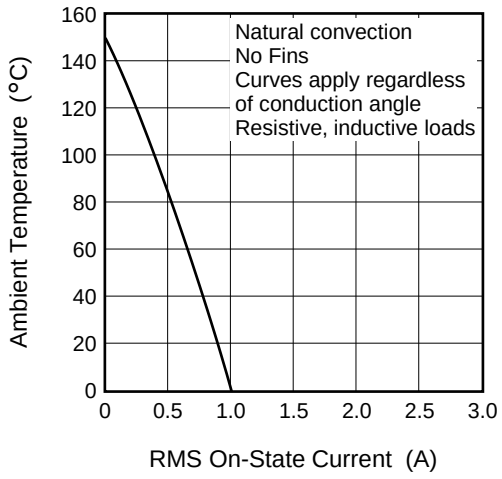
Maximum On-State Power Dissipation



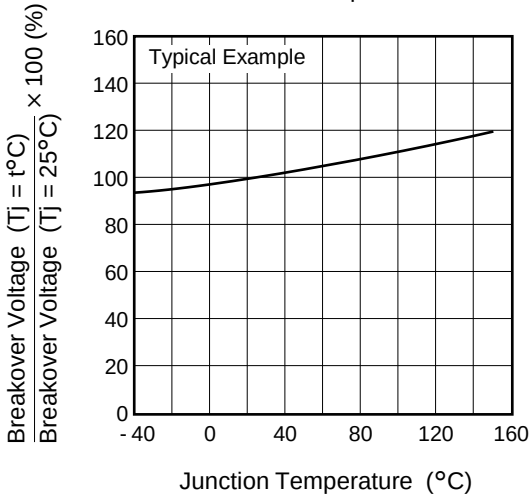
Allowable Case Temperature vs. RMS On-State Current



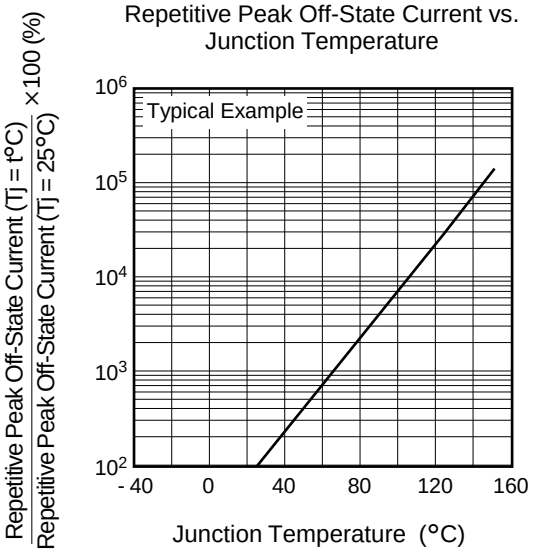
Allowable Ambient Temperature vs. RMS On-State Current



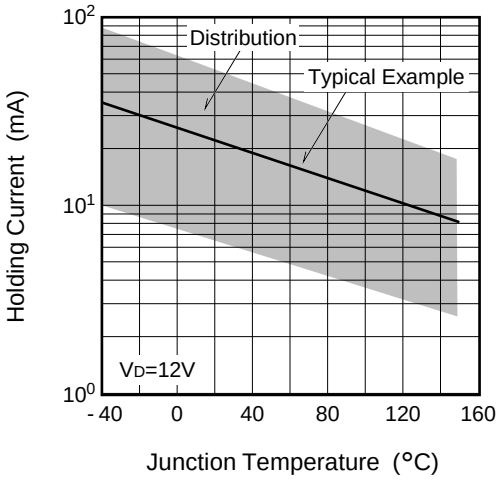
Breakover Voltage vs. Junction Temperature



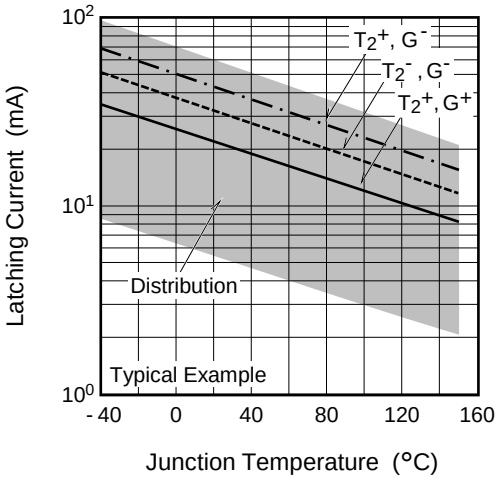
Repetitive Peak Off-State Current vs. Junction Temperature



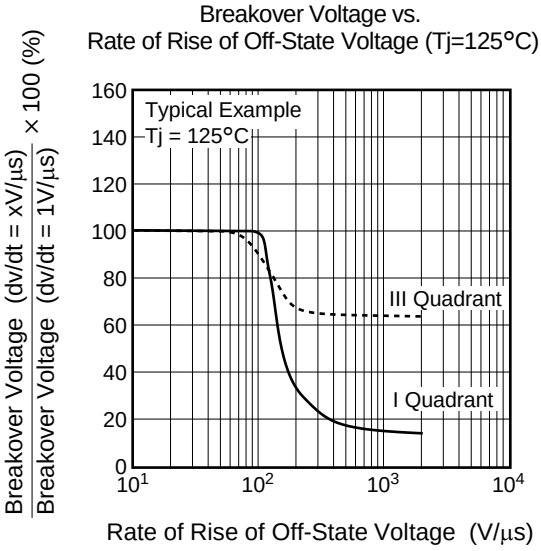
Holding Current vs.
Junction Temperature



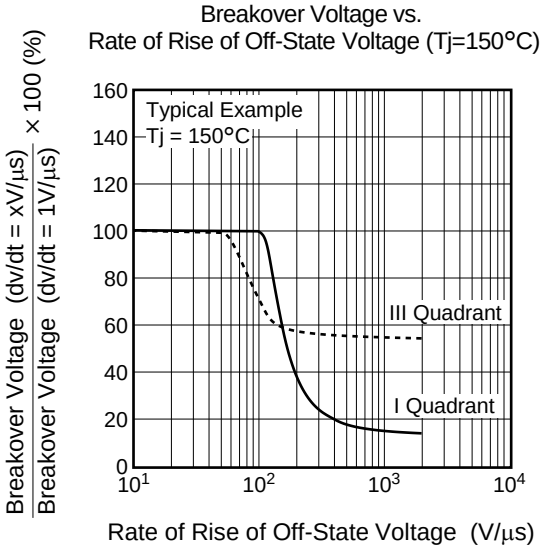
Latching Current vs.
Junction Temperature



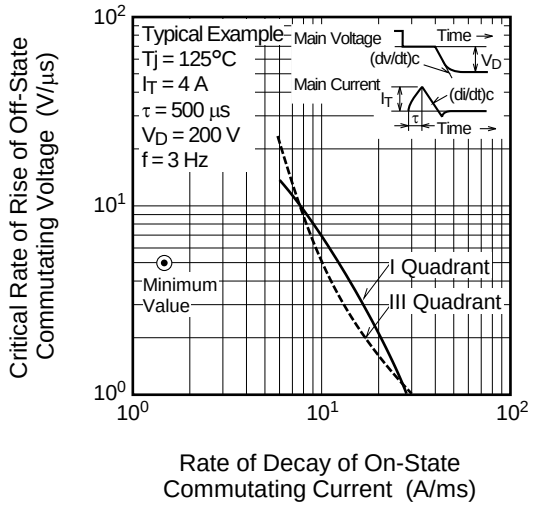
Breakover Voltage vs.
Rate of Rise of Off-State Voltage (Tj=125°C)



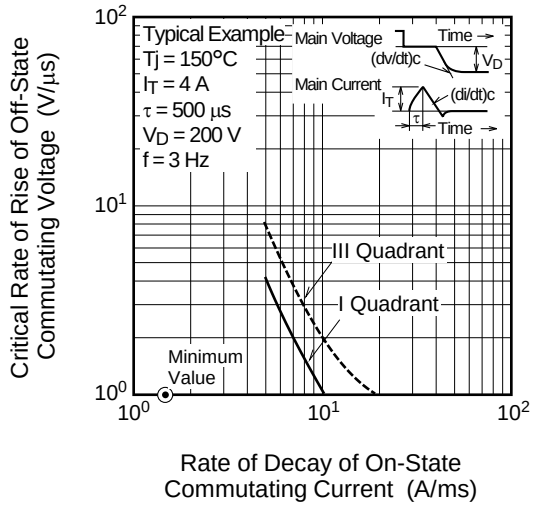
Breakover Voltage vs.
Rate of Rise of Off-State Voltage (Tj=150°C)



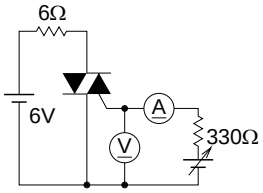
Commutation Characteristics (Tj=125°C)



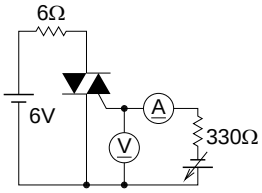
Commutation Characteristics (Tj=150°C)



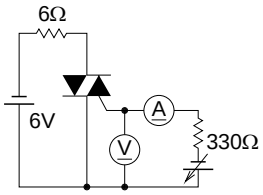
Gate Trigger Characteristics Test Circuits



Test Procedure I

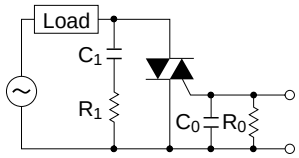


Test Procedure II



Test Procedure III

Recommended peripheral components for Triac



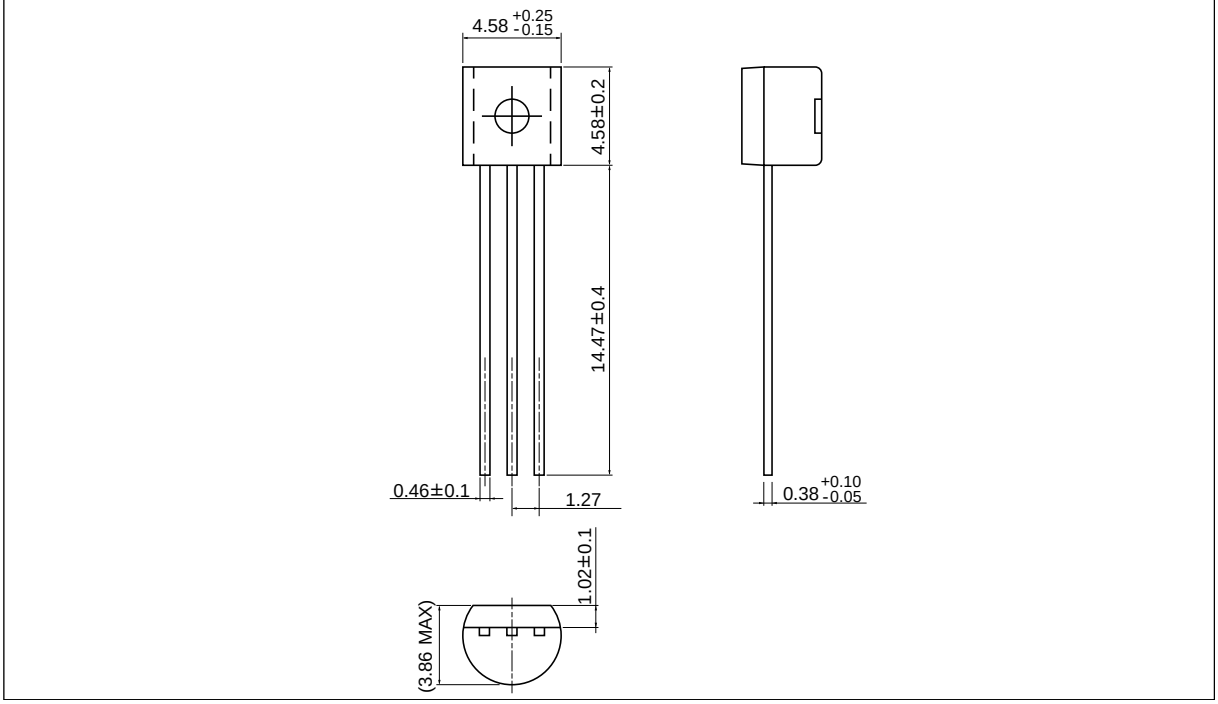
C_1 = 0.1 to 0.47 μF C_0 = 0.1 μF
R_1 = 47 to 100Ω R_0 = 100Ω

Package Dimensions

Ordering code: #BD0 <Active>

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
TO-92	SC-43A	PRSS0003DJ-A	TO-92	0.23 g

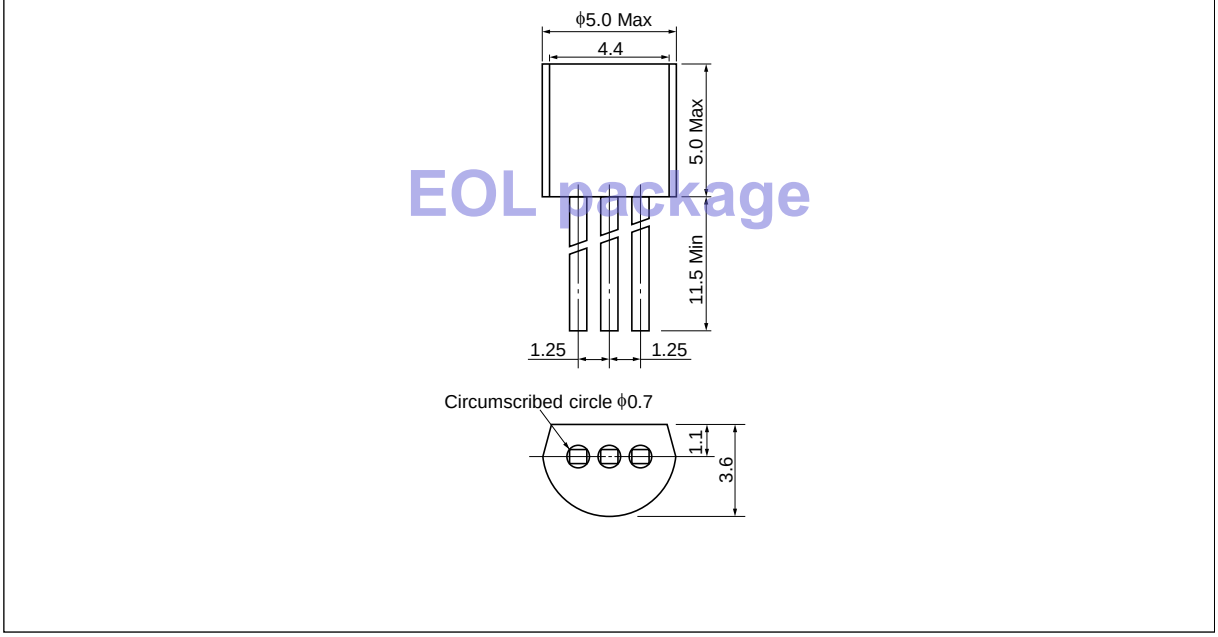
Unit: mm



Ordering code: #B00 <Obsolete>

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
TO-92*	SC-43A	PRSS0003EA-A	T920	0.23 g

Unit: mm



Ordering Information

Orderable Part Number	Package	Packing Note5	Quantity	Remark	Status
BCR3AM-12B#BD0	TO-92	Plastic Bag	1000 pcs.	Straight type	Active
BCR3AM-12B-A6#BD0	TO-92	Plastic Bag	1000 pcs.	A6 Lead form	
BCR3AM-12B#B00	TO-92*	Plastic Bag	500 pcs.	Straight type	Obsolete
BCR3AM-12B-A6#B00	TO-92*	Plastic Bag	500 pcs.	A6 Lead form	

Note: 5. Please confirm the specification about the shipping in detail.

Notice

1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation or any other use of the circuits, software, and information in the design of your product or system. Renesas Electronics disclaims any and all liability for any losses and damages incurred by you or third parties arising from the use of these circuits, software, or information.
2. Renesas Electronics hereby expressly disclaims any warranties against and liability for infringement or any other claims involving patents, copyrights, or other intellectual property rights of third parties, by or arising from the use of Renesas Electronics products or technical information described in this document, including but not limited to, the product data, drawings, charts, programs, algorithms, and application examples.
3. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
4. You shall be responsible for determining what licenses are required from any third parties, and obtaining such licenses for the lawful import, export, manufacture, sales, utilization, distribution or other disposal of any products incorporating Renesas Electronics products, if required.
5. You shall not alter, modify, copy, or reverse engineer any Renesas Electronics product, whether in whole or in part. Renesas Electronics disclaims any and all liability for any losses or damages incurred by you or third parties arising from such alteration, modification, copying or reverse engineering.
6. Renesas Electronics products are classified according to the following two quality grades: “Standard” and “High Quality”. The intended applications for each Renesas Electronics product depends on the product’s quality grade, as indicated below.
   "Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; industrial robots; etc.
   "High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control (traffic lights); large-scale communication equipment; key financial terminal systems; safety control equipment; etc.

   Unless expressly designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not intended or authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems; surgical implantations; etc.), or may cause serious property damage (space system; undersea repeaters; nuclear power control systems; aircraft control systems; key plant systems; military equipment; etc.). Renesas Electronics disclaims any and all liability for any damages or losses incurred by you or any third parties arising from the use of any Renesas Electronics product that is inconsistent with any Renesas Electronics data sheet, user’s manual or other Renesas Electronics document.
7. No semiconductor product is absolutely secure. Notwithstanding any security measures or features that may be implemented in Renesas Electronics hardware or software products, Renesas Electronics shall have absolutely no liability arising out of any vulnerability or security breach, including but not limited to any unauthorized access to or use of a Renesas Electronics product or a system that uses a Renesas Electronics product. RENESAS ELECTRONICS DOES NOT WARRANT OR GUARANTEE THAT RENESAS ELECTRONICS PRODUCTS, OR ANY SYSTEMS CREATED USING RENESAS ELECTRONICS PRODUCTS WILL BE INVULNERABLE OR FREE FROM CORRUPTION, ATTACK, VIRUSES, INTERFERENCE, HACKING, DATA LOSS OR THEFT, OR OTHER SECURITY INTRUSION (“Vulnerability Issues”). RENESAS ELECTRONICS DISCLAIMS ANY AND ALL RESPONSIBILITY OR LIABILITY ARISING FROM OR RELATED TO ANY VULNERABILITY ISSUES. FURTHERMORE, TO THE EXTENT PERMITTED BY APPLICABLE LAW, RENESAS ELECTRONICS DISCLAIMS ANY AND ALL WARRANTIES, EXPRESS OR IMPLIED, WITH RESPECT TO THIS DOCUMENT AND ANY RELATED OR ACCOMPANYING SOFTWARE OR HARDWARE, INCLUDING BUT NOT LIMITED TO THE IMPLIED WARRANTIES OF MERCHANTABILITY, OR FITNESS FOR A PARTICULAR PURPOSE.
8. When using Renesas Electronics products, refer to the latest product information (data sheets, user’s manuals, application notes, “General Notes for Handling and Using Semiconductor Devices” in the reliability handbook, etc.), and ensure that usage conditions are within the ranges specified by Renesas Electronics with respect to maximum ratings, operating power supply voltage range, heat dissipation characteristics, installation, etc. Renesas Electronics disclaims any and all liability for any malfunctions, failure or accident arising out of the use of Renesas Electronics products outside of such specified ranges.
9. Although Renesas Electronics endeavors to improve the quality and reliability of Renesas Electronics products, semiconductor products have specific characteristics, such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Unless designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not subject to radiation resistance design. You are responsible for implementing safety measures to guard against the possibility of bodily injury, injury or damage caused by fire, and/or danger to the public in the event of a failure or malfunction of Renesas Electronics products, such as safety design for hardware and software, including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult and impractical, you are responsible for evaluating the safety of the final products or systems manufactured by you.
10. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. You are responsible for carefully and sufficiently investigating applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive, and using Renesas Electronics products in compliance with all these applicable laws and regulations. Renesas Electronics disclaims any and all liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
11. Renesas Electronics products and technologies shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You shall comply with any applicable export control laws and regulations promulgated and administered by the governments of any countries asserting jurisdiction over the parties or transactions.
12. It is the responsibility of the buyer or distributor of Renesas Electronics products, or any other party who distributes, disposes of, or otherwise sells or transfers the product to a third party, to notify such third party in advance of the contents and conditions set forth in this document.
13. This document shall not be reprinted, reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics.
14. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products.

(Note1) “Renesas Electronics” as used in this document means Renesas Electronics Corporation and also includes its directly or indirectly controlled subsidiaries.
(Note2) “Renesas Electronics product(s)” means any product developed or manufactured by or for Renesas Electronics.

(Rev.5.0-1 October 2020)

Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,
Koto-ku, Tokyo 135-0061, Japan
www.renesas.com

Trademarks

Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners.

Contact information

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit:
www.renesas.com/contact/.