

Tundra Confidential

V1	1	2	3	4	5	6	7	8	9	10	11	12
A	VSS	SR_CLK	SR_DIN	PCIE_TX D_p	VSS	PCIE_RX D_p	VSS	PCIE_RE FCLK_n	VSS	VSSA_PL L	PCIE_RE XT	PCI_CLK O[4]
B	PCI_AD[0]	PCI_CLK O[1]	TEST_ON	PCIE_TX D_n	VDD_PCI E	PCIE_RX D_n	VDD_PCI E	PCIE_RE FCLK_p	PCIE_PE RSTn	VDDA_PL L	PCI_M66 EN	PCI_CLK
C	VSS	PCI_AD[1]	TEST_BI DR_CTL	VDD_PCI E	VSS	VDDA_P CIE	VSS	VDD_PCI E	GPIO[2]	PCI_INTC n	VIO_PCI	PCI_INTD n
D	PCI_AD[6]	VDD_PCI	PCI_AD[7]	SR_CSn	VSS	VDDA_P CIE	TEST_BC E	GPIO[1]	GPIO[3]	VSS	VDD_PCI	PCI_INTA n
E	PCI_AD[1 0]	PCI_AD[4]	PCI_AD[3]	SR_DOU T	VDD	VSS	VSS	VDD	GPIO[0]	PCI_GNT n[3]	VDD_PCI	PCI_RST n
F	PCI_AD[8]	PCI_AD[5]	PCI_CBE n[0]	PCI_AD[2]	VSS	VDD	VDD	VSS	PCI_INTB n	PCI_REQ n[0]	PCI_GNT n[2]	PCI_GNT n[1]
G	PCI_AD[1 1]	VDD_PCI	VSS	PCI_AD[9]	VSS	VDD	VDD	VSS	PCI_GNT n[0]	PCI_REQ n[2]	VDD_PCI	VSS
H	PCI_AD[1 3]	VDD_PCI	PCI_AD[1 2]	PCI_AD[1 4]	VDD	VSS	VSS	VDD	PCI_AD[2 8]	PCI_CLK O[0]	PCI_REQ n[3]	PCI_PME n
J	PCI_AD[1 5]	PCI_SER Rn	PCI_CBE n[1]	PCI_PER Rn	PCI_AD[1 6]	PCI_STO Pn	PCI_AD[1 7]	PCI_CLK O[3]	PCI_CLK O[2]	PCI_REQ n[1]	VDD_PCI	PCI_AD[3 0]
K	PCI_PAR	VDD_PCI	JTAG_TD O	JTAG_TC K	PCI_TRD Yn	PCI_AD[2 0]	PCI_AD[2 1]	PCI_AD[2 3]	JTAG_TR STn	VSS	PWRUP_ EN_ARB	PCI_AD[2 9]
L	PWRUP_ PLL_BYP ASS	VIO_PCI	PCI_LOC Kn	VDD_PCI	PCI_IRDY n	PCI_AD[1 8]	VDD_PCI	PCI_AD[2 6]	PCI_CBE n[3]	PCI_AD[2 5]	VDD_PCI	PCI_AD[3 1]
M	VSS	PCI_DEV SELn	JTAG_TD I	PCI_CBE n[2]	PCI_FRA MEn	PCI_AD[1 9]	VSS	PCI_AD[2 2]	VIO_PCI	PCI_AD[2 4]	PCI_AD[2 7]	JTAG_TM S

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Revision History

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