Renesas Synergy™ Platform

# Synergy MCU Kits Part Number Look-up Table

The orderable part numbers of the Synergy Kits listed in this document have changed. This document provides customers and users with a look-up table with the orderable part number information listing both the current and new part numbers for the Synergy MCU Kits, as shown in Table 1.

**Note: The new part number may be required to order the Synergy MCU kits through the distributors. The old part number will continue to be referenced throughout the existing marketing and engineering collateral associated with the Synergy MCU Kits.**

**Table 1. Synergy MCU Kits Part Number Look-Up**

| Kit Name | Current Part Number | New Part Number |
|---|---|---|
| DK-S124 | YSDKS124S20 | RTK7DKS124S00002BU |
| DK-S128 | YSDKS128E10 | RTK7DKS128S00001BU |
| DK-S7G2 | YSDKS7G2E40 | RTK7DKS7G2S00004BU |
| TB-S1JA | YSTBS1JAE10 | RTK7TBS1JAS00001BU |
| TB-S3A1 | YSTBS3A1E10 | RTK7TBS3A1S00001BU |
| TB-S3A3 | YSTBS3A3E10 | RTK7TBS3A3S00001BU |
| TB-S3A6 | YSTBS3A6E10 | RTK7TBS3A6S00001BU |
| TB-S5D3 | YSTBS5D3E10 | RTK7TBS5D3S00001BU |
| TB-S5D5 | YSTBS5D5E10 | RTK7TBS5D5S00001BU |
| SK-S7G2 | YSSKS7G2E30 | RTK7SKS7G2S00003BU |
| PK-S5D9 | YSPKS5D9E10 | RTK7PKS5D9S00001BU |
| PE-HMI1 | YSPEHMI1S20 | RTK7PEHMP1S00002BU |
| AE-CLOUD2 | YSAECLOUD2 | RTK7AECLD2S00001BU |

Renesas regrets the inconvenience caused due to the above change. For questions and support, please visit renesas.com/synergy/support.

## Website and Support

Visit the following URLs to learn about key elements of the Synergy Platform, download components and related documentation, and get support.

| | |
|---|---|
| Synergy Platform MCUs | www.renesas.com/renesas-synergy-platform-mcus |
| Synergy Software Package | www.renesas.com/synergy/ssp |
| Software add-ons | www.renesas.com/synergy/addons |
| SSP Components | www.renesas.com/synergy/sspcomponents |
| MCU Components | www.renesas.com/synergy/components-synergy-mcus |
| Kits | www.renesas.com/synergy/kits |
| Synergy Solutions Gallery | www.renesas.com/synergy/solutionsgallery |
| Partner projects | www.renesas.com/synergy/partnerprojects |
| Application projects | www.renesas.com/synergy/applicationprojects |
| Self-service support resources: | |
| Knowledgebase | www.renesas.com/synergy/knowledgebase |
| Forums | www.renesas.com/synergy/forum |
| Training | www.renesas.com/synergy/training |
| Videos | www.renesas.com/synergy/videos |
| Chat and web ticket | www.renesas.com/synergy/resourcelibrary |

## Revision History

| Rev. | Date | Description | |
|---|---|---|---|
| | | Page | Summary |
| 1.00 | Nov.15.21 | — | First release document |

# General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Precaution against Electrostatic Discharge (ESD)

   A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity. Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.
2. Processing at power-on

   The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power reaches the level at which resetting is specified.
3. Input of signal during power-off state

   Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.
4. Handling of unused pins

   Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.
5. Clock signals

   After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.
6. Voltage application waveform at input pin

   Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between $V_{IL}$ (Max.) and $V_{IH}$ (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between $V_{IL}$ (Max.) and $V_{IH}$ (Min.).
7. Prohibition of access to reserved addresses

   Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.
8. Differences between products

   Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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## Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,
Koto-ku, Tokyo 135-0061, Japan
www.renesas.com

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