Renesas RA Family

Device Lifecycle Management for Cortex-M33

Introduction

Device Lifecycle Management (DLM) is the management of the process by which a product goes from inception to development to production and then eventually end-of-life. The RA Family MCU debug capability and serial programming capability are defined by the device lifecycle states.

The RA Family MCUs with an Arm® Cortex®-M33 core leverage Arm® TrustZone® technology with authenticated DLM state transitions. This DLM system offers enhanced IP protection while maintaining the capability to regress the device lifecycle state.

This application note focuses on the use cases of the TrustZone-enabled RA Family MCU DLM System, walks through the DLM Key injection process, and demonstrates DLM state regression steps.

The RA6M4 MCU Group is used as an example. The general steps apply to all TrustZone-enabled RA Family MCUs.

Required Resources

The following resources are referenced throughout this application note:

Development Tools and Software

- Renesas Flash Programmer (RFP) v3.16
  https://www.renesas.com/us/en/products/software-tools/tools/programmer/renesas-flash-programmer-programming-gui.html
- Renesas Security Key Management Tool
  https://www.renesas.com/software-tool/security-key-management-tool
- Gpg4win (optional)
  http://www.gpg4win.org/

Hardware

- EK-RA6M4, Evaluation Kit for RA6M4 MCU Group (renesas.com/ra/ek-ra6m4)
- PC running Windows® 10 OS
- One USB device cable (type-A male to micro-B male)

Prerequisites and Intended Audience

This application note assumes you have some experience with the Renesas Flash Programmer (RFP). In addition, the application note assumes that you have some knowledge of RA Family MCU security features. See chapter 49, *Security Features*, in the *Renesas RA6M4 Group MCU User's Manual: Hardware* for background knowledge. User is also recommended to read the Security Key Management Tool (SKMT) User's Manual prior to proceeding with this application note.

The intended audience are product developers, product manufacturers, product support, or end users who are involved with any stage of the device lifecycle management of the RA Family MCUs.

Contents

1. Introduction to Device Lifecycle Management for RA Family MCU Groups

The RA Family DLM system provides an infrastructure that can play a key role in application development, production, product deployment, and failure analysis. Device Lifecycle Management is supported on RA Family MCUs with Arm® TrustZone®.

1.1 Device Lifecycle Management Overview

Device Lifecycle Management (DLM) identifies the current lifecycle phase of the device and controls access permissions to the debug interface and the serial programming interface. These RA Family MCUs offer cryptographic authentication to restore various levels of programming and debugging capabilities.

1.2 Device Lifecycle Management on RA Family MCUs

Figure 1 is a summary of all the possible states and state transitions for the entire lifecycle of the MCU, as described in Table 1.

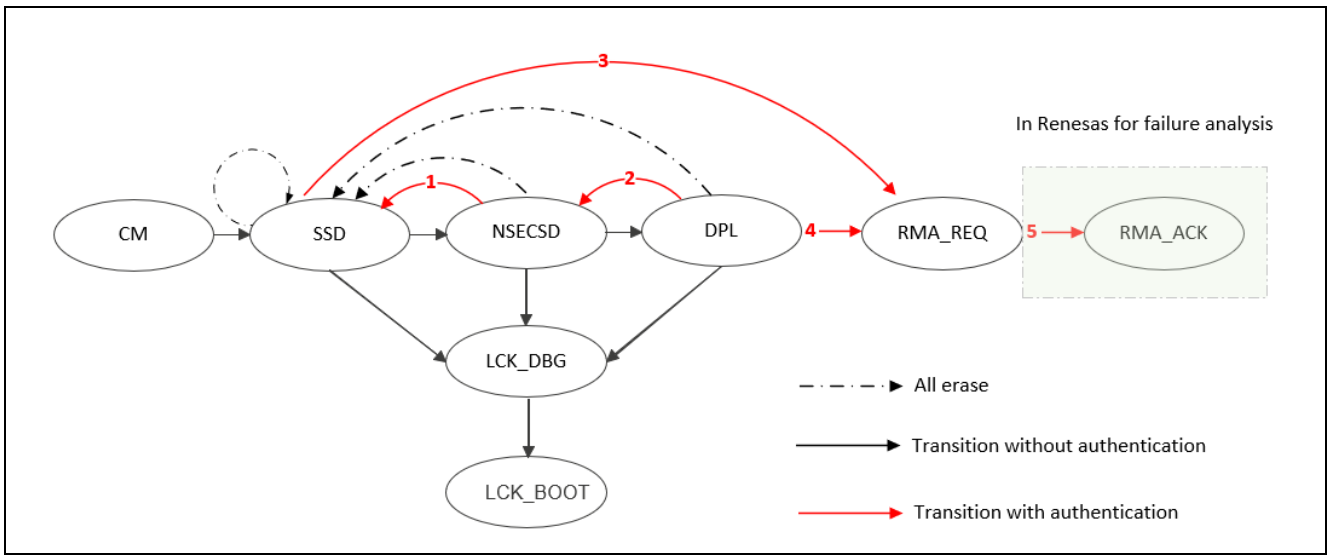

Figure 1. RA MCU Device Lifecycles

1.2.1 Definitions of the Device Lifecycle States

There are three debug levels for RA Family MCUs with the Cortex-M33 core and TrustZone support:

- DBG2: The debugger connection is allowed, with no restriction on access to memories and peripherals
- DBG1: The debugger connection is allowed, with access to only non-secure memory regions and peripherals
- DBG0: The debugger connection is not allowed

The serial programming interface can communicate with the factory bootloader of the device when the device is in boot mode. Tools such as the Renesas Flash Programmer (RFP) can communicate with the factory bootloader via the serial programming interface to change the device lifecycle states. Development tools such as the e^2 studio Interactive Development Environment provide partial support for the DLM state transitions that are typically used during product development.

Table 1 provides definitions of the various device lifecycle states, the corresponding debug level, and serial programming capability.

Table 1. TrustZone-enabled RA Family MCU Group Device Lifecycle States

Lifecycle state	Definition and State Features	Debug Level	Serial Programming
CM	• "**C**hip **M**anufacturing" • The customer may receive the device in this state. If so, the customer needs to move the state to SSD prior to application development.	DBG2	• Available • No access to code/data flash

Lifecycle state	Definition and State Features	Debug Level	Serial Programming
SSD	• "**S**ecure **S**oftware **D**evelopment" • The secure part of the application is being developed. The customer may receive the device in SSD state.	DBG2	• Available • Can program/erase/read all code/data flash
NSECSD	• "**N**on-**SEC**ure **S**oftware **D**evelopment" • The non-secure part of the application is being developed	DBG1	• Available • Can program/erase/read non-secure code/data flash
DPL	• "**DePL**oyed" • The device is in-field	DBG0	• Available. • No access to code/data flash
LCK_DBG	• "**LoCK**ed **DeBuG**" • The debug interface is permanently disabled	DBG0	• Available • No access to code/data flash
LCK_BOOT	• "**LoCK**ed **BOOT** interface" • The debug interface and the serial programming interface are permanently disabled	DBG0	• Not available
RMA_REQ	• "**R**eturn **M**aterial **A**uthorization **REQ**uest" • Request for RMA • The customer must send the device to Renesas in this state	DBG0	• Available • No access to code/data flash
RMA_ACK	• "**R**eturn **M**aterial **A**uthorization **ACK**nowledged" • Failure analysis at Renesas	DBG2	• Available • No access to code/data flash

1.2.2 Summary of the Device Lifecycle Transitions

As we can see from Figure 1, there are three types of transitions.

Non-Authenticated Transitions

These transitions follow a typical device flow from development to production. Each transition reduces the level of access allowed to the MCU.

"All erase" Transitions

The factory bootloader's **Initialize** command will erase the entire flash memory and set the device lifecycle to SSD. The **Initialize** command is not available under the following conditions:

- Permanent Block Protection has been set on one or more flash memory blocks. Permanent block protection cannot be revoked.
- The **Initialize** command has been disabled. Refer to section 3.2 Figure 7 on how to disable the **Initialize** command using RFP. If the **Initialize** command is disabled, it cannot be re-enabled.

Authenticated Transitions

The lifecycle states can be regressed without erasing the contents of the MCU flash memory via authenticated transitions. These authenticated transitions are desirable for failure analysis of the protected code. Authentication is performed with the use of user-defined DLM keys.

1.2.3 DLM Keys

Authenticated transitions are possible through the use of DLM keys. These user-defined keys are injected during specific device lifecycle states to allow authenticated regression back to that state. Authentication is performed via a challenge-response mechanism, protecting against eavesdropping and replay attacks.

The primary keys that most applications will use are:

- SECDBG_KEY – The Secure Debug Key can be injected when the MCU is in the SSD state. It can be used when the device is in the NSECSD state to regress back to the SSD state without erasing flash memory.
- NONSECDBG_KEY – The Non-secure Debug Key can be injected when the MCU is in the NSECSD state. It can be used when the device is in the DPL state to regress back to the NSECSD state without erasing flash memory.

Note that authenticated regression from DPL back to SSD must be done in two steps.
One additional key is available, which can be used to send the MCU to Renesas for failure analysis through the RMA process:

- RMA_KEY – The RMA Key can be injected when the MCU is in the SSD state. It can be used when the device is in either the SSD state or the DPL state to prepare the MCU for return to Renesas.

1.3 Advantages of Device Lifecycle Management

Device Lifecycle Management provides the following advantages over simple ID Code protection:

- Authenticated re-enabling of the debug and the serial programming interfaces protects IP by providing protection against eavesdropping and replay attacks.
- A Root of Trust or other sensitive code can be programmed and stored securely whilst retaining the ability to erase all flash contents. This avoids unnecessary MCU scrappage.
- The separation of Secure and Non-secure development with code protection for both regions enables new business models for providing valuable IP.

2. Device Lifecycle Management Use Cases

This section discusses some of the use cases for Device Lifecycle Management with RA Family TrustZone-enabled MCUs.

2.1 Immutable Root of Trust Provisioning

A Root of Trust (RoT) consists of data and services that provide a unique identity to a product. As such, it is vital to protect the code and data that are used to implement the Root of Trust. Arm TrustZone provides an excellent mechanism for isolating the Root of Trust from the rest of the application.

An important point to note is that the Root of Trust is typically immutable. This is done by using the permanent block protect feature of the MCU. Since permanent block protection disables the **Initialize** command, some DLM capabilities will not be available in this use case.

2.1.1 Development Phase

For MCUs, the Root of Trust is often implemented by the same development team that implements the entire application (that is, the Combined Development Model). During development, the Root of Trust can be programmed into the secure region with the device in the SSD state. The DLM state should then be changed to the NSECSD state for application development, and finally to DPL for system test. However, since all parties are trusted, DLM key usage during development is probably not necessary. If a problem is found with either the Root of Trust or the application, the MCU can simply be initialized to erase all flash memory and return the DLM state to the SSD state.

Note: Do not permanently lock flash blocks at this stage to make the Root of Trust immutable. This will disable the **Initialize** command, and the MCU will not be able to be reprogrammed. For testing purposes, use the temporary block protect mechanism.

2.1.2 Production Phase

Production programming with a TrustZone-enabled application is often a two-step process. For the Root of Trust use case, the Root of Trust (in the secure region) will likely be provisioned separately from the application layer (in the non-secure region) to enable the injection of infrastructure keys.

The following shows the DLM options for a deployed MCU for the Root of Trust use case. For this use case, it is assumed that the RoT (Root of Trust) has been made immutable, rendering the **Initialize** command unavailable. Note that deploying the product in the DPL state with no DLM keys is not useful, since the device can no longer be completely erased

Table 2. RoT Use Case, Possible Production DLM States

DLM State	Initialize Command	DLM Keys	Capabilities after Programming
DPL	Unavailable	SECDBG_KEY NSECDBG_KEY	Authenticated DLM state regression, additional limited serial programming interface access (1).
LCK_DBG	Unavailable	None	Limited serial programming interface access (1).
LCK_BOOT	Unavailable	None	No access

Note: 1. MCU Unique ID, MCU type (for example, R7FA6M4AF3CFB), and factory boot firmware version available.

If the end-product firmware will not be debugged in case of a problem with the end product, the LCK_BOOT or LCK_DBG states are recommended. If the end-product firmware may be debugged, the DPL state with SECDBG_KEY and NSECDBG_KEY injected should be used.

2.2 IP Protection

The IP Protection use case consists of valuable IP provided in a pre-programmed MCU to another party, who then develops the end application. IP protection is critical, but it is also helpful to retain the ability to reprogram the IP to avoid MCU scrappage.

2.2.1 Development – Valuable IP

The valuable IP will be placed in the secure region, with the MCU in the SSD state. Upon delivery to the second party, the DLM state will be moved to NSECSD, but there are some options available, as described by the following table.

Table 3. IP Protection Use Case, Possible IP Delivery DLM States

DLM State	Initialize Command	DLM Keys	Capabilities after Programming
NSECSD	Available	None	Valuable IP is protected, but it can be erased by a second party, including accidental erasure. Cannot return to SSD state to debug Valuable IP with the application.
NSECSD	Available	SECDBG_KEY	Valuable IP is protected, but it can be erased by a second party, including accidental erasure. Can perform authenticated transition to SSD state to debug Valuable IP with the application.
NSECSD	Disabled	None	Valuable IP is protected, and it cannot be erased by the second party. Cannot return to SSD state to debug Valuable IP with the application.
NSECSD	Disabled	SECDBG_KEY	Valuable IP is protected, and it cannot be erased by the second party. Can perform authenticated transition to SSD state to debug Valuable IP with the application.

2.2.2 Development – Application

The second party will receive the device in the NSECSD state. They can then develop and debug the application code that uses the IP, but they cannot view the IP or any data that has been placed in the secure region.

To test the full application in a production configuration (i.e., the DPL state), it is recommended to inject a NONSECDBG_KEY DLM Key for the following reasons:

- If the pre-programmed MCU was delivered with the **Initialize** command disabled, there is no way to return from the DPL state to the NSECSD state without using an authenticated transition with the NONSECDBG_KEY.
- If the pre-programmed MCU was delivered with the **Initialize** command enabled, using the **Initialize** command will erase both secure and nonsecure regions, including the purchased IP.

2.2.3 Production – Final Product

Since the usage of the SECDBG_KEY and NSECDBG_KEY and the configuration of the **Initialize** command are all independent, there are eight variations on delivering the end product in the DPL state. Table 4

summarizes the options. Note that some of the options depend on the state in which the pre-programmed MCU is delivered.

Table 4. IP Protection Use Case, Possible Production DLM States

DLM State	Initialize Command	DLM Keys	Capabilities after Programming
DPL	Disabled	SECDBG_KEY NSECDBG_KEY	Authenticated DLM state regression to NSECSD and SSD states, additional limited serial programming interface access (1).
DPL	Disabled	NSECDBG_KEY	Authenticated DLM state regression to NSECSD state, additional limited serial programming interface access (1).
DPL	Disabled	SECDBG_KEY	Limited serial programming interface access (1).
DPL	Disabled	None	Limited serial programming interface access (1).
DPL	Enabled	SECDBG_KEY NSECDBG_KEY	Authenticated DLM state regression to NSECSD and SSD states, the device can be **Initialized**, additional limited serial programming interface access (1).
DPL	Enabled	NSECDBG_KEY	Authenticated DLM state regression to NSECSD state, the device can be **Initialized**, additional limited serial programming interface access (1).
DPL	Enabled	SECDBG_KEY	Device can be **Initialized**, additional limited serial programming interface access (1).
DPL	Enabled	None	Device can be **Initialized**, additional limited serial programming interface access (1).
LCK_DBG	Unavailable	None	Limited serial programming interface access (1).
LCK_BOOT	Unavailable	None	No access

Note: 1. MCU Unique ID, MCU type (for example, R7FA6M4AF3CFB), and factory boot firmware version available.

2.3 Non-TrustZone Usage

Arm TrustZone usage on an RA Family MCU is optional. The Flat Project Development Model supports the use case of creating an application without TrustZone awareness. (Note that TrustZone register configuration is still recommended and is required for ethernet applications.) However, DLM keys are still useful for the ability to provide authenticated re-enabling of the debug and serial programming interfaces.

2.3.1 Development Phase

It is not necessary to use DLM keys during development. In a Flat Project, all development is done in the SSD state. To test the application as it will be delivered in a production state, change the DLM state to DPL. To reprogram the device, issue the **Initialize** command.

Note: Do not permanently lock any flash blocks at this stage. This will disable the **Initialize** command, and the MCU will not be able to be reprogrammed. For testing purposes, use the temporary block protect mechanism.

2.3.2 Production Phase

If it is desired to enable authenticated debugging, the SECDBG_KEY and NSECDBG_KEY must both be programmed. For simplicity, the same value can be used for both keys. However, the regression will be a two-step process: DPL to NSECSD, then NSECSD to SSD.

If the end product may need to be reprogrammed but retention of the existing code flash is not required, the DPL state can be used without DLM keys.

Table 5 summarizes the options.

Table 5. Non-TrustZone Use Case, Possible Production DLM States

DLM State	Initialize Command	DLM Keys	Capabilities after Programming
DPL	Disabled or Unavailable	SECDBG_KEY NSECDBG_KEY	Authenticated DLM state regression, additional limited serial programming interface access (1).
DPL	Disabled or Unavailable	None	Limited serial programming interface access (1).
DPL	Enabled	SECDBG_KEY NSECDBG_KEY	Authenticated DLM state regression to NSECSD and SSD states, the device can be **Initialized**, additional limited serial programming interface access (1).
DPL	Enabled	None	Device can be **Initialized**, additional limited serial programming interface access (1).
LCK_DBG	Unavailable	None	Limited serial programming interface access (1).
LCK_BOOT	Unavailable	None	No access

Note: 1. MCU Unique ID, MCU type (e.g., R7FA6M4AF3CFB), and factory boot firmware version available.

2.4 Renesas RMA

To submit the MCU for failure analysis by Renesas through the RMA process, an RMA_KEY must be injected, and the device must be in either the SSD state or the DPL state prior to performing a transition to the RMA_REQ state. This transition will erase all code and data flash that has not been permanently locked, and the MCU can be delivered to Renesas for failure analysis.

This use case can be combined with all of the above use cases.

3. Non-Authenticated DLM State Transitions

Non-authenticated DLM state transitions do not require the use of DLM keys. These transitions reflect a typical lifecycle flow from development through to production and deployment, with decreasing levels of access with each transition.

3.1 Non-Authenticated DLM State Transition Using the Renesas Device Partition Manager

The Renesas Device Partition Manager is a utility integrated into the e^2 studio IDE for Device Lifecycle State management during development. The Renesas Device Partition Manage can perform the following functions:

- Query the current device lifecycle state
- Query the device IDAU region setup
- **Initialize** the device to the SSD state (unless any flash blocks have been permanently locked)
- Set up IDAU regions

Note: The user must power cycle the board prior to working with the **Renesas Device Partition Manager** after a debug session if J-Link is used as the connection interface.

In the e^2 studio IDE, open the **Renesas Device Partition Manager**.

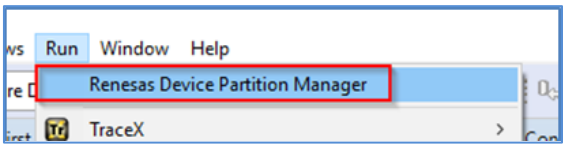

Figure 2. Open the Renesas Device Partition Manager

Figure 3 shows how to **Initialize** the device back to factory default. Choose the connection method and then click **Run**.

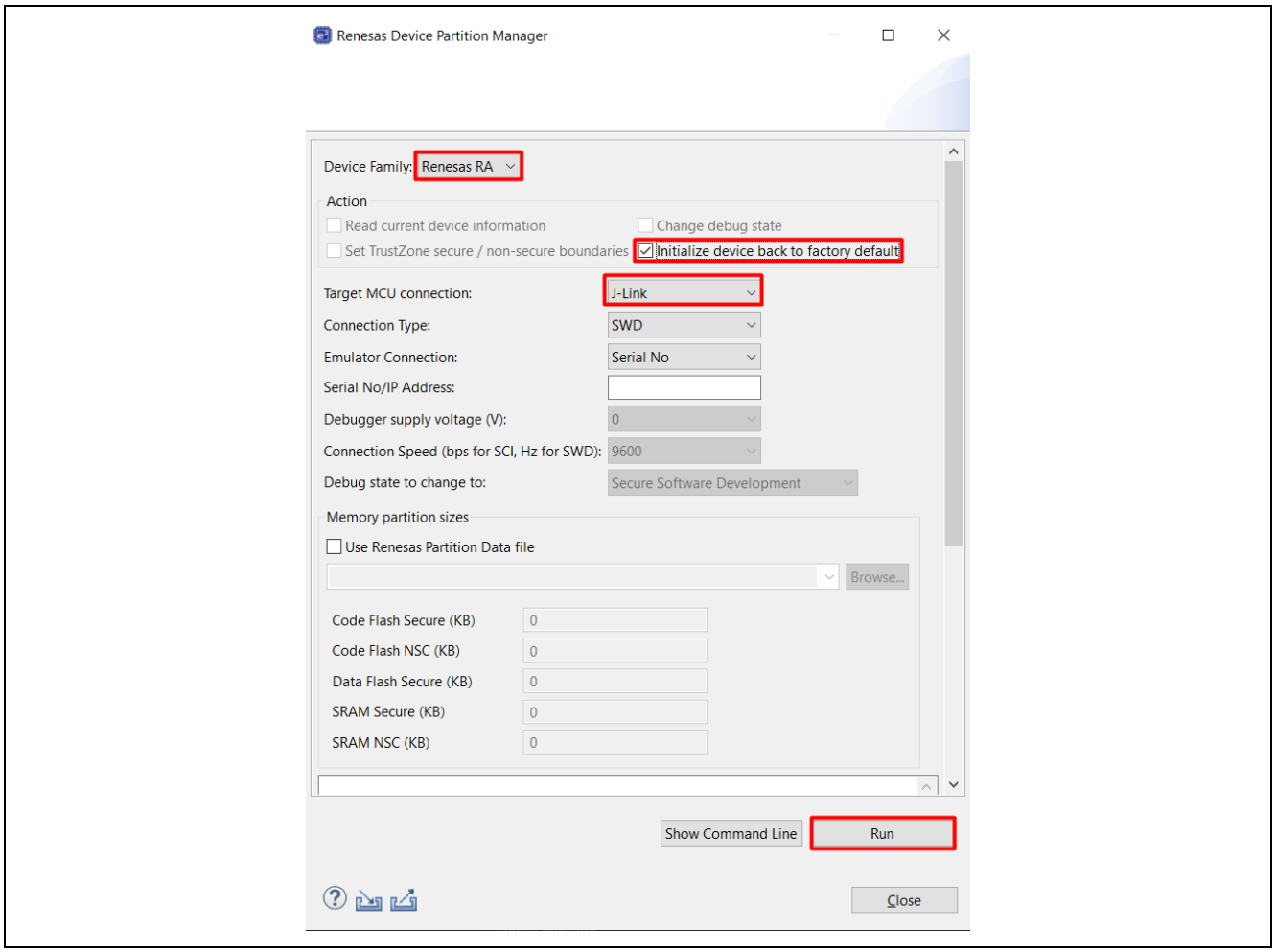

Figure 3. Initialize RA6M4 Using Renesas Device Partition Manager

During Product Development, users typically use the **Renesas Device Partition Manager** to perform Device Lifecycle Advancements (SSD to NSECSD, NSECSD to DPL) and to **Initialize** the device (erase all device memories and set the DLM state to SSD).

Limitations with the Renesas Device Partition Manager

- It does not support authenticated transitions.
- It supports transitions to limited device lifecycle states
- It requires the e^2 studio IDE to operate.

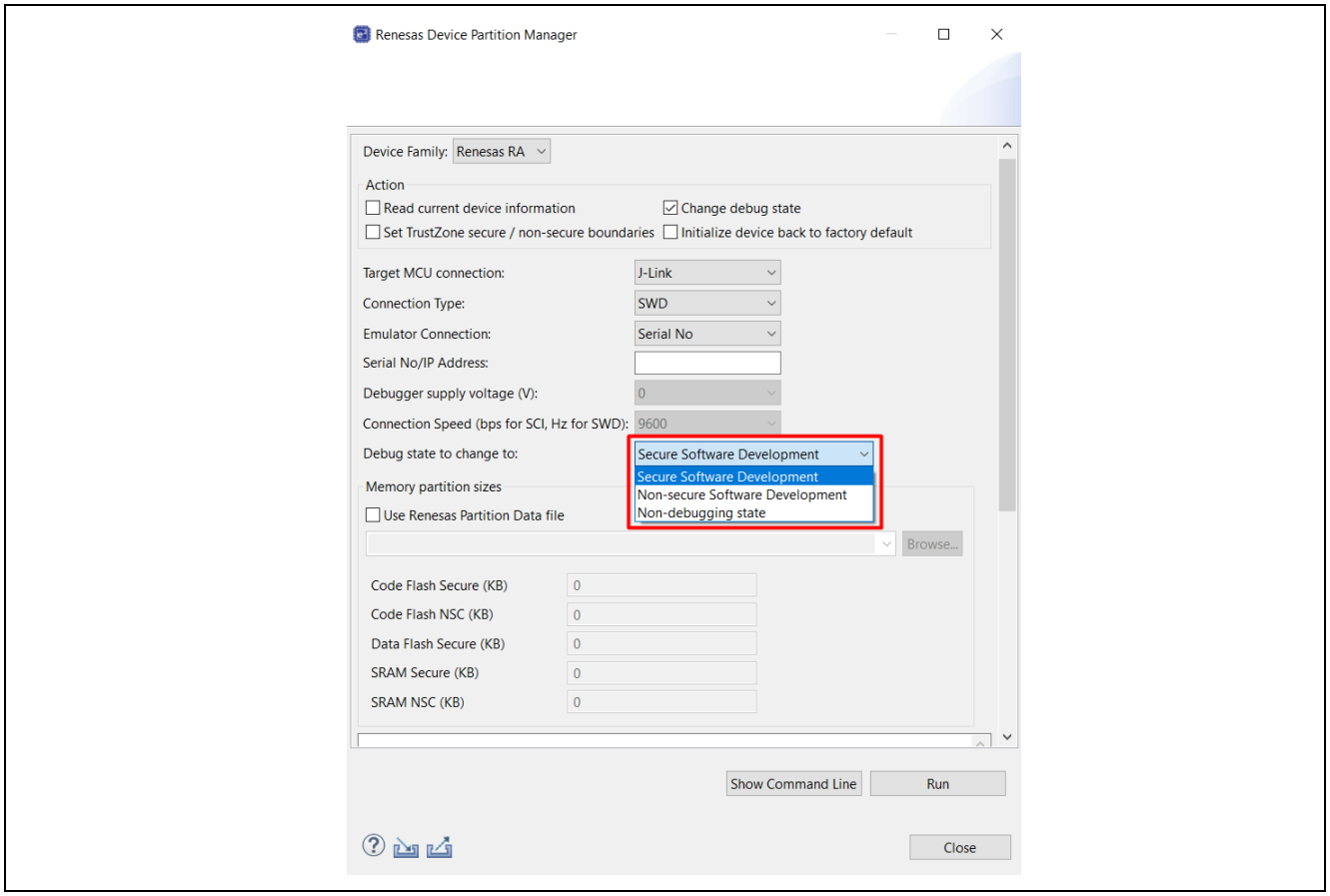

Figure 4. Advance Device Lifecycle States using Renesas Device Partition Manager

3.2 Non-Authenticated DLM State Transitions Using the Renesas Flash Programmer

The Renesas Flash Programmer provides end-to-end production flow support. RFP provides the following functionalities.

- Supports all unauthenticated device lifecycle state transitions.
- Supports DLM key injection.
- Supports authenticated device lifecycle state transitions.
- Supports disabling the **Initialize** command. This may be desired if the device is deployed in a DPL state and there is a requirement to avoid accidental flash content erasing. However, once the **Initialize** command is disabled, it cannot be re-enabled.

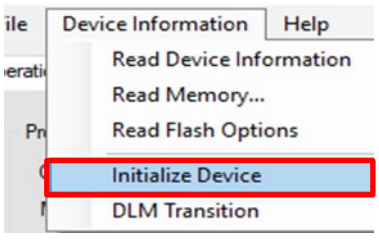

Figure 5. Using the Initialize Device Command

Unauthenticated transitions can be performed as shown in Figure 6.

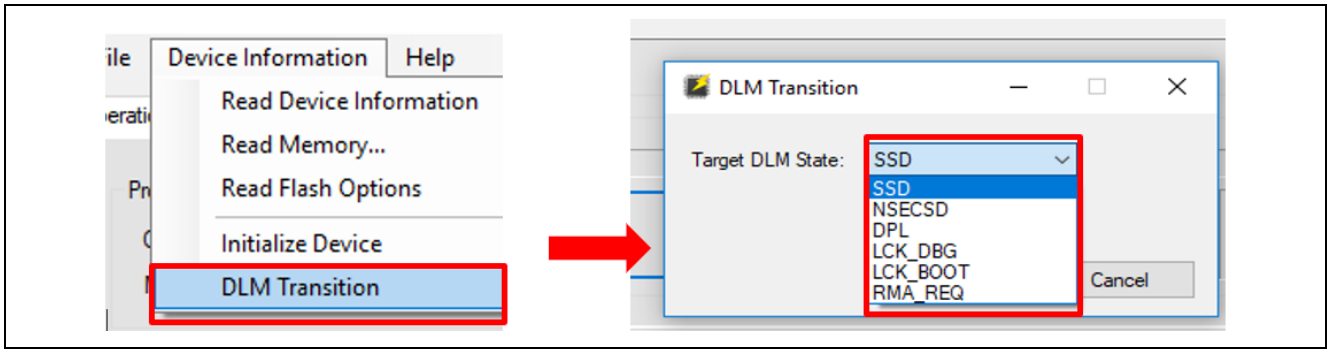

Figure 6. Available Lifecycle State Transitions Supported by RFP

The Initialize command can be disabled, as shown below. This operation cannot be reversed.

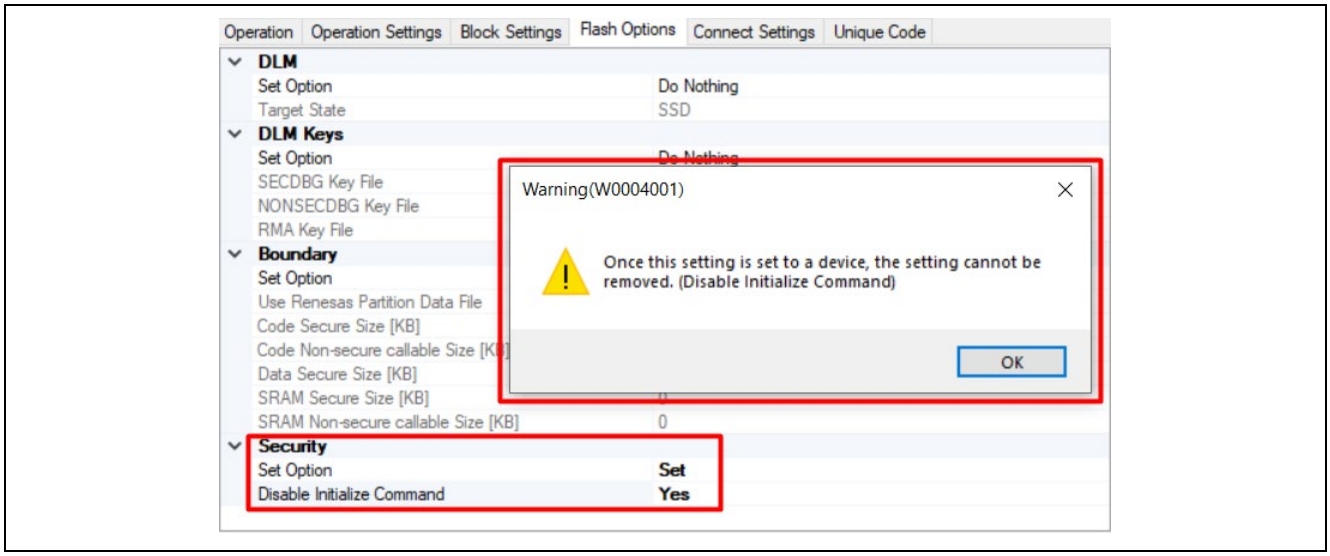

Figure 7. Disable the Initialize Command

4. DLM Key Injection

4.1 Tools Used in the DLM Key Injection Process

Three tools are used in the process of DLM key injection.

- Gpg4win - This tool is used to generate and use PGP keys, which are used to establish a secure communication channel between the developer and the Renesas Key Wrap server. Gpg4win is used here for demonstration, but any PGP management tool can be used.
- Renesas Security Key Management Tool (SKMT) - This tool is to generate the various key files needed for DLM key injection.
- Renesas Flash Programmer (RFP) - This tool is used to inject DLM keys.

4.2 DLM Key Injection Process Overview

There are three high-level steps required to inject DLM keys into the MCU.

1. Create an arbitrary 256-bit User Factory Programming Key (UFPK) and obtain a Wrapped UFPK (W-UFPK). The UFPK is used to encrypt the DLM key, to ensure that no plaintext keys are exposed

during the injection process. The UFPK must be wrapped by the Renesas Key Wrapping Service to obtain the W-UFPK.

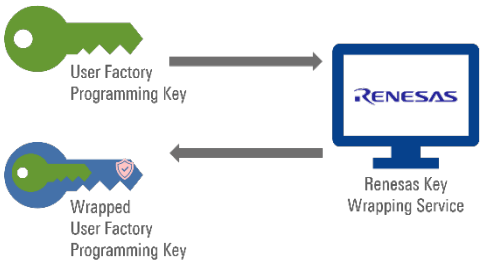

Figure 8. Wrap the User Factory Programming Key (UFPK)

2. Wrap the DLM key using the UFPK.

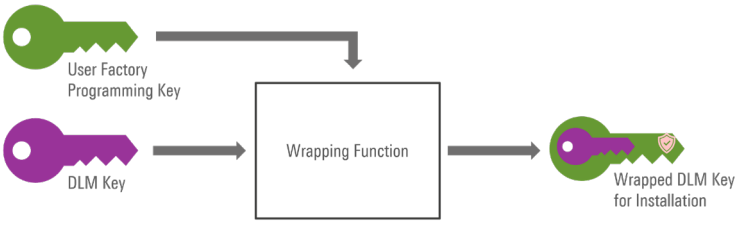

Figure 9. Wrap the DLM Key Using UFPK

3. Inject the DLM key using the serial programming interface by providing the W-UFPK and the wrapped DLM key. Note that this is a conceptual representation; after the wrapped DLM key is generated by SKMT and injected by the RFP, it is not necessary to inject the W-UFPK through the RFP. The information in the W-UFPK is included in the generated wrapped DLM Key.

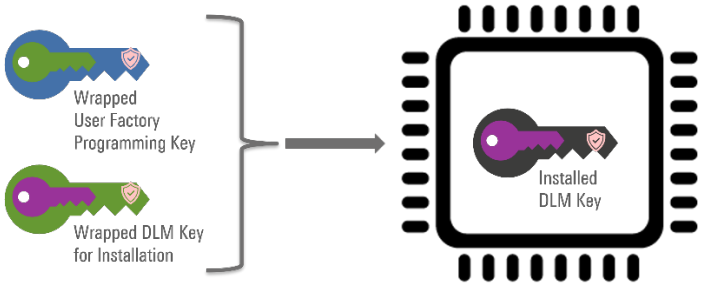

Figure 10. Created Wrapped DLM Keys

4.3 Establish PGP-Encrypted Communication with the Renesas DLM Server

Key material is exchanged with the Renesas DLM server using PGP encryption. This requires an exchange of public keys. This is a one-time process.

4.3.1 Overview of Device Lifecycle Management (DLM) Server

Using a web browser, enter the URL https://dlm.renesas.com/ to access the Renesas DLM server.

The *Key Wrap Service User's Manual* can be accessed by clicking on the FAQ link on the right of the screen. The link to the *DLM server User's Manual* is in the answer to the FAQ question, "Is there a manual of this system?" as shown in Figure 11.

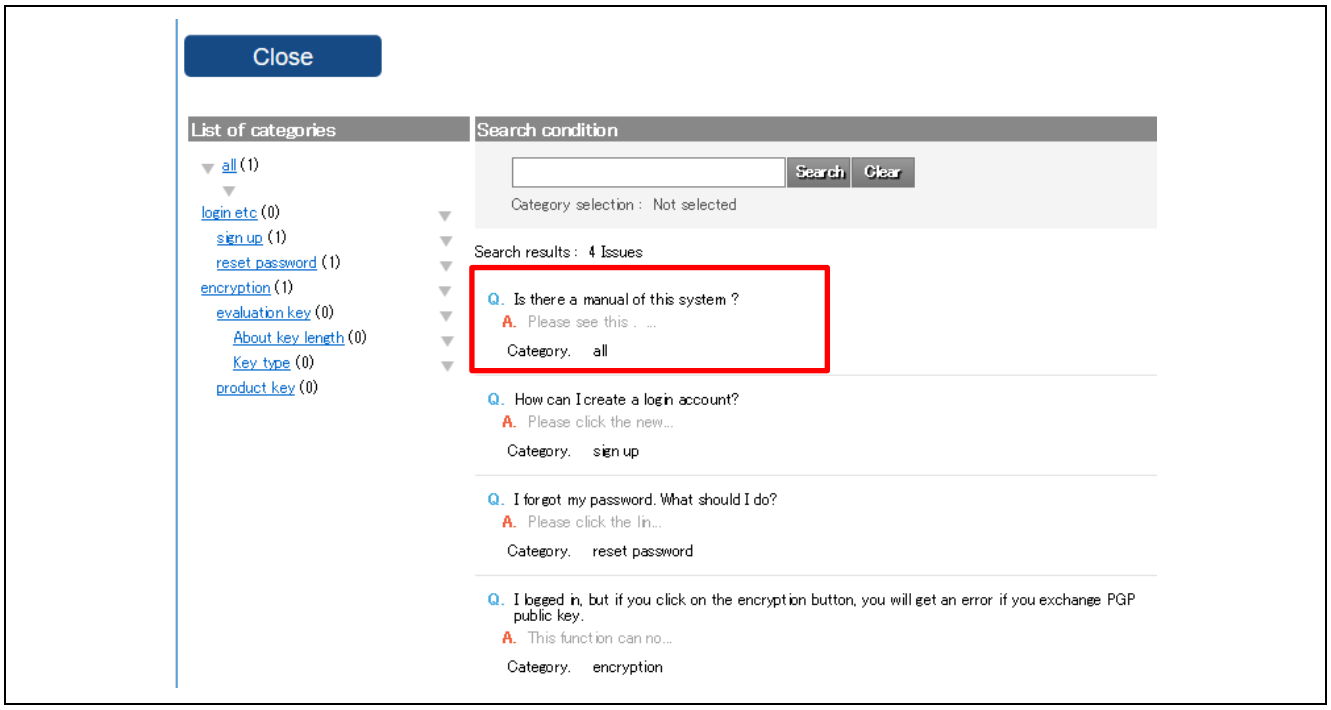

Figure 11. DLM Server FAQ and User's Manual

The information communicated between the user and the DLM server needs to be encrypted by OpenPGP. Figure 12 shows an overview of the operational flow for the Key Wrapping service with OpenPGP encryption as a security measure to protect the DLM Keys in transit.

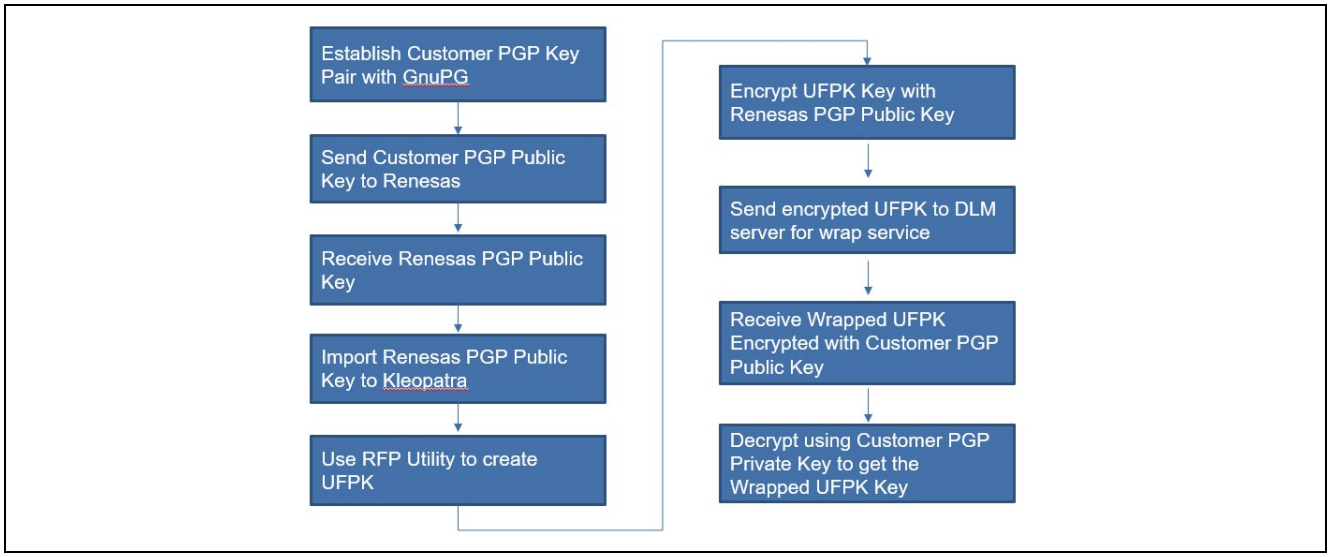

Figure 12. Overview of DLM Key Wrapping Service Using PGP

4.3.2 Create a PGP Key Pair

If you already have a PGP key pair, you can skip this step. Otherwise, use the following steps to create an OpenPGP key pair.

This Application Note uses Gpg4win, the official GnuPG distribution for Windows®, for the PGP key generation, encryption, and decryption service. Note that the generated private key needs to be managed securely and protected from exposure.

Gpg4Win can be downloaded from this URL: http://www.gpg4win.org/

Kleopatra is the certificate manager and unified crypto GUI provided in Gpg4Win. The screenshots included in this application note are based on Gpg4win-4.3.1. There may be minor graphic interface updates with later versions; however, the functionality used in this application note should persist.

1. Launch Kleopatra. Click File->New OpenPGP Key Pair…

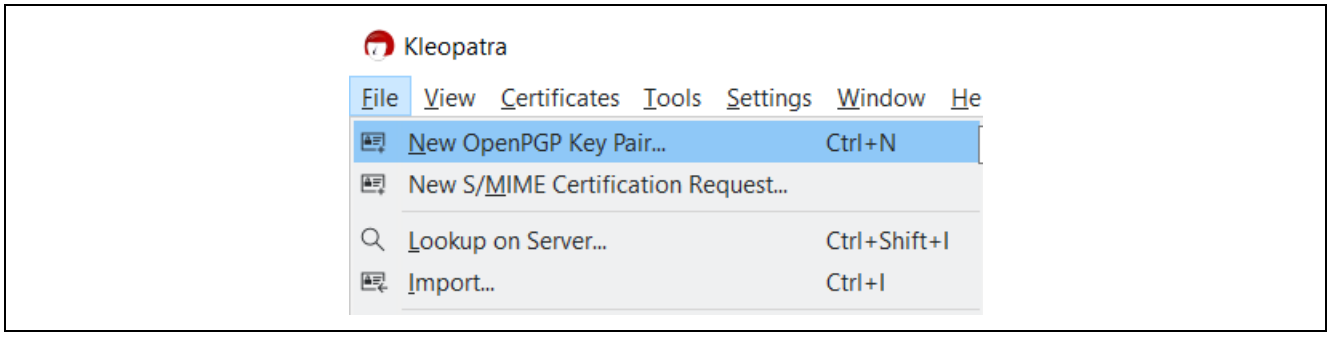

Figure 13. Generate PGP Key Pair

2. Enter your details as shown. Check Protect the generated key with a passphrase.

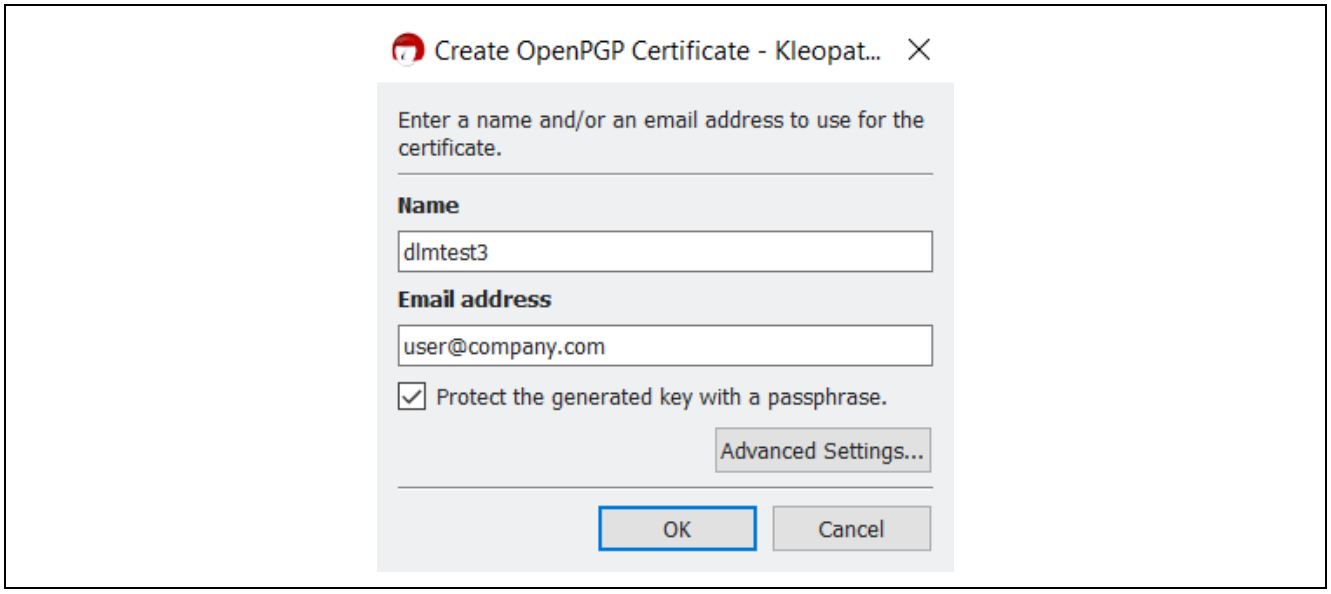

Figure 14. Provide Credentials to Create the PGP Key Pair

3. Click **Advanced Settings** and select **RSA** and **3,072 bits**. Click **OK** and then click **Create**.

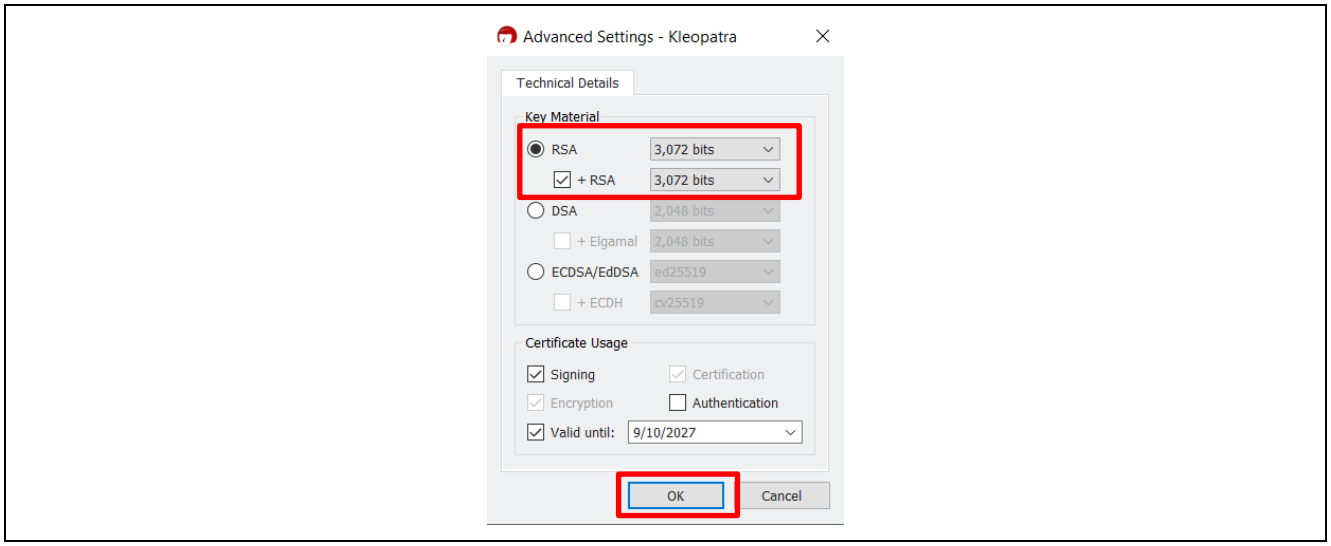

Figure 15. Advanced Settings for the PGP Key pair

4. Provide a passphrase to protect the private key. Be sure to save your passphrase.

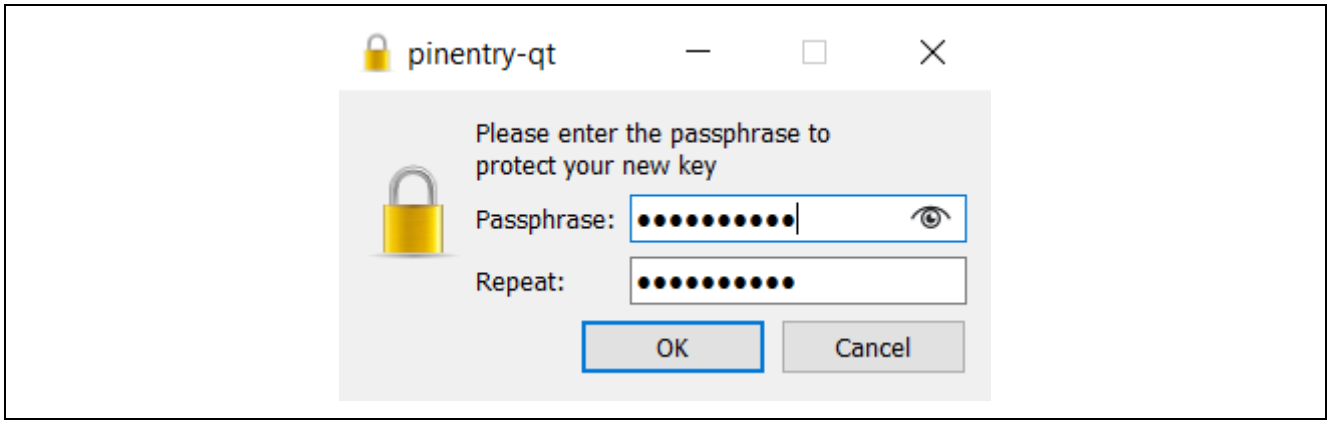

Figure 16. Provide passphrases

5. Observe that the PGP key pair is created successfully.

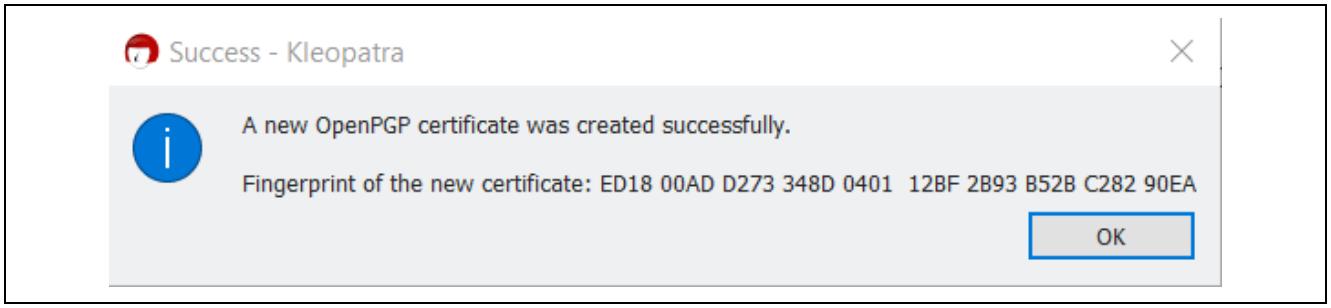

Figure 17. Key Pair Successfully Created

6. Export your public key by right clicking the name you just created and selecting **Export**, as shown below.

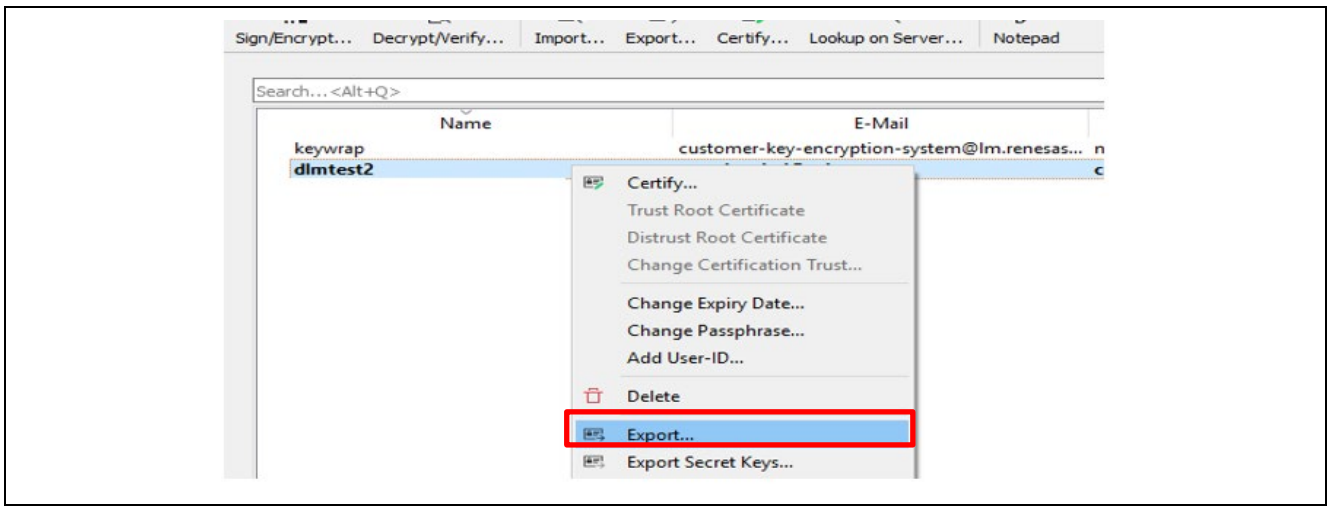

Figure 18. Export the Public key

7. Save your public key to a file with `*.asc` extension, for example `public_key.asc`.

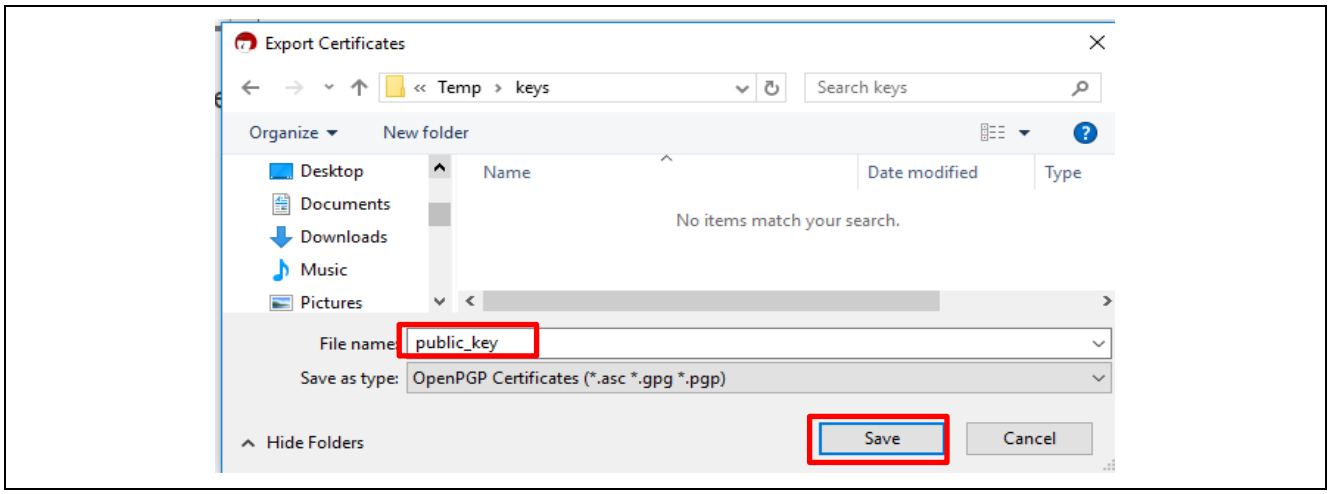

Figure 19. Save the Public Key

4.3.3 Registration with DLM Server

This section provides a brief walk-through of the DLM server registration steps. This is a one-time process for new users. You can also review the section “New registration” in the DLM user manual for further details on the new registration steps.

Open the URL https://dlm.renesas.com/ in a web browser and click **New registration**. Follow the prompt to provide an email address and click **Send mail**.

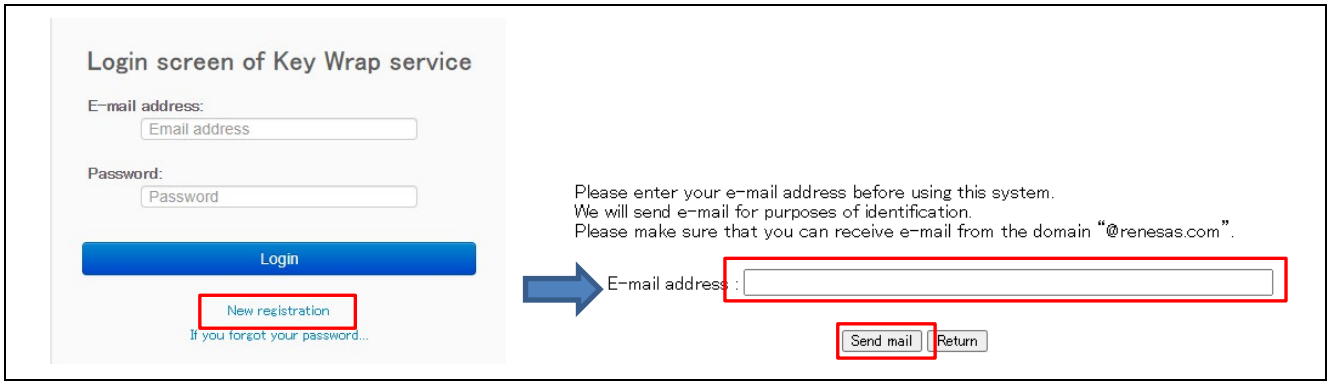

Figure 20. Provide Email Address for New Registration

You will receive the registration link in an email, as shown in Figure 21.

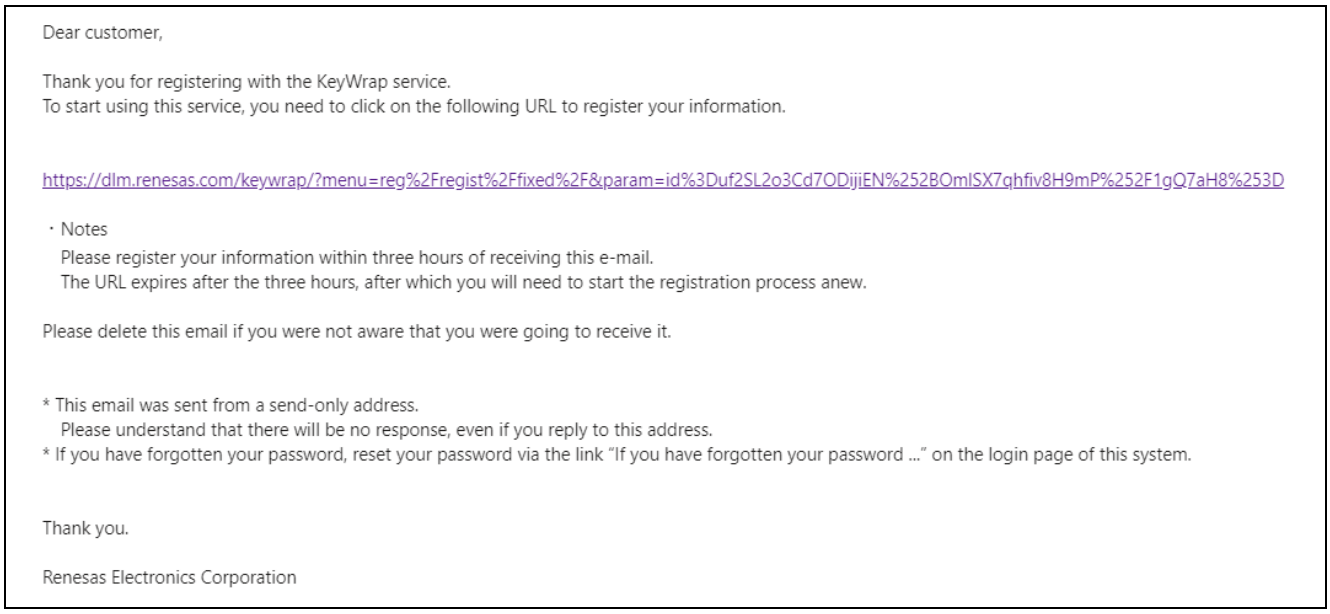

Figure 21. Email with Registration Link

Click on the URL in the confirmation email and provide the requested information. Click the **Next (confirmation)** button. After the confirmation screen is displayed, click on the **Registration** button to complete the user registration.

Figure 22. Register Customer Information

4.3.4 PGP Public Key Exchange

Once you have successfully registered, the following screen will open. Click on the **Start service** button to start using the key encryption system.

Figure 23. Start Using the DLM Service

Accept the **Trusted Secure IP Key Wrap Agreement** as shown below by clicking **Agree**. Note that this Agreement will come up every time you log into the DLM server.

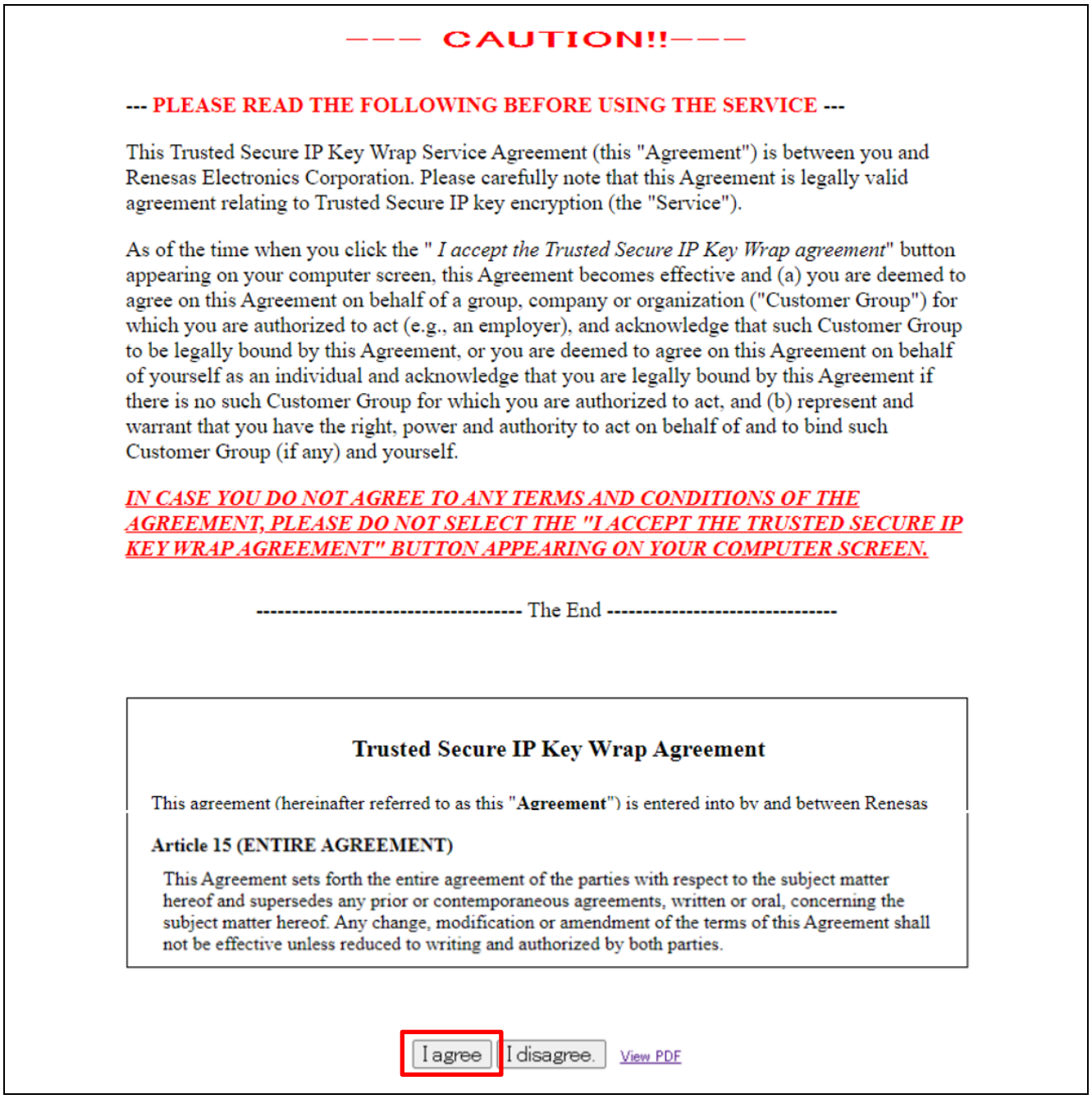

Figure 24. Agree with the Key Wrap Agreement

Communication between the user and the DLM server uses PGP to encrypt all transferred data. Public PGP keys must be exchanged to enable these transfers.

If you try to transfer data to the key wrap service without exchanging PGP keys, you will receive the error message shown below.

Figure 25. Request for PGP Key Exchange

To exchange PGP keys, click the **PGP key exchange** button. The dialog shown in Figure 26 will open. Click **Reference** and select the public key that you created and exported earlier (section 4.3.2, in Figure 19). Next, click **PGP key exchange** and wait to receive the Renesas PGP public key at the email address you provided during registration.

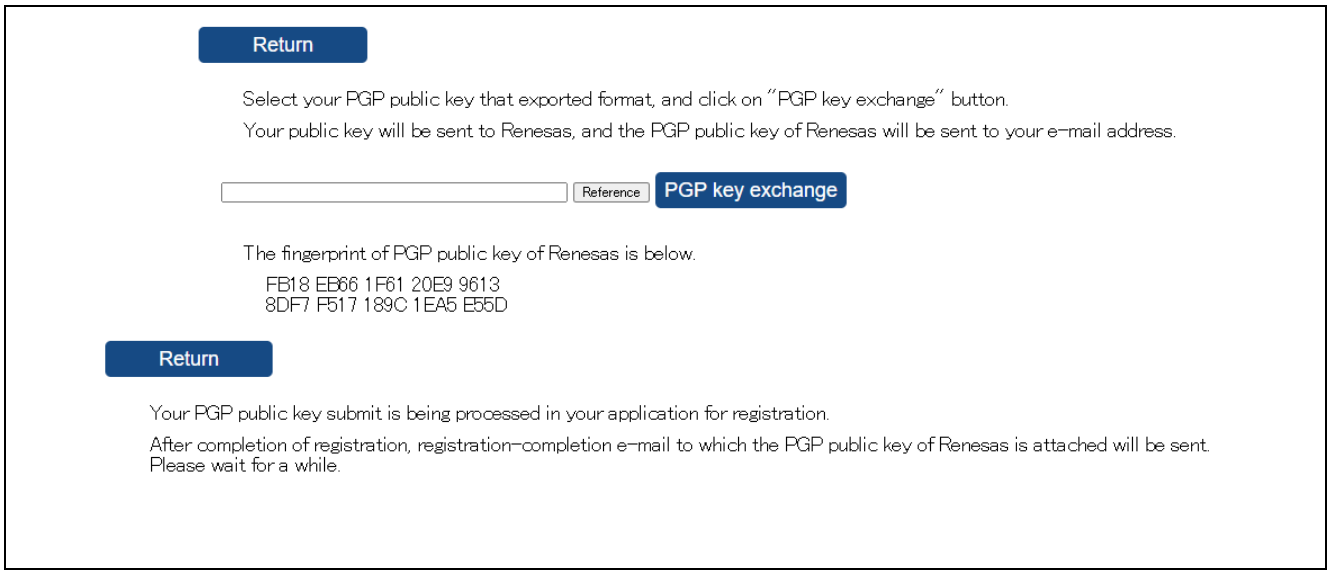

Figure 26. Provide PGP Public Key to DLM Server

You will receive an email with the content as shown in Figure 27, including the Renesas public key. Save the Renesas PGP public key received (`keywrap-pub.key`). Note that the fingerprint of this key is provided on the PGP key exchange dialog, as shown in Figure 26.

PGP keys can be exchanged any number of times. If keys are exchanged multiple times, all previous PGP keys are discarded, and the key wrap service will use the latest PGP public key that has been successfully exchanged to encrypt the transferred data.

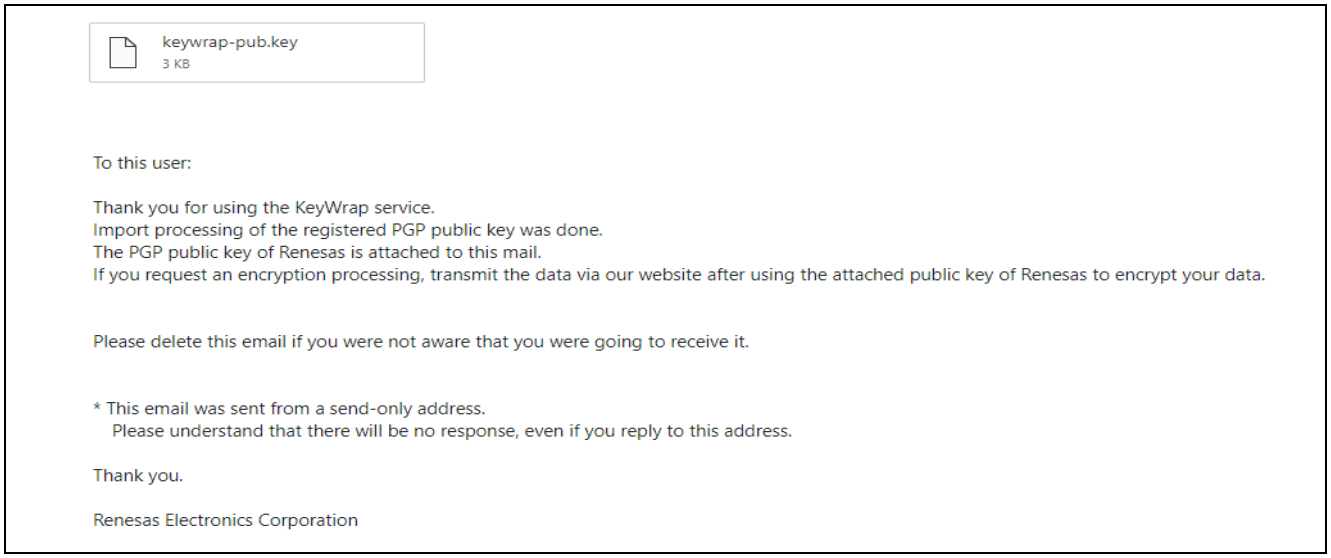

Figure 27. Receive the Renesas PGP Public Key

4.3.5 Import Renesas PGP Public Key into Kleopatra

Go back to the Kleopatra application and import the Renesas PGP Public key into Kleopatra as shown in Figure 28.

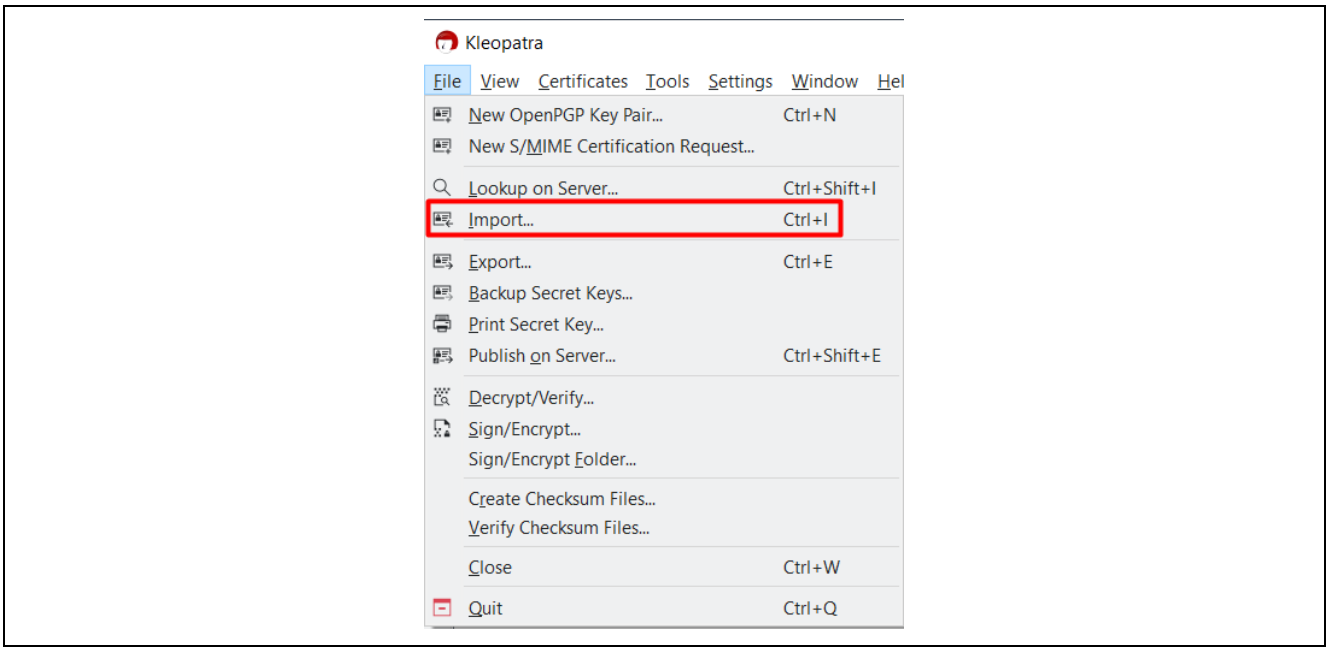

Figure 28. Import Renesas Public Key into Kleopatra

Click **File->Import**, and select the `keywrap-pub.key` file, received from the key wrap service.

The following item will appear in the **Imported Certificates** in Kleopatra, and the Renesas public key is ready to be used.

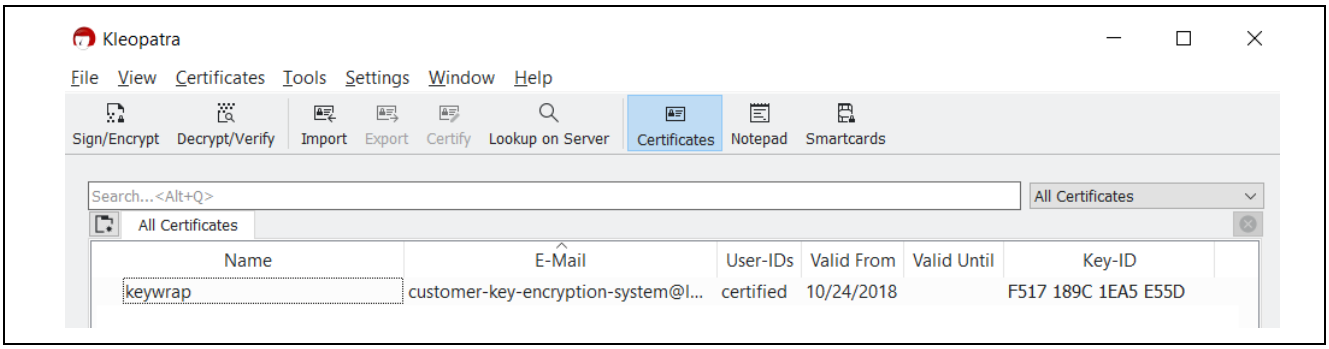

Figure 29. Renesas PGP Public Key

4.4 Create the UFPK and W-UFPK

This section walks the user through the process of creating a UFPK and obtaining the W-UFPK.

4.4.1 Renesas Security Key Management Tool

The Renesas Security Key Management Tool (SKMT) performs several functions during the DLM key injection process. The SKMT is available from the following link:

https://www.renesas.com/software-tool/security-key-management-tool

Locate the **Downloads** area, download the latest Security Key Management Tool installer, and install it as required for your operating environment. This tool supports Windows and Linux. The screenshots and instructions in this document use the Windows environment.

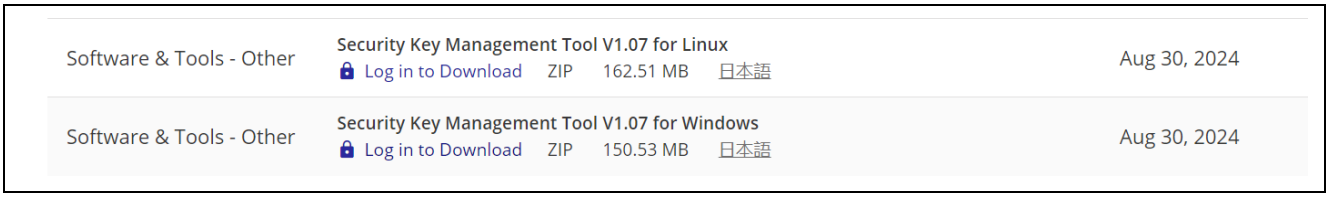

Figure 30. Download the Security Key Management Tool for Windows or Linux

The User's Manual of this tool is located in the extract folder. We recommend that you read through the User's Manual before proceeding to the following section.

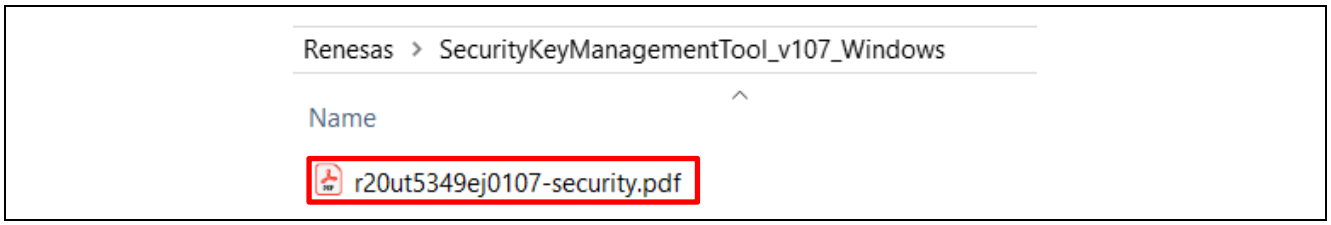

Figure 31. User's Manual for the Security Key Management Tool

The SKMT provides two interfaces to users: a Command Line Interface (CLI) and a Graphical User Interface (GUI). The GUI interface is primarily intended for development usage. The CLI interface is primarily intended for production support or development efforts involving multiple keys due to its ability to create key file-generation scripts. This application note will explain how to use both interfaces to perform DLM key injection.

4.4.2 Create a UFPK Key File

The Security Key Management Tool (SKMT) can be used in either Graphic User Interface (GUI) or Command Line Interface (CLI) mode to create a UFPK key file.

4.4.2.1 Generate UFPK Using the GUI

Launch the **Security Key Management Tool** executable.

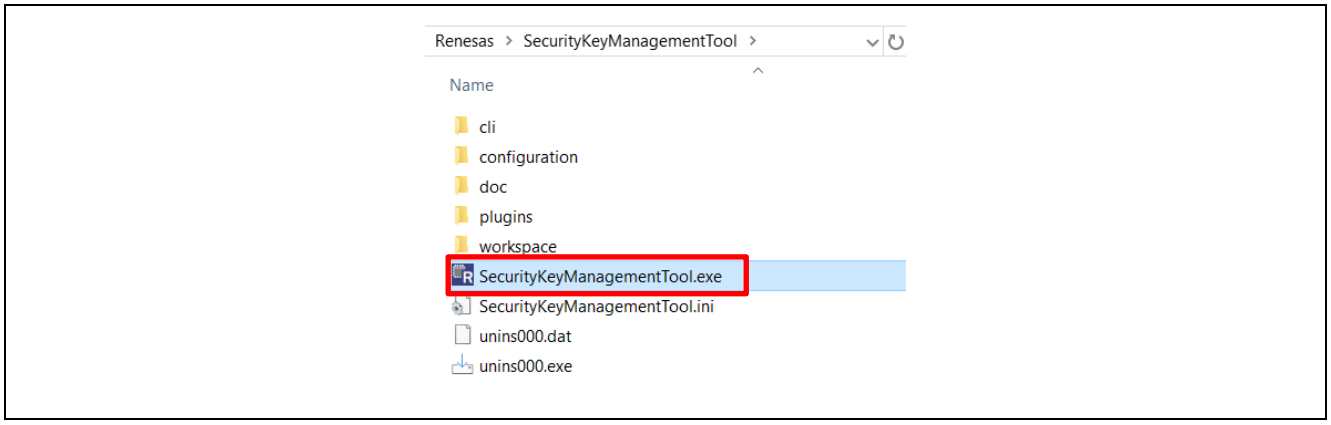

Figure 32. Launch SKMT GUI Interface

In the **Overview** window, select the microcontroller/microprocessor and crypto engine as **RA Family, SCE9 Security Functions and Protected Mode**.

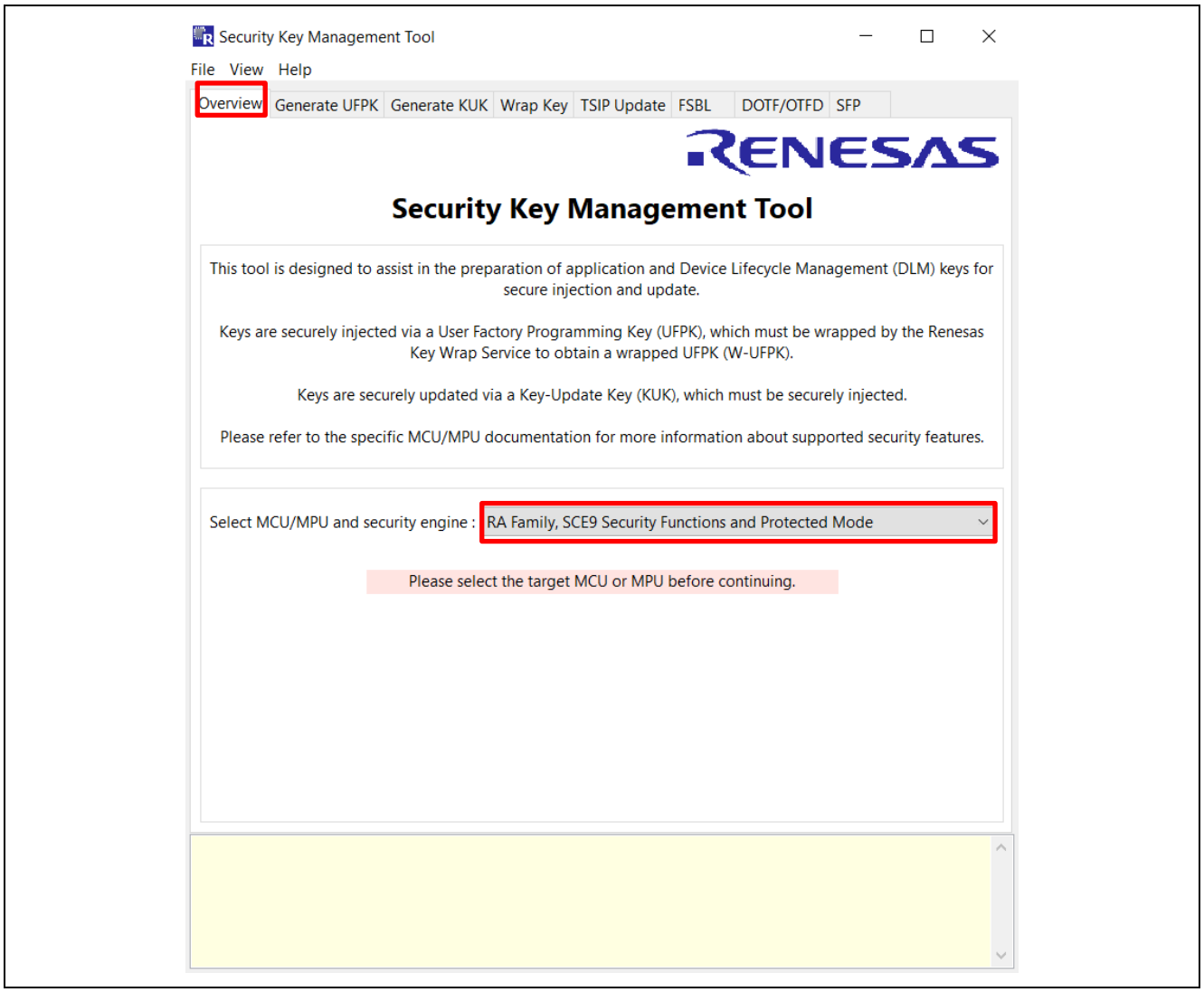

Figure 33. Select RA Family, SCE9 Protected Mode

Next, click on the **Generate UFPK** tab.

As shown in Figure 34 and Figure 35, you can specify a desired value for the UFPK, or the tool can create a random value. Figure 34 shows the tool generating a random value by selecting **Generate random value**. Figure 35 shows specifying a specific value by selecting **User specified value** and entering the 32-byte key value in big-endian format.

Click the **Browse** button to enter a file name for the generated key file. Here we have chosen `ufpk.key`.

Finally, click the button **Generate UFPK key file** to generate the key file `ufpk.key`.

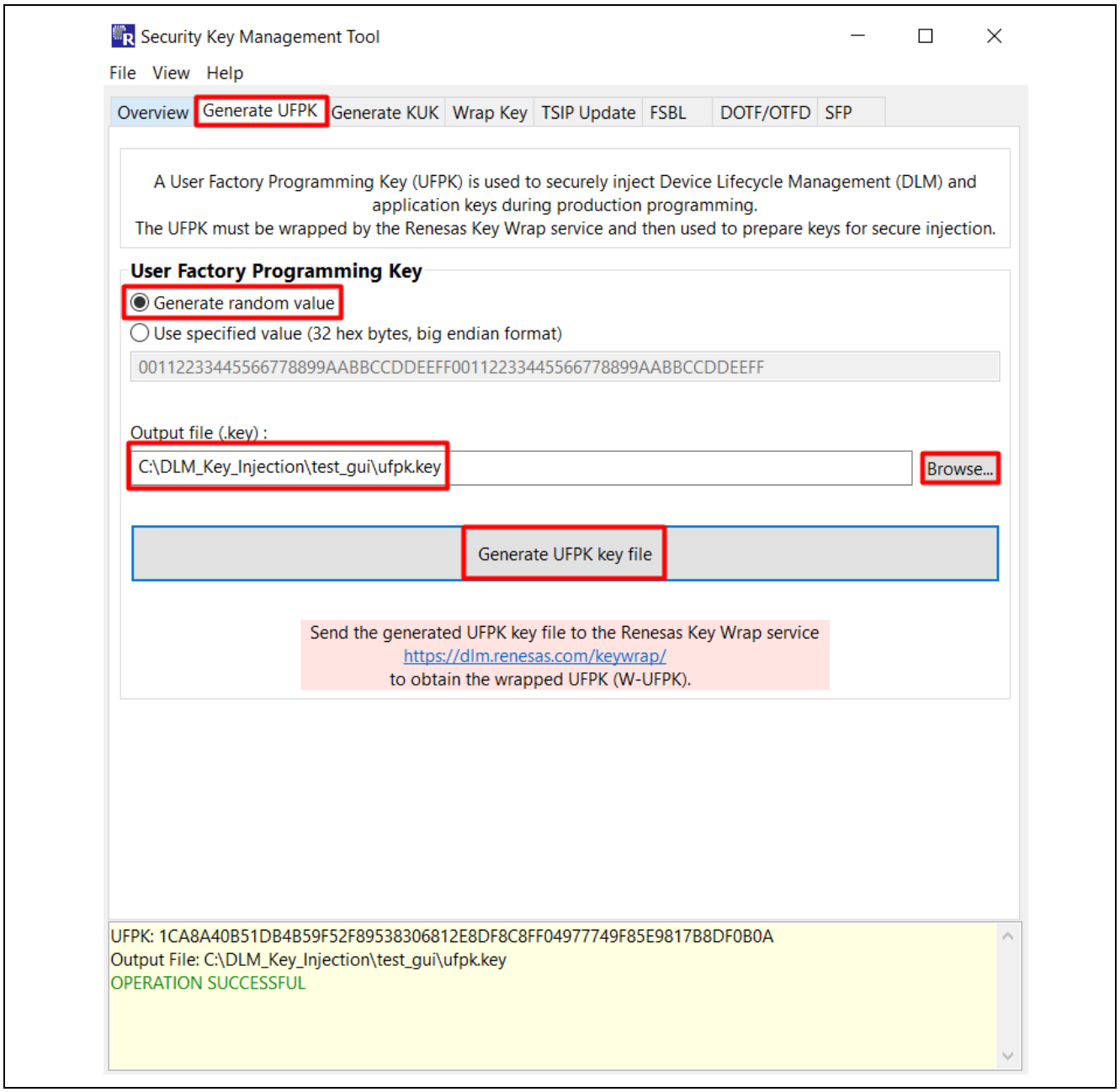

Figure 34. Generate a Random UFPK Using the GUI

In this example, the following 32-byte key is used:

`000102030405060708090A0B0C0D0E0F000102030405060708090a0b0c0d0e0f`

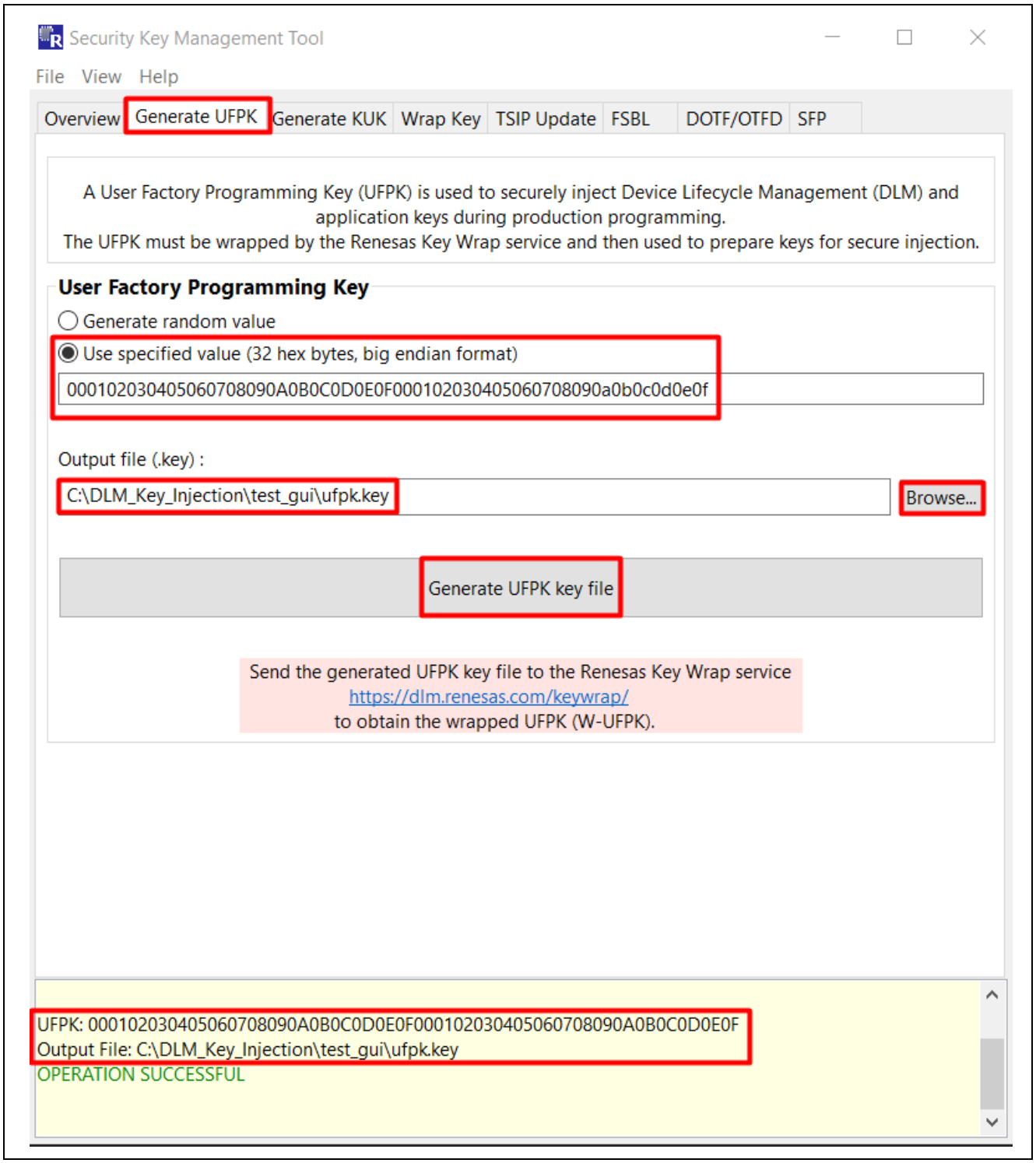

Figure 35. Create a Fixed UFPK Using the GUI

4.4.2.2 Generate UFPK Using the CLI

Open a Command Prompt window and navigate to the folder where `skmt.exe` resides, typically under `\Renesas\Security Key Management Tool\CLI\`.

Use the following command to generate a random UFPK and place it in a key file (`ufpk.key`). If desired, a complete file name with a path may be specified. Refer to the Security Key Management Tool User's Manual for more details about the `/genufpk` command.

```
skmt.exe /genufpk /output "C:\DLM_Key_Injection\keys\ufpk.key"
```

This command will generate a random 256-bit UFPK, as shown below.

```
UFPK: E8AB23E99C9AD42823DA4215549A41496720F7243680A4715F4B944ACC94B691
Output File: C:\DLM_Key_ Injection\test\ufpk.key
```

Figure 36. Generate a Random UFPK Using the CLI

It is also possible to specify a specific UFPK, as shown by the following command:

```
skmt.exe /genufpk /ufpk
"000102030405060708090A0B0C0D0E0F000102030405060708090a0b0c0d0e0f" /output
"C:\DLM_Key_Injection\test\ufpk.key"
```

```
UFPK: 000102030405060708090A0A0C0D0E0F000102030405060708090a0b0c0d0e0f
Output File: C:\DLM_Key_Injection\test\ufpk.key
```

Figure 37. Create a Fixed UFPK Using the CLI

Note: The `ufpk.key` generated from any one of the previous executions can be used for the purpose of exercising the example project.

4.4.3 Encrypt UFPK with Renesas PGP Public Key

Select **Sign/Encrypt** from Kleopatra and select to encrypt the `ufpk.key` key file with the Renesas PGP public key.

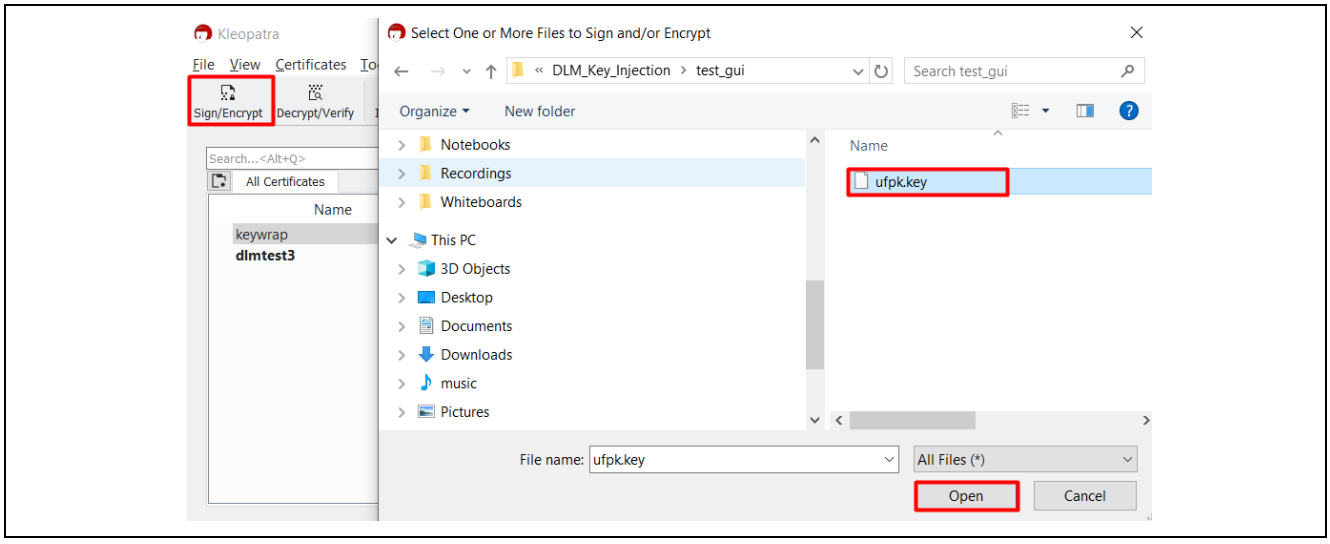

Figure 38. Select the UFPK Key to be Encrypted

Choose **Encrypt for others** and select the Renesas PGP Public key. Click **Sign/Encrypt**.

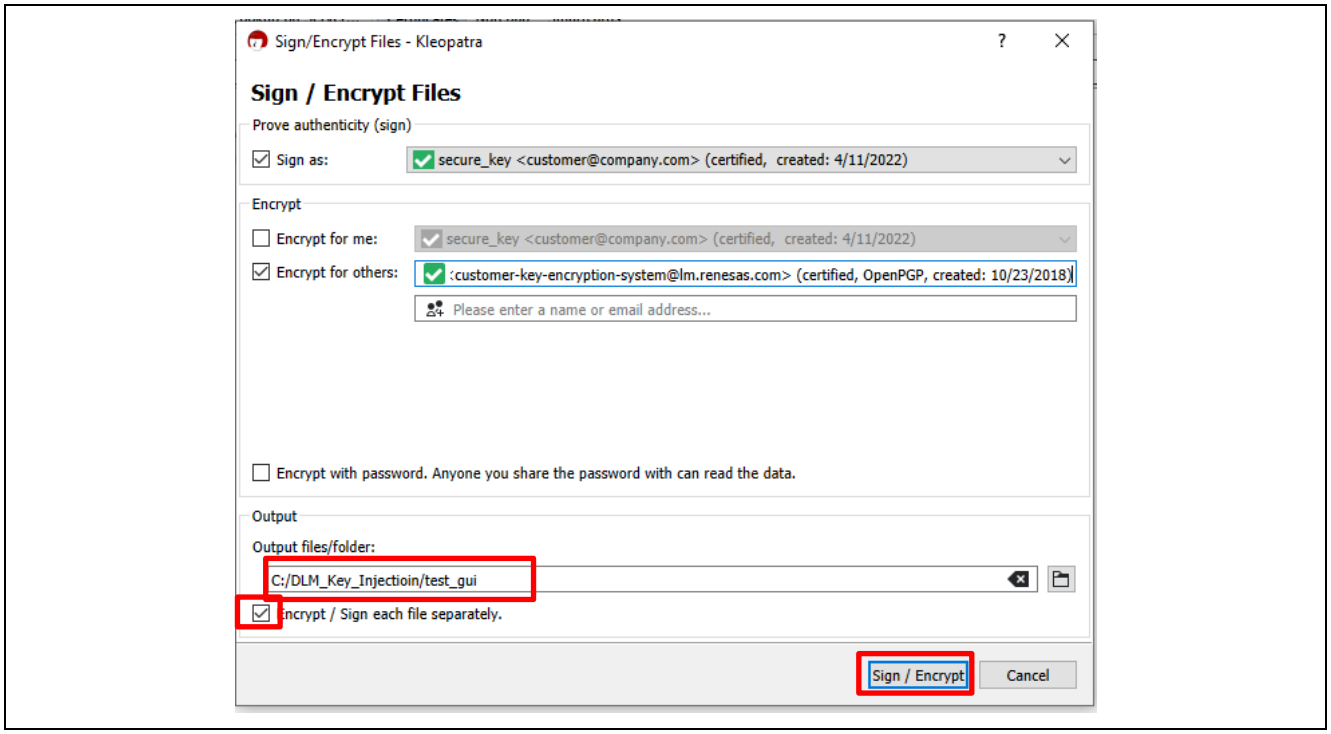

Figure 39. Use Renesas Public Key to Encrypt UFPK

You may also want to select **Encrypt for me** so you have an encrypted version of the UFPK key file to archive. If you do not, you will get an Encrypt-To-Self Warning that you cannot decrypt the data. Press **Continue**.

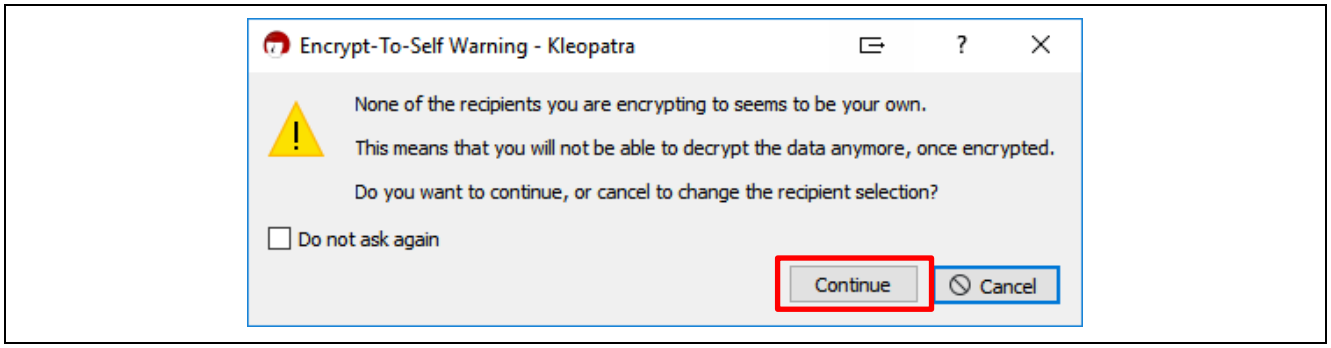

Figure 40. Confirm Encryption Option

The UFPK encrypted with the Renesas public key will be generated in the selected folder, and .gpg will be added to the extension. In this example, `ufpk.key.gpg` is generated. Click **Finish**.

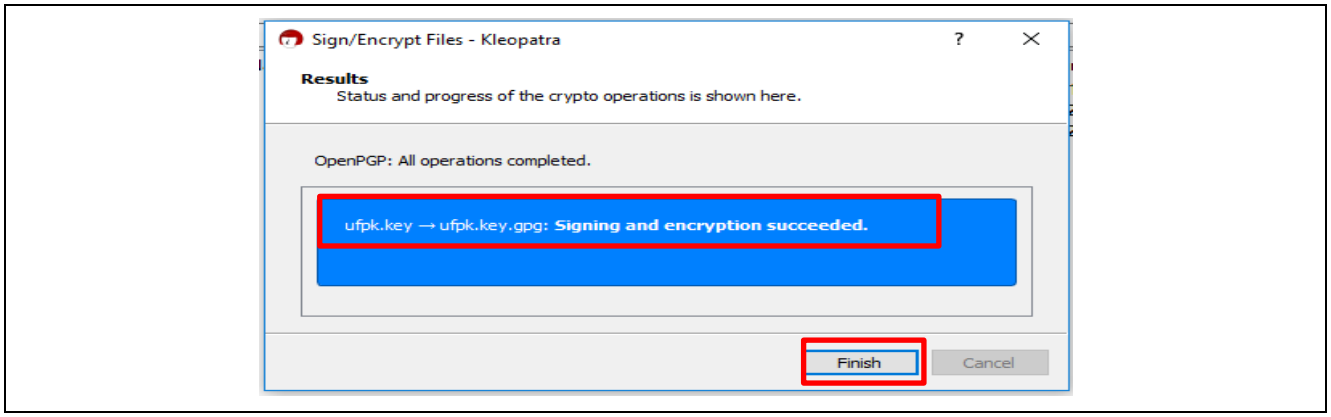

Figure 41. Encrypt the UFPK Key with Renesas Public Key

4.4.4 Send UFPK to Renesas DLM Server

Now, we can send the encrypted UFPK to the Renesas DLM Server, where it will be decrypted by the Renesas private key and used to generate the Wrapped UFPK (W-UFPK).

From the DLM Server user interface, select the RA Family series and choose **RA6M4/RA6M5 Encryption of customer's data** -> **Encryption service for products,** as shown below.

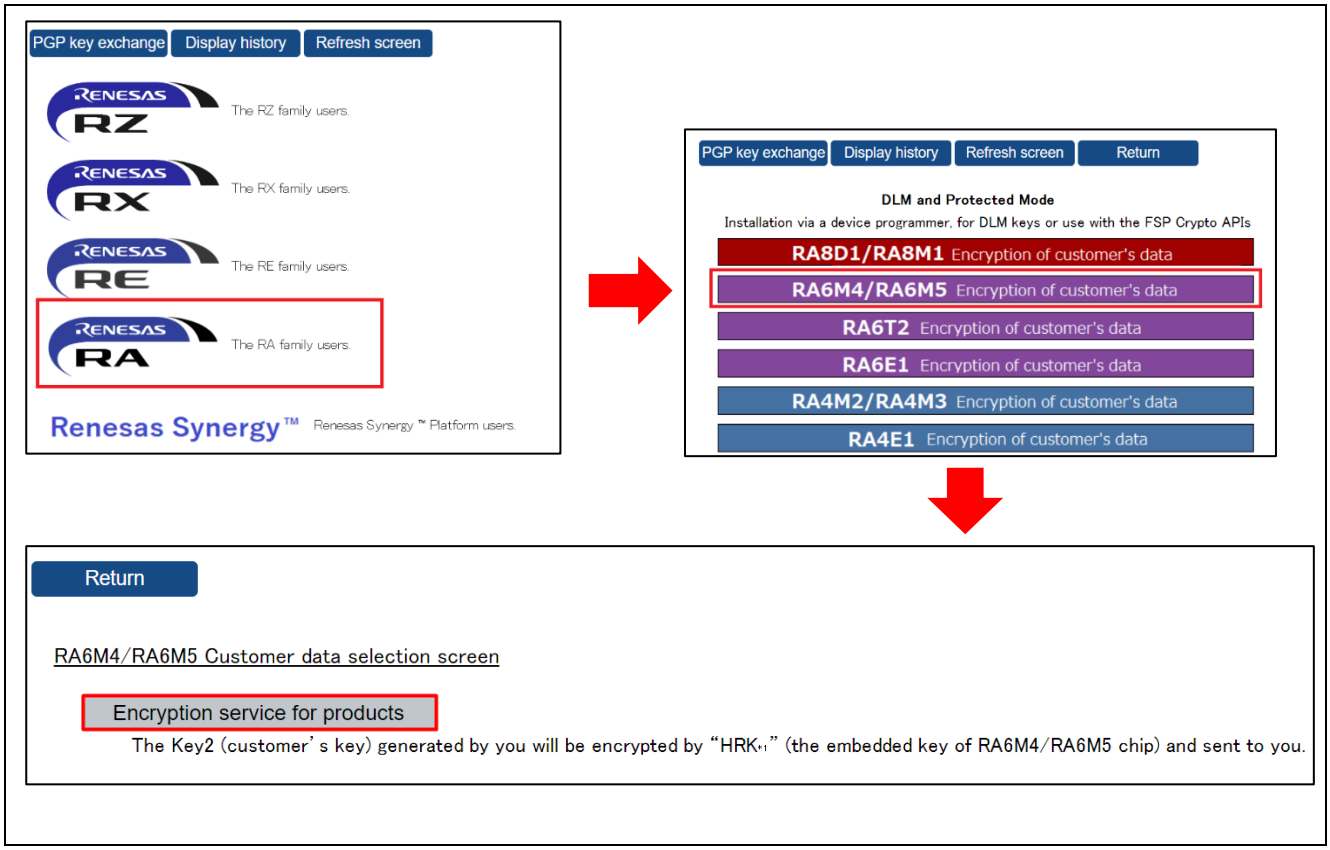

Figure 42. Select RA Device

Next, click **Reference,** and select the `.gpg` file generated from Figure 41.

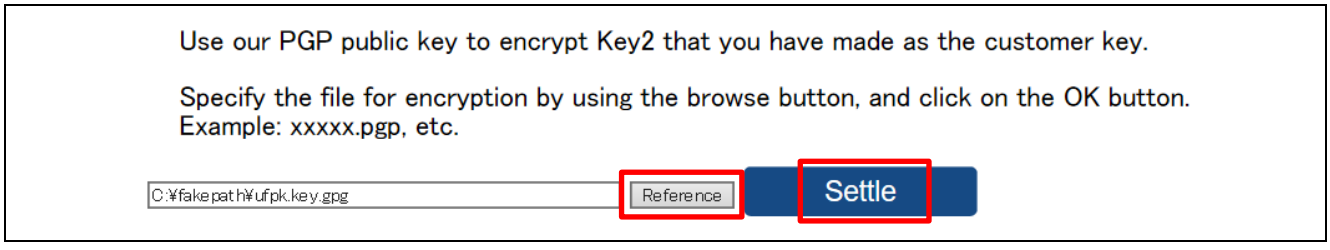

Figure 43. Send Encrypted UFPK to DLM Server

Click **Settle.** The DLM server will display a message that the file has been received, and the W-UFPK will be sent to the registered email address.

Figure 44. Message from DLM Sever

4.4.5 Receive the Encrypted W-UFPK Key

The W-UFPK, encrypted with your PGP public key, should arrive in your email within a few minutes.

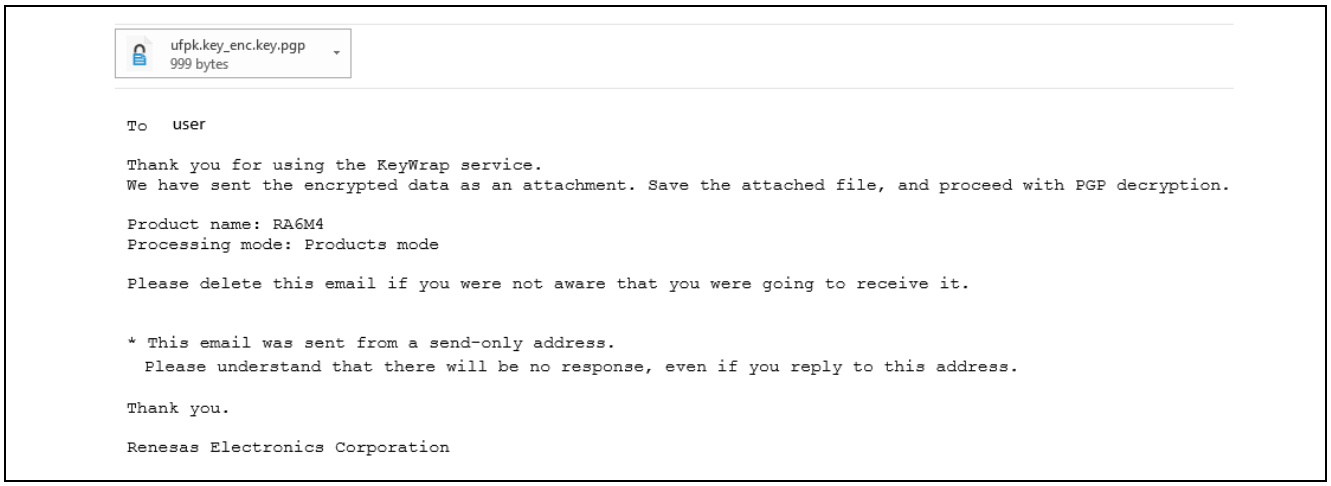

Figure 45. Receive the Wrapped DLM Key

Save this file for use in next step.

4.4.6 Decrypt the Encrypted W-UFPK Key

The W-UFPK received from the email is encrypted with your public key. You must decrypt the W-UFPK key file using your private key.

In Kleopatra, click **Decrypt/Verify** and select the encrypted W-UFPK key file.

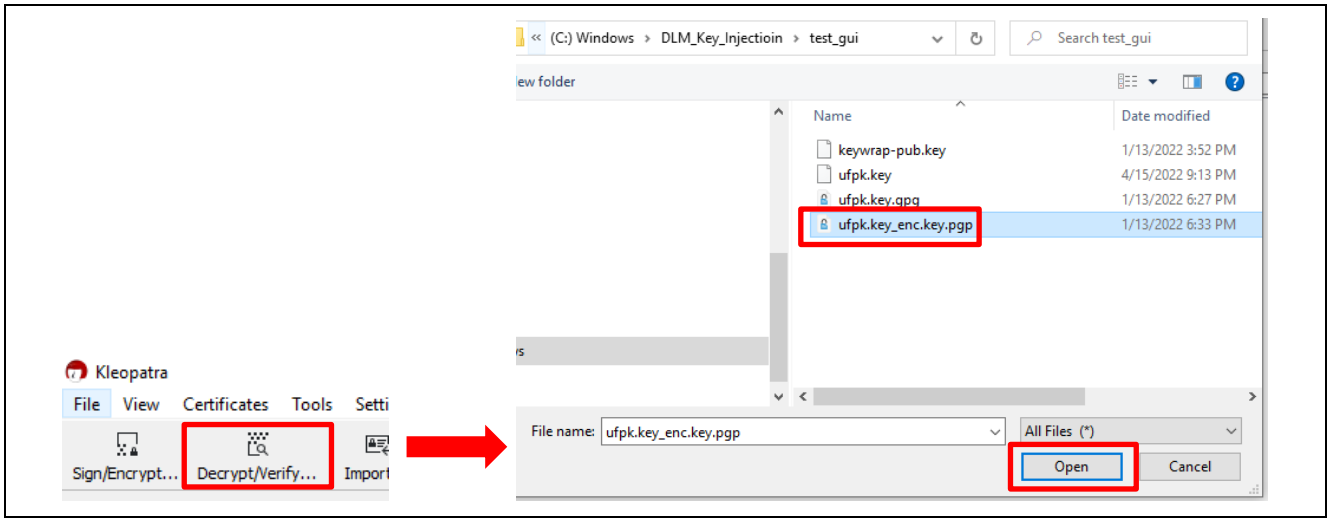

Figure 46. Encrypted W-UFPK Key File

If prompted, provide your PGP key passphrase. The decrypted file will be stored at the specified location.

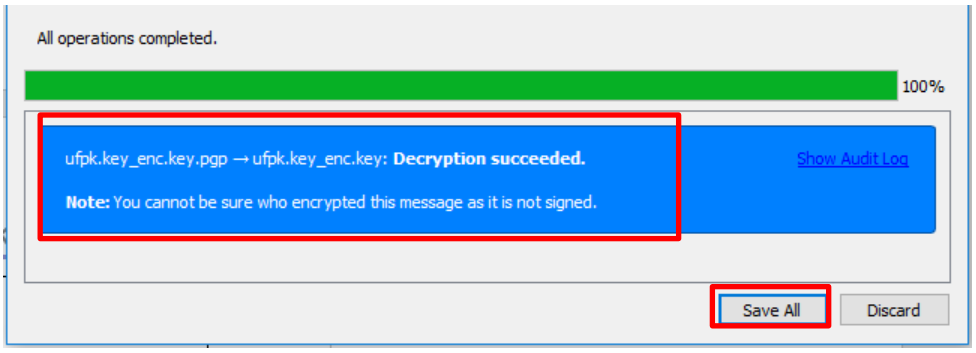

Figure 47. Decrypt with PGP Private Key

4.5 Generate the DLM Key Injection File

To inject a DLM key, the key needs to be wrapped by the UFPK, and both the wrapped DLM Key and the W-UFPK need to be sent to the MCU via the serial programming interface. The SKMT can create an RFP key injection file, which includes the wrapped DLM key and W-UFPK.

4.5.1 Wrap DLM Keys Using SKMT GUI

This section shows how to create key injection files for the SECDBG_KEY and NONSECDBG_KEY using the GUI interface.

4.5.1.1 Wrap a SECDBG_KEY

Launch the SKMT GUI and select the **Wrap Key** tab. Then open the **Key Type** tab and select **DLM**. First, we will inject the SECDBG_KEY, so select **DLM-SSD**.

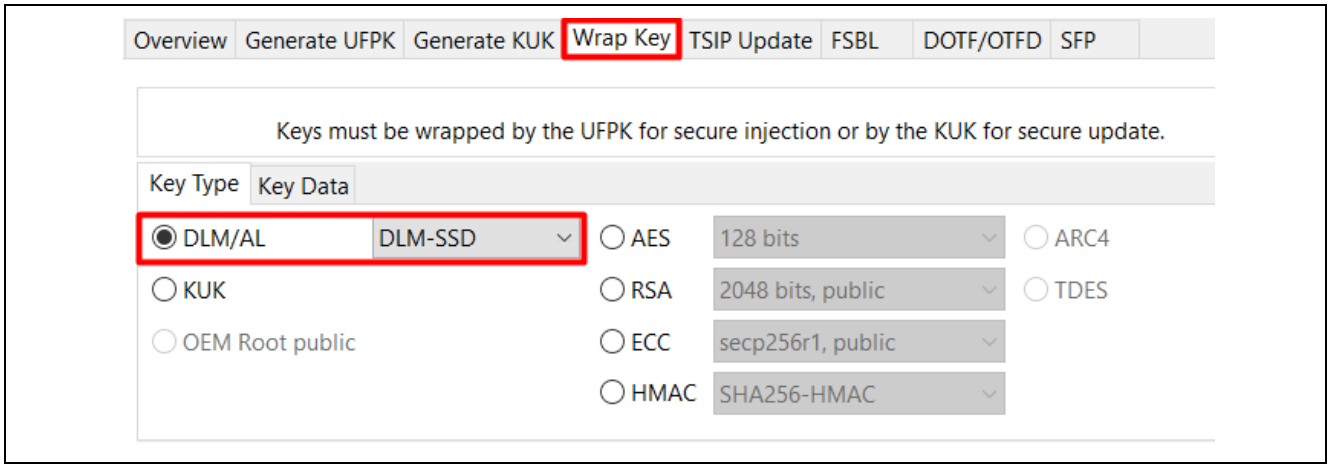

Figure 48. Choose DLM-SSD as the Key Type

Select the **Key Data** tab. For this example, we will specify the key as raw data by selecting **Raw** and entering the key value (`000102030405060708090A0B0C0D0E0F`), but we could also specify a binary key file containing the key value.

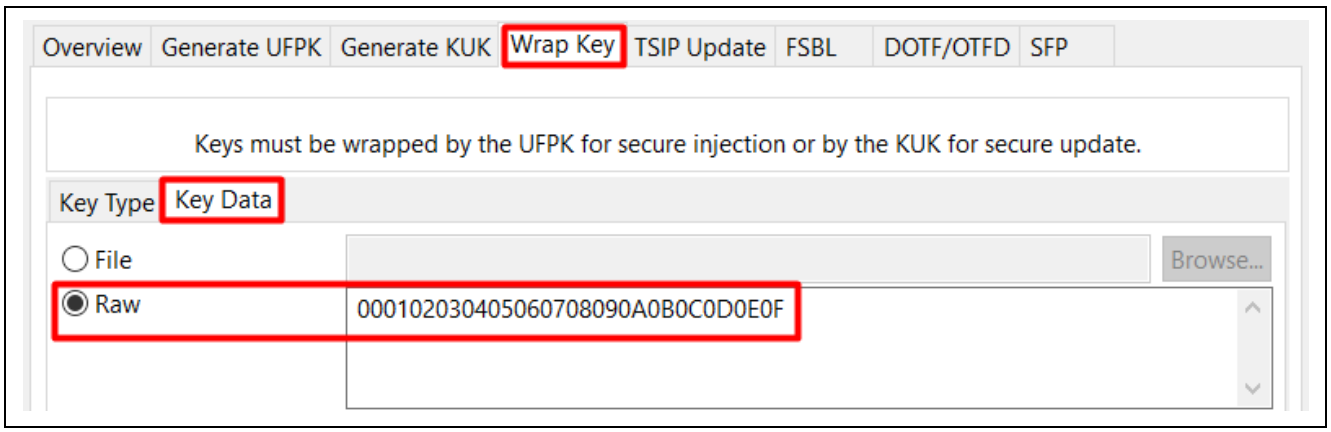

Figure 49. Enter the SECDBG_KEY Data

In the **Wrapping Key** section, click the corresponding **Browse** buttons to select the **UFPK** and **W-UFPK** key pair created in section 4.4.2 and 4.4.6. Choose **Generate random value** for the IV. In the **Output** section, select **RFP** and click the **Browse** button to enter the output file name.

Now click the **Generate File** button. The Renesas key file (`SECDBG.rkey`) will be generated.

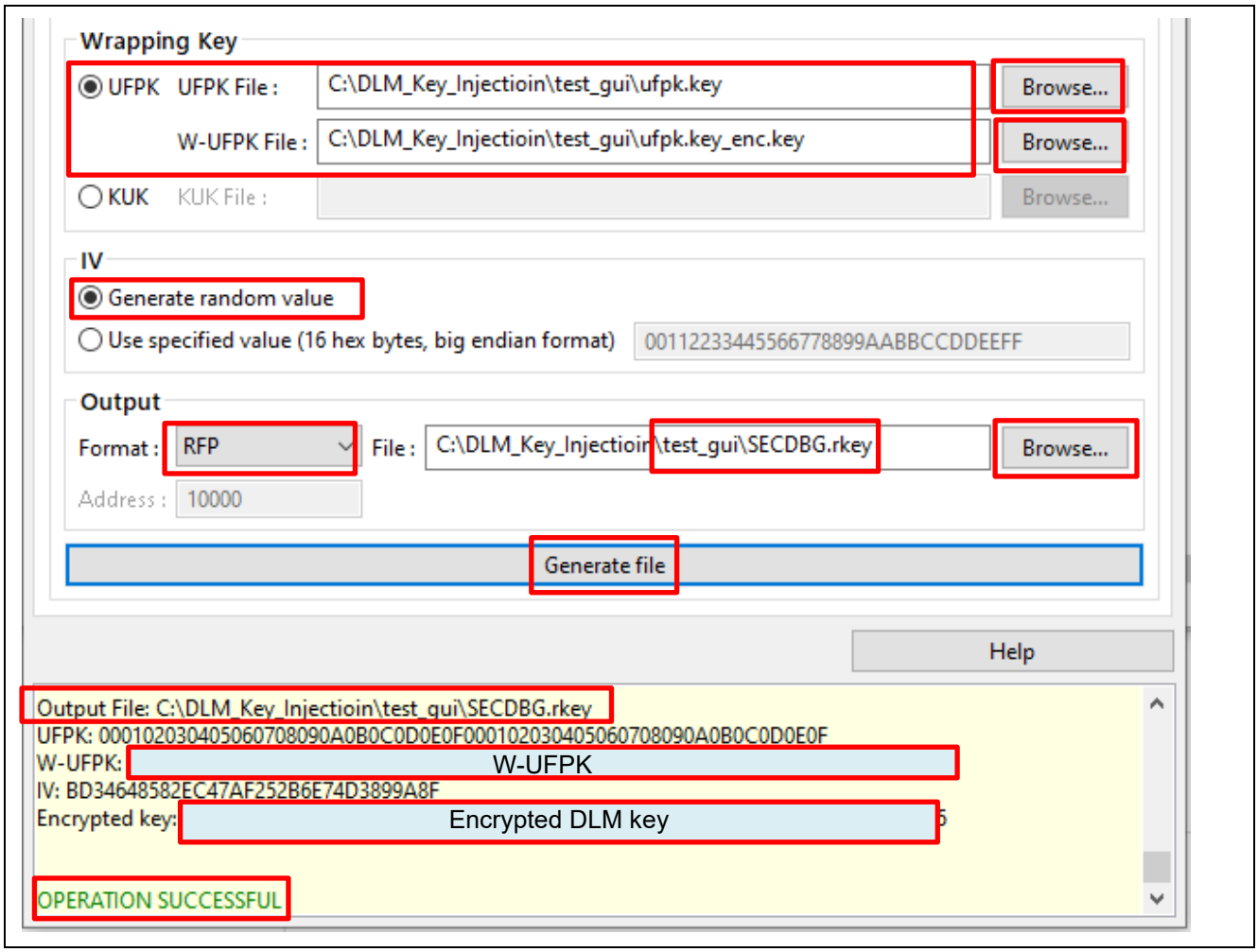

Figure 50. Generate the RFP Injection File for the SECDBG_KEY

The plaintext DLM key and UFPK are NOT contained in the `*.rkey` file, enabling confidential transfer of the key injection file.

4.5.1.2 Wrap a NONSECDBG_KEY

Select **DLM-NSECSD** in the **Key Type** tab.

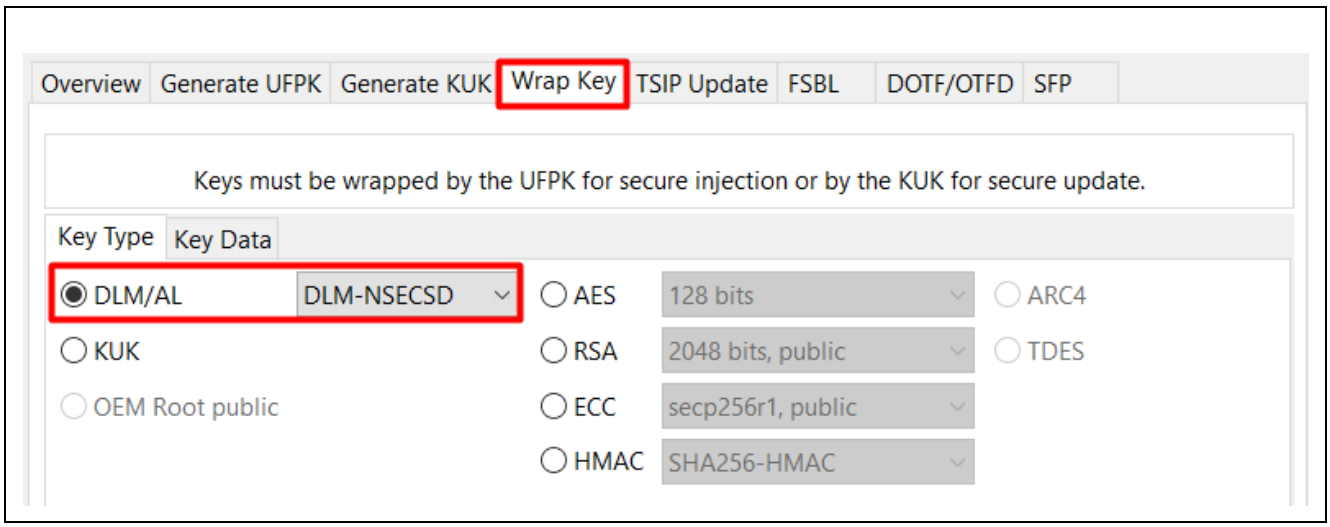

Figure 51. Choose DLM-NSECSD as the Key Type

Enter the data for the key value on the **Key Data** tab. Again, for this example, we will specify raw data for the key (`0101020304050607080090A0B0C0D0E0F`).

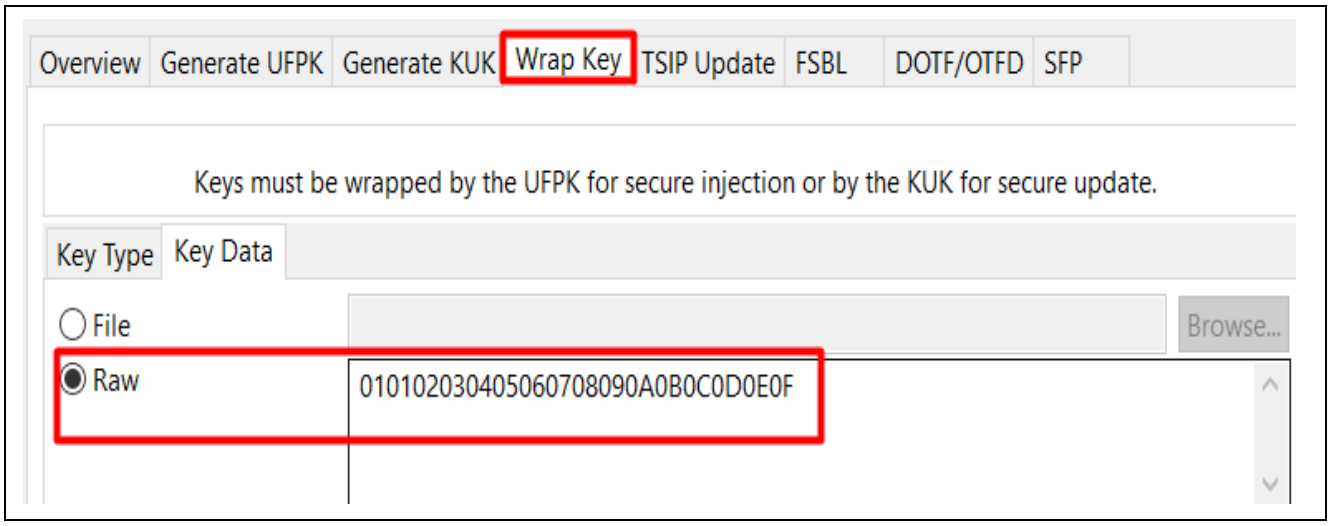

Figure 52. Enter the NONSECDBG_KEY Data

In the **Wrapping Key** section, click the corresponding **Browse** buttons to select the **UFPK** and **W-UFPK** key pair created in section 4.4.2 and 4.4.6. Choose **Generate random value** for the IV. In the **Output** section, select **RFP**, and click the **Browse** button to enter the output file name.

Now click the **Generate File** button. The Renesas key file (`NON-SECDBG.rkey`) will be generated.

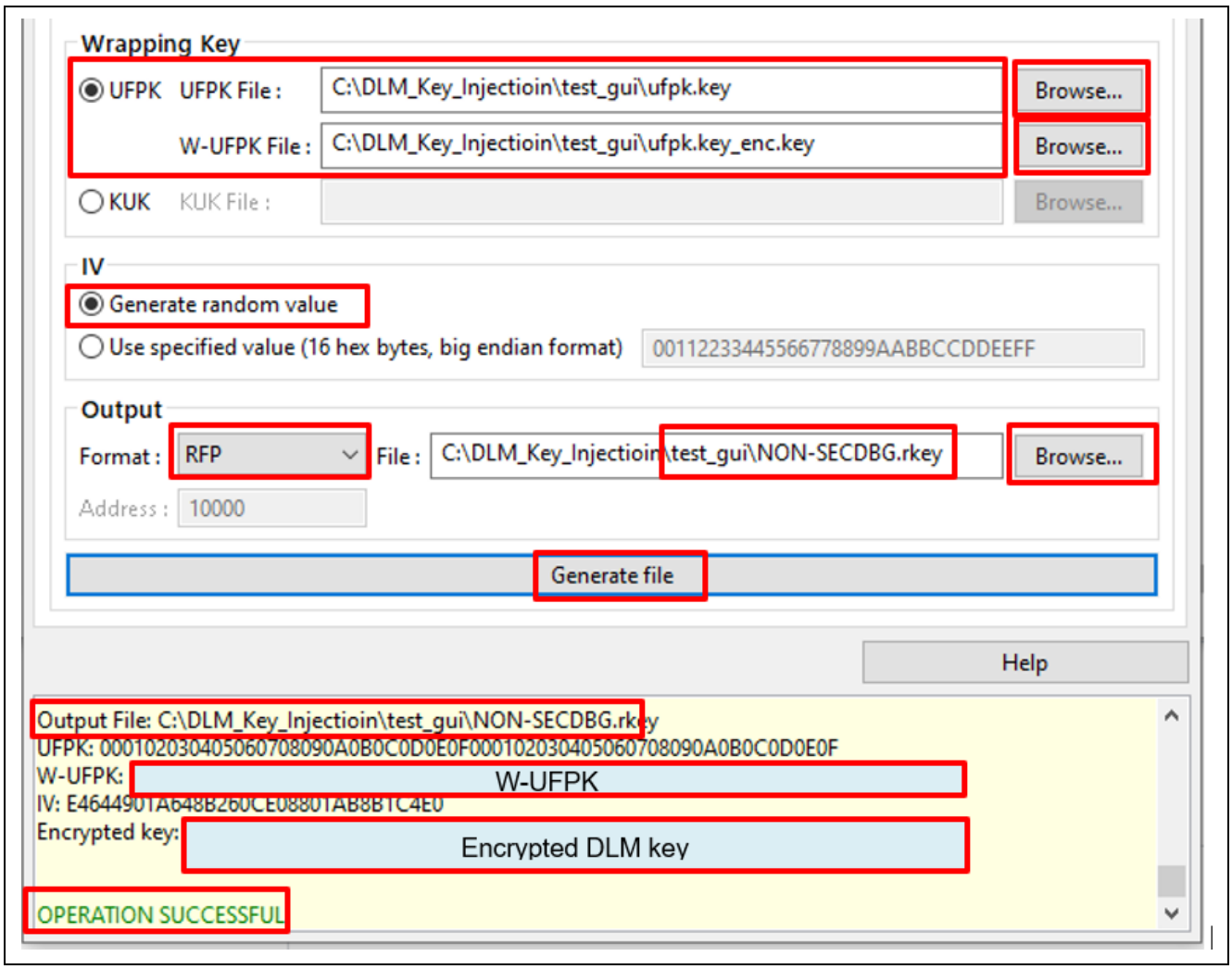

Figure 53. Generate the RFP Injection File for the NONSECDBG_KEY

4.5.2 Wrap DLM Keys Using SKMT CLI

This section shows how to create key injection files for the SECDBG and NON_SECBUG keys using the CLI interface.

4.5.2.1 Wrap a SECDBG_KEY

In a Command Prompt window, use the following command to create the SECDBG_KEY key injection file (`SECDBG_CLI.rkey`). Refer to the Security Key Management Tool user manual for more details about the `/genkey` command.

```
skmt.exe /genkey /ufpk file="C:\DLM_Key_Injection\test\ufpk.key" /wufpk
file="C:\DLM_Key_Injection\test\ufpk.key_enc.key" /mcu "RA-SCE9" /keytype
"DLM-SSD" /key "000102030405060708090A0B0C0D0E0F " /filetype "rfp" /output
"C:\DLM_Key_Injection\test_cli\SECDBG_CLI.rkey"
```

In this example:

- The UFPK key file created earlier is specified.
- The decrypted W-UFPK file received from the Renesas key wrap service is specified.
- The MCU selection for the RA6M4 is `RA-SCE9`, as per the options specified in the Security Key Management Tool User's Manual.
- The selected key type is `DLM-SSD`, as per the options specified in the Security Key Management Tool User's Manual.
- The DLM key value is `000102030405060708090A0B0C0D0E0F`. When this operation is performed for production hardware, this value should be replaced by a unique value and managed in a secure environment.
- Secure key injection on the RA6M4 is performed over the serial programming interface, so we must select `RFP` for the output file type.
- We are using a randomly generated IV. The IV is updated in each encryption instance, so if you execute this command multiple times, you will generate a different key injection file each time.
- The Renesas Key File that can be used by RFP to securely inject the DLM key will be generated as `SECDBG_CLI.rkey`.

The command will generate the Renesas key file as shown below.

```
Output File: C:\DLM_Key_Injection\test_cli\SECDBG_CLI.rkey
UFPK: 000102030405060708090A0B0C0D0E0F000102030405060708090A0B0C0D0E0F
W-UFPK: [W-UFPK]
IV: 5AAF97858C3769AF470840419878C665
Encrypted key: [Encrypted SECDBG_KEY]
```

Figure 54. Create the SECDBG_KEY Injection File

The generated key file `SECDBG_CLI.rkey` can be used with RFP to inject the wrapped DLM SECDBG_KEY. This file contains the wrapped DLM key along with the W-UFPK. The plaintext SECDBG_KEY and UFPK are NOT contained in the `*.rkey` file, enabling confidential transfer of the key injection file.

4.5.2.2 Wrap a NONSECDBG_KEY

Similarly, use the following command to generate the NONSECDBG_KEY key injection file.

```
skmt.exe /genkey /ufpk file="C:\DLM_Key_Injection\test\ufpk.key" /wufpk
file="C:\DLM_Key_Injection\test\ufpk.key_enc.key" /mcu "RA-SCE9" /keytype
"DLM-NSECSD" /key "010102030405060708090A0B0C0D0E0F " /filetype "rfp" /output
"C:\DLM_Key_Injection\test_cli\NON-SECDBG_CLI.rkey"
```

This example is identical to the previous example, with the following differences:

- A different value is used for the DLM key (`01010203040506070809 0A0B0C0D0E0F`).
- The selected key type is `DLM-NSECSD`, as per the options specified in the *Security Key Management Tool User's Manual*.
- The Renesas Key File that can be used by RFP to securely inject the DLM key will be generated as `NON-SECDBG_CLI.rkey`.

The command will generate the Renesas key file, as shown below.

```
Output File: C:\DLM_Key_Injection\test_cli\NON-SECDBG_CLI.rkey
UFPK: 000102030405060708090A0B0C0D0E0F000102030405060708090A0B0C0D0E0F
W-UFPK: (  W-UFPK
IV: 032B697B6FAF0C22071087035AC810FE
Encrypted key:  Encrypted NONSECDBG_KEY
```

Figure 55. Create the Non-SECDBG DLM Key Injection File

The generated key file `NON-SECDBG_CLI.rkey` can be used with RFP to inject the wrapped DLM NONSECDBG_KEY. This file contains the wrapped DLM key along with the W-UFPK. The plaintext NONSECDBG_KEY and UFPK are NOT contained in the `*.rkey` file, enabling confidential transfer of the key injection file.

4.5.2.3 Wrap an RMA_KEY

Similarly, the user can generate an RMA_KEY key injection file using the following command line input:

```
skmt.exe /genkey /ufpk file="C:\DLM_Key_Injection\test\ufpk.key" /wufpk
file="C:\DLM_Key_Injection\test\ufpk.key_enc.key" /mcu "RA-SCE9" /keytype
"DLM-RMA-REQ" /key "02010203040506070809 0A0B0C0D0E0F" /filetype "rfp" /output
"C:\DLM_Key_Injection\test_cli\RMA_CLI.rkey"
```

This example is identical to the previous example, with the following differences:

- A different value is used for the DLM key (`02010203040506070809 0A0B0C0D0E0F`).
- The selected key type is `DLM-RMA-REQ`, as per the options specified in the *Security Key Management Tool User's Manual*.
- The Renesas Key File that can be used by RFP to securely inject the DLM key will be generated as `RMA_CLI.rkey`.

The command will generate the Renesas key file, as shown below.

```
Output File: C:\DLM_Key_Injection\test_cli\RMA_CLI.rkey
UFPK: 000102030405060708090A0B0C0D0E0F000102030405060708090A0B0C0D0E0F
W-UFPK:  W-UFPK
IV: FE978448A629E1997B03B2B943D95ED8
Encrypted key:  Encrypted RMA_KEY
```

Figure 56. Create the RMA DLM Key Injection File

The generated key file `RMA_CLI.rkey` can be used with RFP to inject the wrapped RMA_KEY. This file contains the wrapped DLM key along with the W-UFPK. The plaintext RMA_KEY and UFPK are NOT contained in the `*.rkey` file, enabling confidential transfer of the key injection file.

4.6 Inject the DLM Keys

This section demonstrates how to perform DLM Key injection. For instructions on how to create an RFP project and establish connections to the target board, see the *RFP User's Manual*. This section provides key configuration settings for each of the state transitions.

4.6.1 Inject a SECDBG_KEY

A Secure Debug Key (SECDBG_KEY) and a Return Material Authorization Key (RMA_KEY) can be injected when the MCU is in an SSD state.

Note: DLM key injection is not mandatory. It is also not mandatory to inject all possible DLM keys if only specific DLM capabilities are required. Be sure to examine the DLM states and authenticated transitions to determine which, if any, DLM keys should be injected.

This example uses the SECDBG_KEY Renesas key file generated in the previous section as an example. The RMA_KEY can be injected simultaneously using the same general steps. \Since the transition to RMA_REQ using the RMA_KEY cannot be regressed, it is not demonstrated here.

Unzip `ra6m4_dlm_key_inject_rfp.zip` to reveal `ra6m4_dlm_key_inject.rpj`. Launch RFP and open the RFP project `ra6m4_dlm_key_inject.rpj` and review the settings explained in this section.

On the **Flash Options** tab, under **DLM Keys**, the option **Set Option** is set to **Set**. Click on **Encrypted SECDBG Key,** and then click the "**…**" to the right to select the corresponding SECDBG_KEY key file.

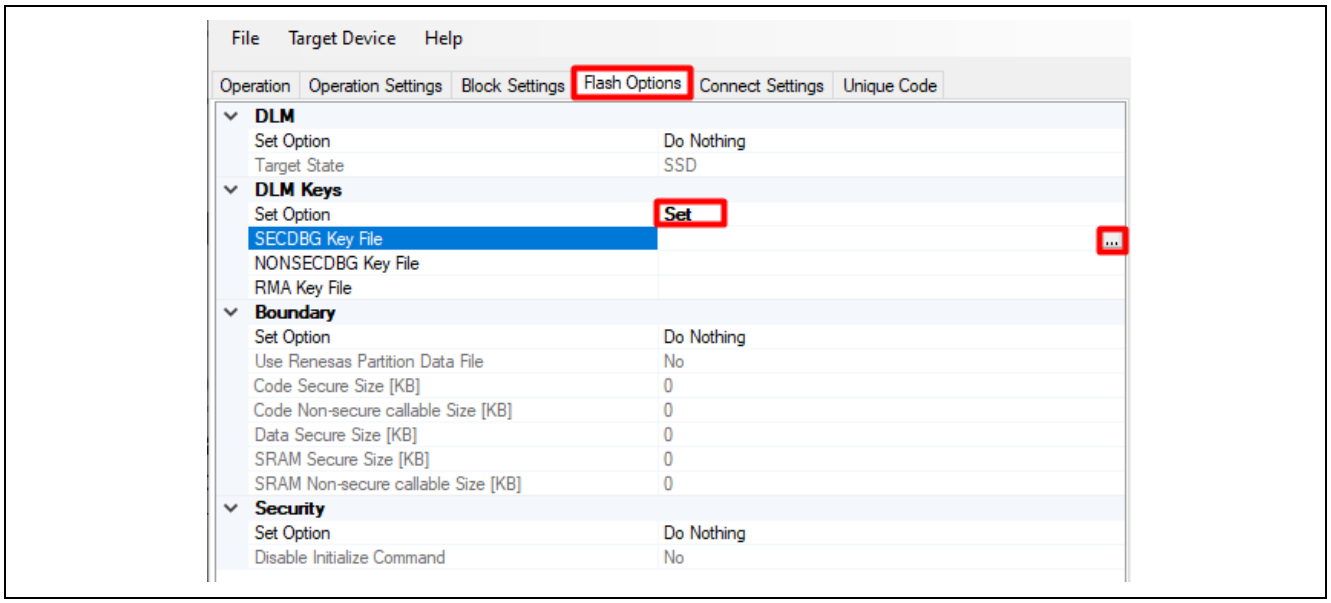

Figure 57. Select the SECDBG_KEY to Inject

Select the `SECDBG.rkey` generated in section 4.5.

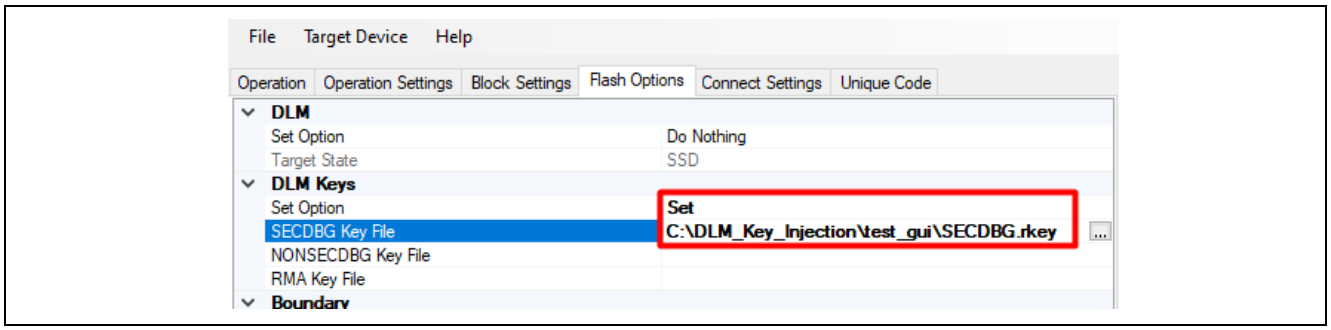

Figure 58. Select the SECDBG_KEY to Inject

Under the **Operation Settings** tab, **Program Flash Options** and **Verify Flash Options** are selected.

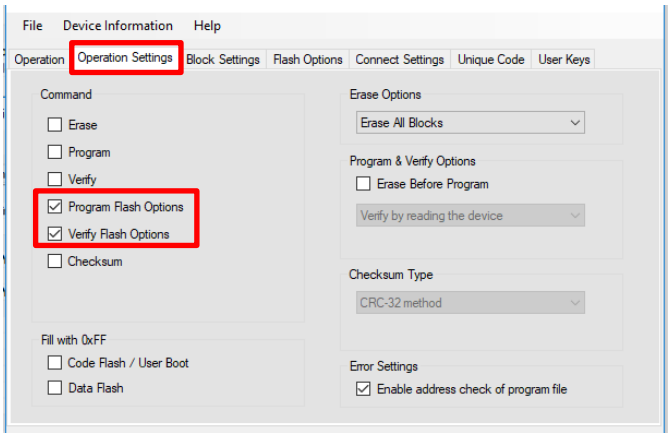

Figure 59. Select Program and Verify Flash Option Setting

DLM keys and program code can be programmed at the same time or separately. Unzip `test.zip` to reveal `bare_metal_minimal_s.srec` and `bare_metal_minimal_ns.srec`. These binaries are included with this application note for demonstration, but **Program File** can also be left blank and **Code Flash** unchecked in the **Block Settings** tab.

Navigate to the **Operation** tab, select the secure application binary (if desired), and click **Start** to program the data.

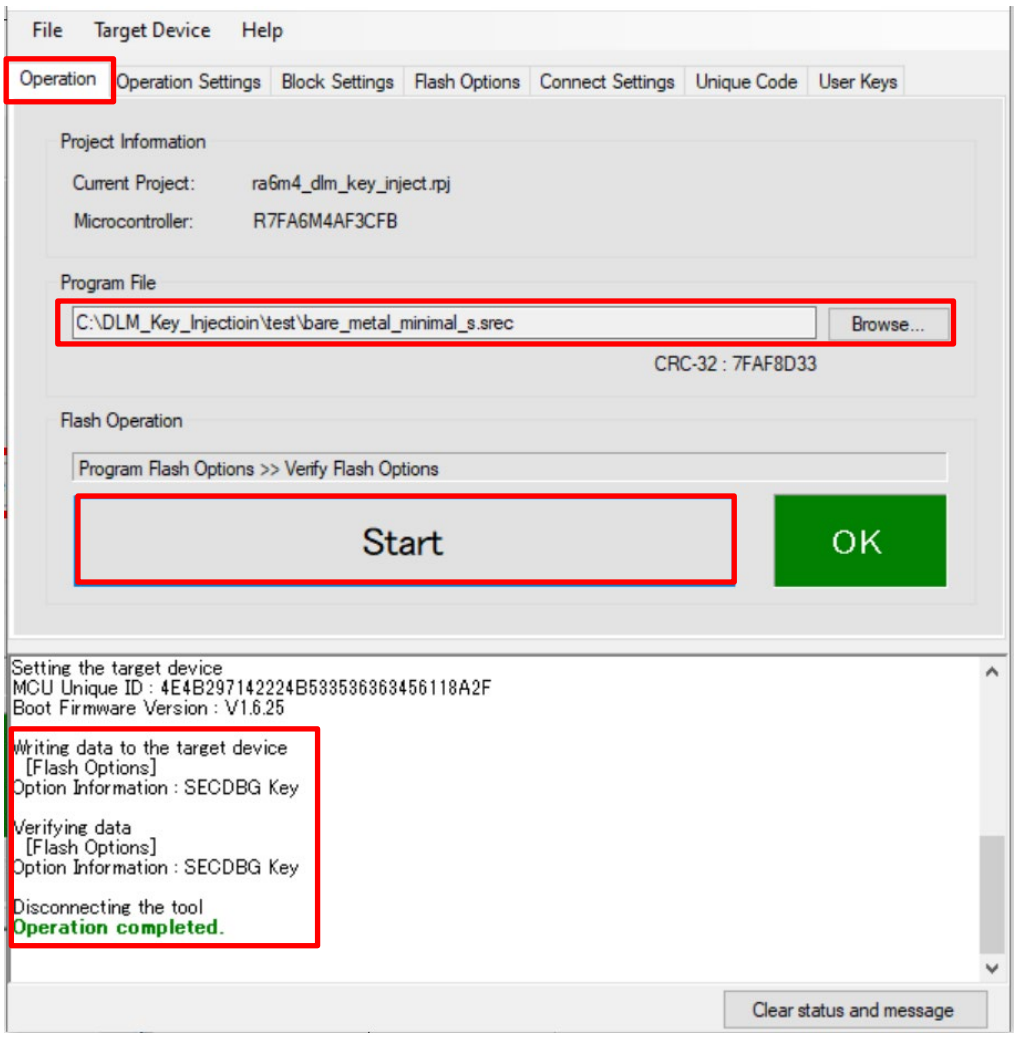

Figure 60. Inject the SECDBG_KEY

4.6.2 Inject a NONSECDBG_KEY

The NONSECDBG_KEY DLM key must be injected when the MCU is in the NSECSD DLM state. Transition the MCU to this state as shown below.

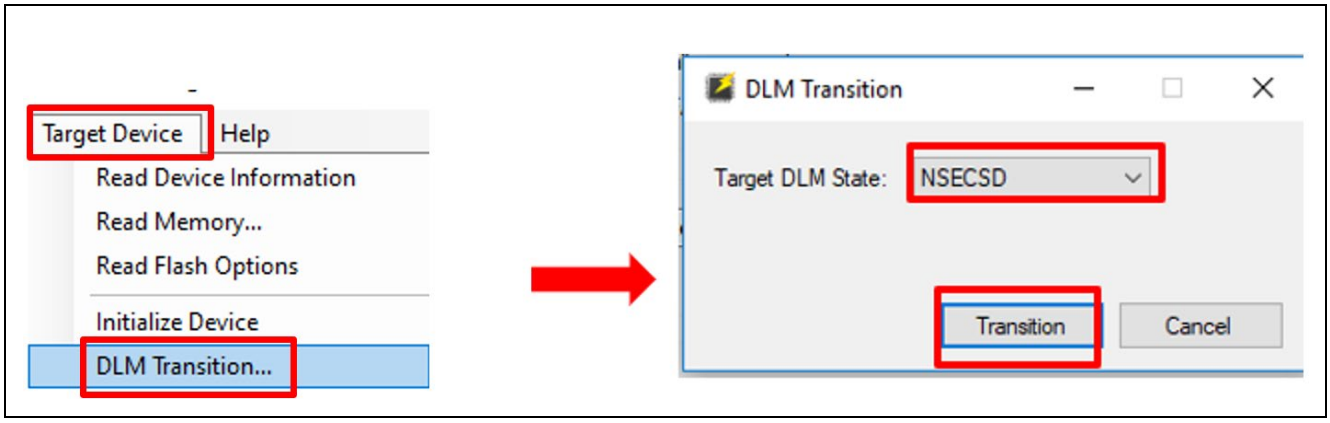

Figure 61. Transition the MCU Device Lifecycle State to NSECSD

Next, follow similar steps as in section 4.6.1 to inject the NONSECDBG_KEY DLM Key. In this example, we can use the NONSECDBG_KEY (`NONSECDBG.rkey`) generated from Figure 53 to illustrate the operation.

Add the NONSECDBG_KEY key file entry as shown below. Note that any SECDBG and RMA key file entries must be deleted.

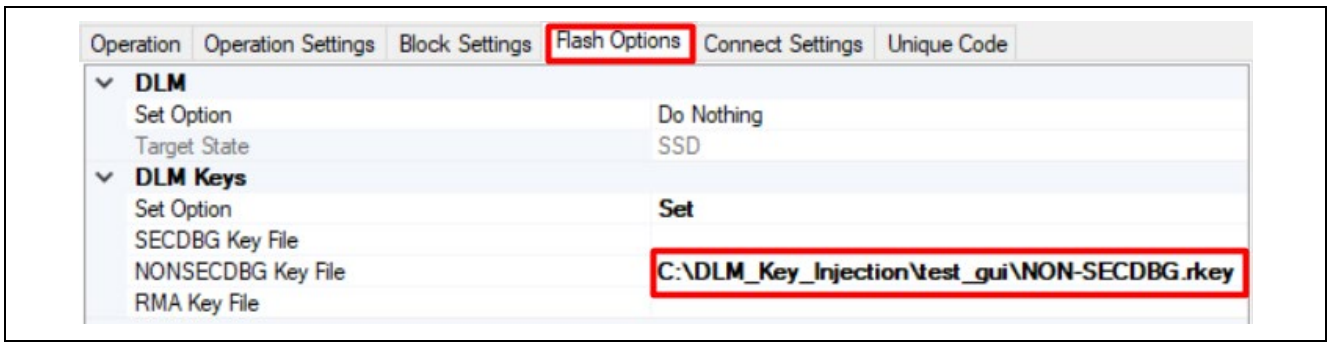

Figure 62. Select the NONSECDBG_KEY to Inject

DLM keys and program code can be programmed at the same time or separately. Unzip `test.zip` to reveal `bare_metal_minimal_s.srec` and `bare_metal_minimal_ns.srec`. These binaries are included with this application note for demonstration, but **Program File** can also be left blank and **Code Flash** unchecked in the **Block Settings** tab.

Navigate to the **Operation** tab, select the non-secure application binary (if desired), and click **Start** to program the data.

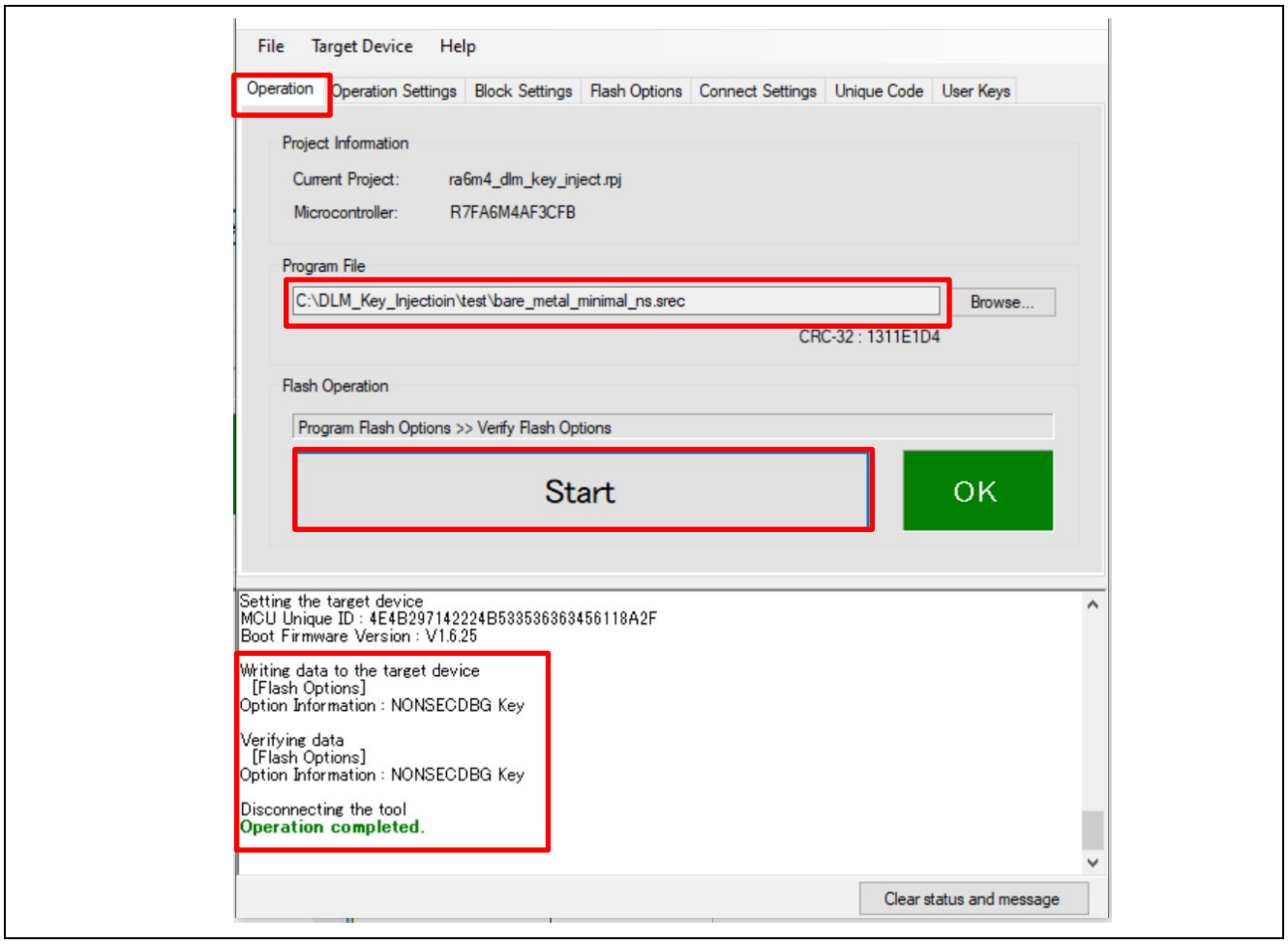

Figure 63. Inject the NONSECDBG_KEY Using RFP

To test the authenticated DLM State regression as shown in section 5, Authenticated DLM State Transitions, we can advance the DLM state to DPL.

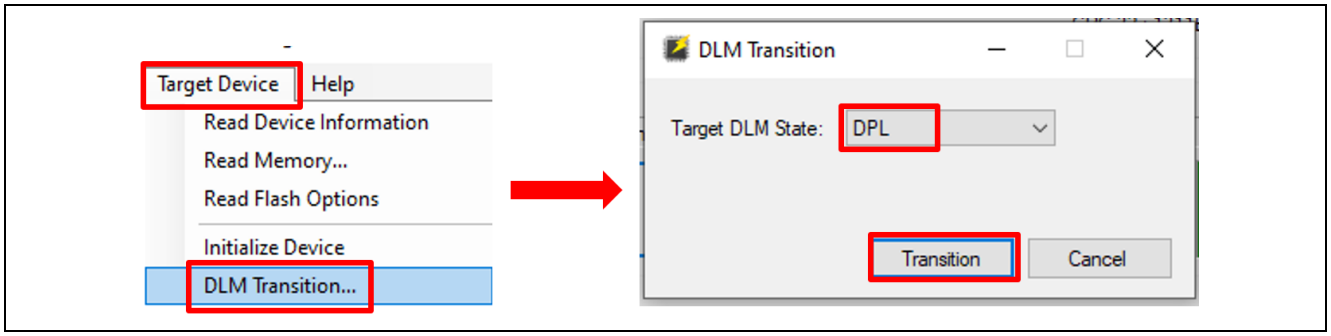

Figure 64. Transition the MCU Device Lifecycle State to DPL

The MCU Device Lifecycle State can be confirmed by reading out the Device Information as shown in the following graphic.

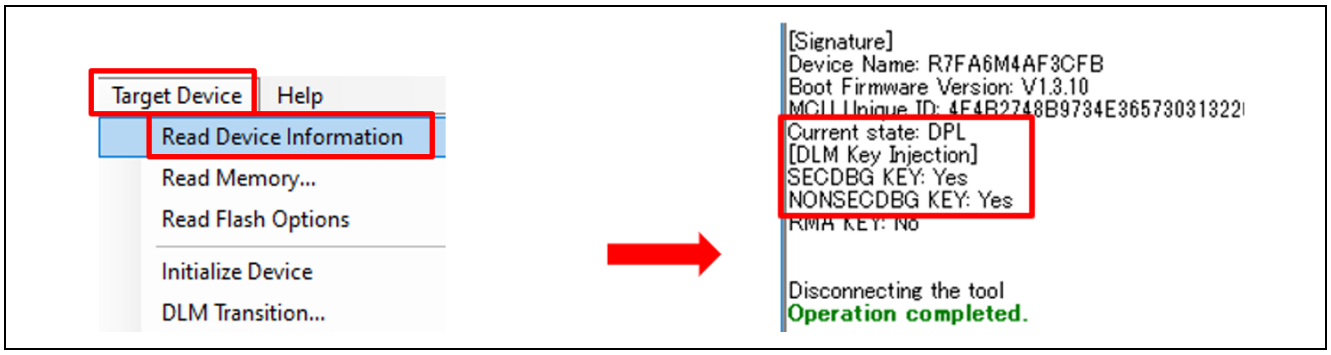

Figure 65. Confirm the DPL Device Lifecycle State

5. Authenticated DLM State Transitions

This section provides operational steps for authenticated DLM state transitions using RFP. The assumption is that SECDBG_KEY and NONSECDBG_KEY are already injected using the previous examples.

Note: For practice purposes, you can inject an RMA_KEY. However, unless you are returning the MCU to Renesas for failure analysis, DO NOT transition to the RMA_REQ state using the RMA_KEY. Once transitioned to RMA_REQ state, the **Initialize** command is disabled, and the MCU will be locked out of debugging and serial reprogramming capabilities.

5.1 Authenticated Transition from Deployed State to Non-secure Debug State

As explained in section 1.2, if the DLM state is DPL and a NONSECDBG_KEY has been injected, it is possible to regress the MCU device lifecycle state from DPL to NSECSD and retain flash memory contents.

Use the following steps to regress the Device Lifecycle State to NSECSD.

1. Select to transition to **NSECSD**.

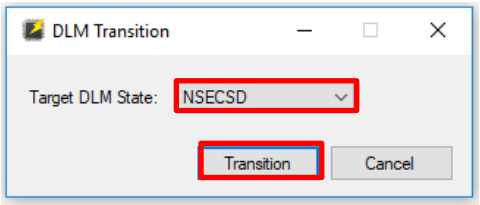

Figure 66. Select Transition to NSECSD

2. If the device is in the DPL state and a NONSECDBG_KEY has been injected on the MCU, the following prompt will pop up. Follow the prompt to provide the NONSECDBG_KEY and then click **OK**. In the example shown here, this is `01010203040506070809 0A0B0C0D0E0F`.

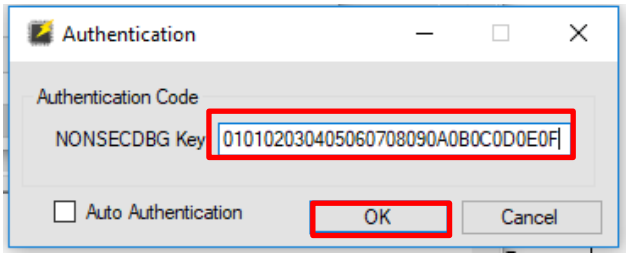

Figure 67. Provide the Authentication Key for NONSECDBG

3. You can now confirm that the Device Lifecyle State is regressed back to NSECSD state.

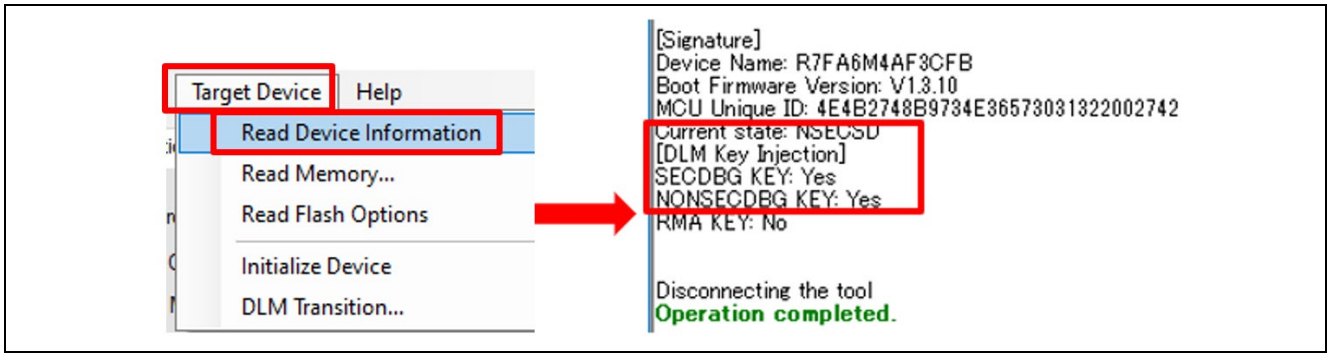

Figure 68. Confirm the MCU is in NSECSD and SECDBG_KEY is Injected

5.2 Authenticated Transition from Non-secure Debug State to Secure Debug State

When the device is in the NSECSD DLM state, and a SECDBG_KEY has been injected, the following steps will regress the MCU to the SSD DLM state without erasing flash memory:

1. Select to transition to **SSD**.

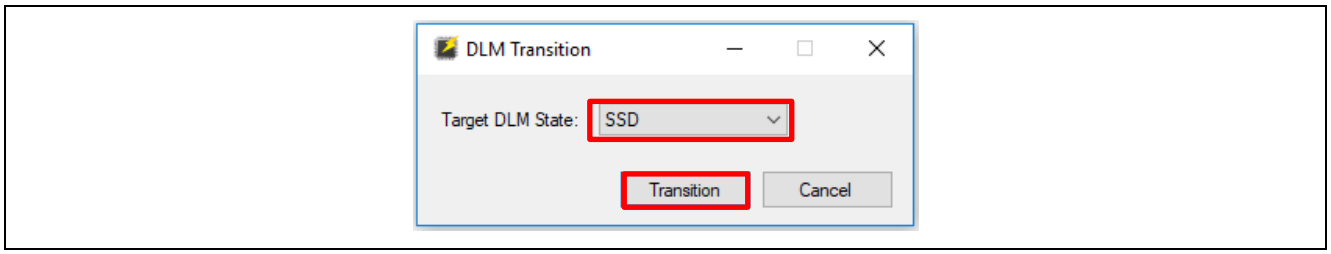

Figure 69. Select Transition to SSD

2. If the Device is in NSECSD state and a SECDBG_KEY has been injected on the MCU, the following prompt will pop up. Follow the prompt to provide the SECDBG_KEY and then click **OK**. In the example shown here, this is `000102030405060708090A0B0C0D0E0F`.

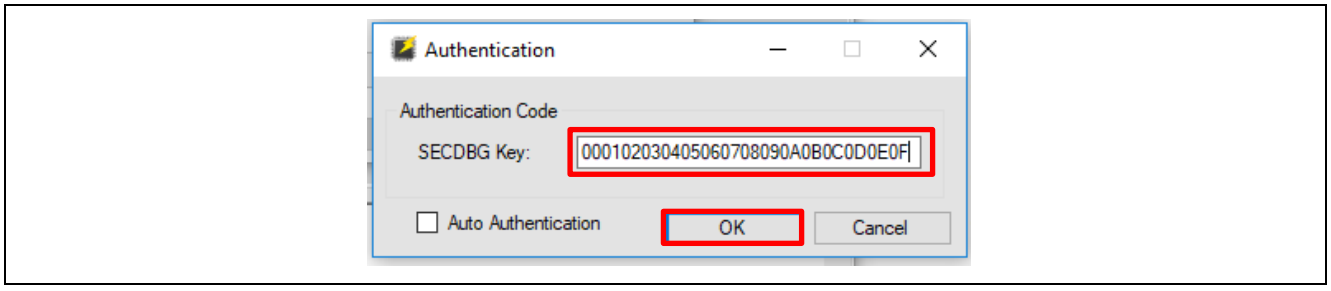

Figure 70. Provide the Authentication Key for SECDBG

3. If the DLM state regression is successful, the following output will be printed.

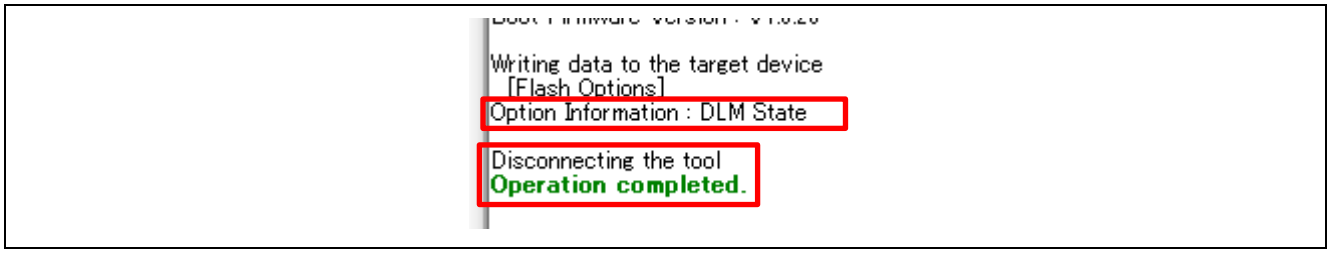

Figure 71. RFP Output with DLM State Regression

4. You can now confirm that the Device Lifecycle State has transitioned back to SSD state.

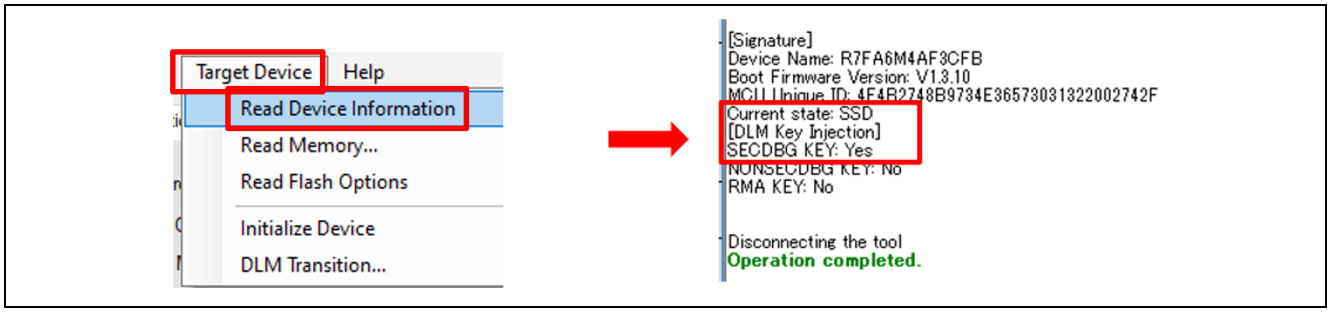

Figure 72. Confirm Device Lifecycle has Transitioned Back to SSD

6. References

- Renesas RA Family Secure Key Injection and Update Application Project (R11AN0496)
- Renesas RA6M4 Group User's Manual: Hardware
- Flexible Software Package (FSP) User's Manual

7. Appendix

7.1 Glossary

Term	Meaning
SCE9	Secure Crypto Engine 9 is a hardware unit that resides on Renesas Arm® Cortex®-M33 MCU
Device Certificate	Certificate uniquely identifying an individual device. It is digitally signed, asserting that the certificate comes from a known source and has not been modified and that the device is trusted.
Root of Trust	Roots of trust are highly reliable hardware, firmware, and software components that perform specific, critical security functions. (https://csrc.nist.gov/projects/hardware-roots-of-trust)
SCE	Secure Crypto Engine – A module in the MCU that provides for efficient, low-power cryptographic acceleration, TRNG (True Random Number Generation), and creation and isolation of cryptographic keys.
PKI	Public Key Infrastructure – A set of roles, policies, and procedures needed to create, manage, distribute, use, store, and revoke digital certificates, which are typically used to manage secure identity via public key cryptography.
Key Pair	Asymmetric keys are generated in pairs – a public and private key. The private key is held in secret by only one party and can be used to assert that party's identity. The public key is freely distributed and is uniquely associated with the private key.
Secure Code	A function or group of functions that resides in a secure region of internal flash, as defined and enforced by the MPUs. These secure functions can access both secure data and non-secure data regions.
Non-Secure code	A function or group of functions that resides in a non-secure region of internal flash. These non-secure codes cannot access the secure region. They can only access the non-secure region.
HUK	Hardware Unique Key. This is a key stored inside the RA Family MCU that is unique to every individual MCU.
Challenge String	Randomly generated string at the host application. This string is used by the host application to validate the ownership of the private key by the target.
Unique ID	An identification value, unique to each individual RA Family MCU is stored inside the MCU.
Challenge Response String	The response to the challenge string. The Challenge Response String is the signature of the challenge data as created by signing the Challenge String with the receiver's private key.

Website and Support

Visit the following URLs to learn about key elements of the RA family, download components and related documentation, and get support.

EK-RA6M4 Resources	renesas.com/ra/ek-ra6m4
RA Product Information	renesas.com/ra
RA Product Support Forum	renesas.com/ra/forum
RA Flexible Software Package	renesas.com/FSP
Renesas Support	renesas.com/support

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Oct.01.20	—	First release document
1.10	Dec.09.20	—	Updated importing Renesas Public Key to Kleopatra procedure
2.00	June.23.22	—	Updated to use Security Key Management Tool (SKMT)
2.10	Sep.10.24	—	Update with environment FSP version 5.5.0

General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Precaution against Electrostatic Discharge (ESD)

   A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity. Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.
2. Processing at power-on

   The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power reaches the level at which resetting is specified.
3. Input of signal during power-off state

   Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.
4. Handling of unused pins

   Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.
5. Clock signals

   After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.
6. Voltage application waveform at input pin

   Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between V_{IL} (Max.) and V_{IH} (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between V_{IL} (Max.) and V_{IH} (Min.).
7. Prohibition of access to reserved addresses

   Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.
8. Differences between products

   Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,
Koto-ku, Tokyo 135-0061, Japan
www.renesas.com

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