

ISL9122 Family

This manual provides installation procedures, features, and user information for the graphical user interface (GUI) evaluation software used by the ISL9122 product family.

Contents

1.	Syst	em Requ	uirements			
2.	Soft	ware Inst	tallation			
3.	Usin	g the So	ftware			
	3.1	Open S	Software			
	3.2	Evaluat	tion Software Initialization			
3.3 Device/Slave Address						
	3.4	ISL912	2 Specific GUI Tab and User Controls6			
		3.4.1	IC Control			
	3.5	Supple	mentary Support Features			
		3.5.1	File Menu			
		3.5.2	HID Status and Connection			
		3.5.3	I ² C ACK/NACK			
		3.5.4	Product (IC) Selection			
		3.5.5	Find Device/Slave Address			
		3.5.6	Write All			
		3.5.7	Read All			
		3.5.8	I ² C Log			
	3.6	Genera	ıl Tab			
		3.6.1	Manual Transaction			
		3.6.2	USB Dongle Setup			
		3.6.3	16-Bit Converter Tool			
		3.6.4	Register Dump			
4.	Soft	ware Uni	install Process			
5.	Trou	bleshoo	ting Tips			
6.	Revi	sion His	tory16			



1. System Requirements

Following are the minimum system requirements for the evaluation software

- Operating System PC or virtual Windows machine with Windows 7 or greater
- .NET Framework 4.5 or greater, and 3.5 (CLR 2)
- **RAM** 30MB RAM
- Hard Disk 10MB free disk space

Note: No special drivers are required for the Renesas USB Dongle (ISLUSBMINIZEVAL1Z), it operates as a Human Interface Device (HID).

2. Software Installation

The following steps describe installing the PC software.

Note: This evaluation software is a single evaluation tool for the ISL9122 product family.

- 1. Download the installer file from the website or from the USB drive.
- 2. The installer file named ISL9122_Installer_vX.Y.Z.exe, where X.Y.Z is the version number of the evaluation software, will be downloaded into the system or user designated download folder.
- 3. Run the installer file.
- 4. Follow the installation wizard to install the evaluation software.
- 5. Read the license agreement, choose the accept option, and click **Next** to continue with the evaluation software installation.

🛃 Setup - ISL9122 Evaluation Software	_		×
License Agreement Please read the following important information before continuing.		Q	
Please read the following License Agreement. You must accept the te agreement before continuing with the installation.	erms of t	his	
EVALUATION TOOL LICENSE AGREEM	ENT	^	
IT IS IMPORTANT THAT YOU CAREFULLY RI UNDERSTAND THIS AGREEMENT. BY CLICKI ACCEPT" BUTTON, YOU AGREE TO BE BOUND BY THE TERMS OF THIS AGREEMENT DO NOT AGREE WITH ALL THE TERMS	NG TH LEGAI . IF Y OF 1	E "I LLY YOU THIS	
○ I accept the agreement			
 I do not accept the agreement 			
Next	:>	Can	icel

Figure 1. License Agreement



6. The default directory is C:\Program Files (x86)\Renesas\ISL9122. To change the default folder, click the **Browse** button. Click **Next** to continue to Select Start Menu Folder.

🔀 Setup - ISL9122 Evaluation Software -	-		×					
Select Destination Location Where should ISL9122 Evaluation Software be installed?			J					
Setup will install ISL9122 Evaluation Software into the following folder.								
To continue, click Next. If you would like to select a different folder, clic	k Brow	se.						
C:\Program Files (x86)\Renesas\ISL9122	B <u>r</u> ov	vse						
At least 2.4 MB of free disk space is required.								
< <u>B</u> ack <u>N</u> ext >		Canc	el					

Figure 2. Select Evaluation Software Installation Folder

7. To change the default location, click the **Browse** button. Click **Next** to create the Renesas folder in the Start Menu. Click **Next** to continue to Ready to Install.

🔀 Setup - ISL9122 Evaluation Software	-		×
Select Start Menu Folder Where should Setup place the program's shortcuts?		G	
Setup will create the program's shortcuts in the following S	tart Menu	u folder.	
To continue, click Next. If you would like to select a different folder,	click Bro	wse.	
Renesas\ISL9122	Bro	owse	
< <u>B</u> ack <u>N</u> ex	t >	Can	cel

Figure 3. Select Start Menu Folder



8. Click **Install** to complete the installation process.

🔂 Setup - ISL9122 Evaluation Software —		×
Ready to Install Setup is now ready to begin installing ISL9122 Evaluation Software on your computer.		
Click Install to continue with the installation, or click Back if you want to revi change any settings.	ew or	
Destination location: C:\Program Files (x86)\Renesas\ISL9122		
Start Menu folder: Renesas\ISL9122		
<	>	
< Back Install	Ca	ncel

Figure 4. Ready to Install

9. Click **Finish** to exit setup. If the "Launch ISL9122" check box is checked, the evaluation software automatically runs.



Figure 5. Exit Setup



3. Using the Software

3.1 Open Software

Open ("run") the evaluation software from the Windows Start menu. Unless changed during the installation process, the program resides under the "Renesas" folder.

3.2 Evaluation Software Initialization

During initialization, an attempt is made to establish connection to the HID dongle (ISLUSBMINIEVAL1Z). If the connection is successful the main GUI form opens and the HID Status button becomes green. The default product selection is 9122A as displayed in Figure 6.

R ISL9122_Family Evaluation Software	- • ×
File Help	
HID Status Product (IC) Selection Device/Slave Address (7-bit hex) CONNECT 9122A 0x18 Find Slave Address IC Control General	
R0_REG1 (Reg 0x02) VSET (Reg 0x11) Check IC INFO VSET Control FAMILY_ID ISL3122 family HW_REV HW REV A RaiL_VAR 9122A INTFLG_REG (0x03) O Check Status OC_FAULT VSET UNDR VSET OVR OT FAULT OC_FAULT	CONV CFG (Reg 0x12) Soft Start EN_AND [7] DSCHG [6] Soft Start enabled Soft DSCHG enabled Soft Start inhibit Soft DSCHG disabled DVSRATE [5:4] FMODE [3:2] CTRL Type [0] 3.125 mV/us Normal operation Type I error amp Hiccup Mode EN_OR [5] Control by EN pin
Log Sr No Slave Add R/W Address Data Status Timestamp Load/Run Script Sr No Slave Add R/W Address Data Status Timestamp Load/Run Script User Notes: 1) Redo a Command: select an item/row using mouse click or arrow up/dn key, and Press < Christers to redo the command	

Figure 6. Default Evaluation Software UI

Figure 7 shows the Renesas HID dongle.

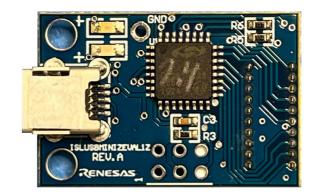


Figure 7. Renesas HID Dongle



If the connection attempt is unsuccessful a warning message appears (see Figure 8) prior to the main GUI form being displayed.

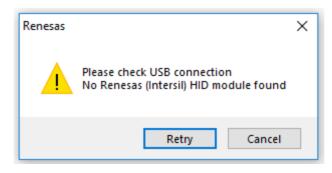


Figure 8. No HID Dongle Error Message

After the dongle is connected to the PC, click **Retry**; if successful the default GUI form displays (see Figure 6). Alternatively, clicking **Cancel** closes the warning message and opens the main GUI form to run in "off-line" mode, where no serial communication can occur until the HID connection is successfully (re)established.

3.3 Device/Slave Address

The first step after installing the evaluation software is to click **Find Slave Address**. If the Auto-read IC Info box is checked, the feature searches for an ISL9122 family device on the I²C bus, and sets the correct slave addresses to be used by the GUI. If the Auto-read IC Info box is unchecked, only the slave address of the IC connected is searched. The routine searches for an I²C acknowledge ("ACK") by attempting I²C commands with incrementing slave address from 0x00 to 0x7F. If an address is ACK'd, the routine does a qualified command to verify a qualified command to verify an ISL9122 family device. If an ISL9122 family device is located, it will read back the slave addresses programmed to the device, set those to the GUI, and a message box with the I²C slave address (see Figure 9). If the evaluation software cannot find the device on the bus, a message box with an error message appears (see Figure 16).

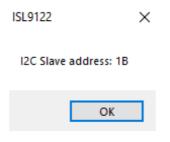


Figure 9. Slave Address Found

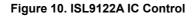
3.4 ISL9122 Specific GUI Tab and User Controls

3.4.1 IC Control

The IC Control tab has all the device specific registers. Figure 10 shows the IC control tab for the ISL9122A device. The IC Control tab for each product in the family varies slightly from the one shown below. The IC Control tab updates automatically as discussed in Product (IC) Selection. Details of the IC Control tab can be found in the product specific evaluation manual.



IC Control General RO_REG1 (Reg 0x02) Check IC INFO FAMILY_ID ISL9122 family	VSET (Reg 0x11) VSET Control	Write Reg	CONV CFG (Reg 0x12) Soft Start EN_AND [7] Soft Start enabled Soft Start inhibit Soft DSCHG deabled Soft DSCHG disabled	
HW_REV HW REV E RAIL_VAR ISL9122A	Min Max 132 DEC 0x84 HEX 3.3 V	Read Reg	DVSRATE [5:4] FMODE [3:2] 3.125 mV/us Normal operation	CTRL Type [0] Read Reg Type I error amp
INTFLG_REG (0x03) Check Status	INTFLG_MASK (0x Write	0C_FAULT [7:6]	Hiccup Mode V EN_OR [5] Control by EN pi	n v
VSET UNDR	OT FAULT OC FAULT Read			



3.5 Supplementary Support Features

The evaluation software has supplementary features such as data-logging and script execution, read and write of all registers, finding or changing a slave address, and others as shown in Figure 11. These features are accessible from both the IC Control and General tabs.

CONNECT	Product (IC) Select 9122A	~	Device/Slave 0x18		hex) Slave Address V Auto-re IC Info	ad		WRITE ALL		Interface I2C	ે રા	
RO_REG1 (Reg 0x) Check IC II AMILY_ID ISL912 HW_REV HW R RAIL_VAR ISL912	NFO 22 family EV D	VSET (R VSET Min	Control	DEC 0x84	HEX 33 V	Max	Write Reg Read Reg	CONV CFG (Reg 0x12) Soft Start EN_AND [7] Soft Start enabled Soft Start inhibit DVSRATE [5:4] 3.125 mV/us	DSCHG [6] Soft DSCHG enabled Soft DSCHG disabled FMODE [3:2] Normal operation	CTRL Type [0] Type II error amp	~	Write Reg Read Reg
						147.5	OC FAULT 17:61	Hiccup Mode V FI	N OR [5] Control by EN pir	1 ×		
Check Status			OT FAULT		C FAULT	Write Read	OC_FAULT [7:6]	fliccup Mode 🧹 El	N_OR [5] Control by EN pir	n V		
VSET UNDR		Address	OT FAULT	Status	Timestamp			ficcup Mode v El	N_OR [5] Control by EN pir	a v		
VSET UNDR	d R/W		Data 0x00 0x00 0x00 0x00		Timestamp 12C::2021/12/06 16:09:29 12C::2021/12/06 16:09:29 12C::2021/12/06 16:09:29	Read	Script	ficcup Mode v El	N_OR [5] Control by EN pir			
VSET UNDR Slave Ad 0x0F 0x10 0x11 0x12	d R/W Write Write Write	Address 0x02 0x02 0x02 0x02 0x02	Data 0x00 0x00 0x00 0x00 0x00	Status 1 1	Timestamp 120::2021/12/06 16:09:29 120::2021/12/06 16:09:29 120::2021/12/06 16:09:29 120::2021/12/06 16:09:29	Read	Script	ficcup Mode v El	N_OR [5] Control by EN pir	n v		
• Slave Ad 0x0F 0x10 0x11 0x12 0x13 0x14	d R/W Write Write Write Write Write	Address 0x02 0x02 0x02 0x02 0x02 0x02 0x02 0x	Data 0x00 0x00 0x00 0x00 0x00 0x00 0x00	Status 1 1	Timestamp 12C::2021/12/06 16:09:29 12C::2021/12/06 16:09:29 12C::2021/12/06 16:09:29 12C::2021/12/06 16:09:29 12C::2021/12/06 16:09:29 12C::2021/12/06 16:09:29	Read	Script	fliccup Mode v El	N_OR [5] Control by EN pir	1 V		
VSET UNDR	d R/W Write Write Write Write Write	Address 0x02 0x02 0x02 0x02 0x02 0x02 0x02 0x	Data 0x00 0x00 0x00 0x00 0x00 0x00 0x00 0	Status 1 1	Timestamp 120:2021/12/06 16:09:29 120:2021/12/06 16:09:29 120:2021/12/06 16:09:29 120:2021/12/06 16:09:29 120:2021/12/06 16:09:29 120:2021/12/06 16:09:29 120:2021/12/06 16:09:29 120:2021/12/06 16:09:29	Read	Script	ficcup Mode v El	N_OR [5] Control by EN pir	, v		
VSET UNDR lo Slave Ad 0x0F 0x10 0x11 0x12 0x13 0x14	d R/W Write Write Write Write Write	Address 0x02 0x02 0x02 0x02 0x02 0x02 0x02 0x	Data 0x00 0x00 0x00 0x00 0x00 0x00 0x00	Status 1 1	Timestamp 12C::2021/12/06 16:09:29 12C::2021/12/06 16:09:29 12C::2021/12/06 16:09:29 12C::2021/12/06 16:09:29 12C::2021/12/06 16:09:29 12C::2021/12/06 16:09:29	Read	Script	ficcup Mode v El	N_OR [5] Control by EN pir	, v		

Figure 11. Supplementary Features

3.5.1 File Menu

3.5.1.1 Save

The Save option saves all the register settings, as currently shown in the GUI, into a text file (see Figure 12).

File	Help	с			
	Save	Ctrl+S	uct (IC) Se	lection	Device/S
	Open	Ctrl+0	2A	~	0x18

Figure 12. File > Save

The text file generated has 8-bit data values in hexadecimal format for the 0x00 to 0xFF register space. Each line of the text file corresponds to register address (n - 1). For example, line one of the text file contains data from register address 0, and line 256d contains data from register address 255d (0xFF).

It is acceptable to manually add notes or comments to any line in a text load file, when they are placed *after* the 8-bit hexadecimal data value (two characters) – no special delimiters are required.

3.5.1.2 Open

The **Open** option (re)loads all the register settings from a properly formatted text load file into the evaluation software (see Figure 13) and updates the GUI display.

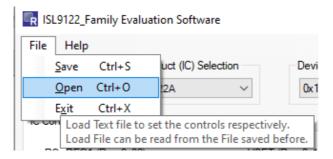


Figure 13. File > Open

Note: By default the evaluation software does not automatically write the data loaded from the text file into the IC registers. To write all register data click Write All after File > Open.

3.5.1.3 Exit

The **Exit** options closes the application window and exits the program. Functionally, this is the same as clicking the "X" in the upper right of the GUI window to close the program.

3.5.2 HID Status and Connection

The HID Status displays the state of the connection between the software/PC and Renesas HID dongle. The software automatically detects removal and insertion of the HID dongle to the PC. The **CONNECT** button is colored green when the HID dongle is connected and red when disconnected. Clicking the **CONNECT** button resets the HID connection and attempts to reestablish it.

If the dongle is disconnected while the evaluation software is running, a message box appears (see Figure 14). Click **Retry** to manually attempt to reconnect, or click **Cancel** to return to the GUI.

Renesas	×	
	Please check USB connection No Renesas (Intersil) HID module found	HID Status CONNECT
	<u>R</u> etry Cancel	

Figure 14. No HID Dongle

3.5.3 I²C ACK/NACK

Successful I²C communication contains acknowledge bits (ACK) at the end of each byte. No special notification or action is provided by the GUI for an ACK. However, the GUI monitors for unsuccessful I²C communication in the form of a not acknowledge (NACK) bit. If a NACK is encountered, the GUI notifies the user with a message box (see Figure 15) and generally terminates the current operation.

Renesas		×
	Please check connections. I2C NACK occured.	
	ОК]

Figure 15. I2C NACK

3.5.4 Product (IC) Selection

This is a drop-down list that allows you to choose a product from the ISL9122 family. After choosing a product, the evaluation software updates accordingly. Alternatively, the Find Slave Address feature searches for the connected device and updates the evaluation software to match that connected device to the PC.

3.5.5 Find Device/Slave Address

This feature searches for an ISL9122 device on the I^2C bus, and sets the correct slave addresses to be used by the GUI. Click **Find Slave Address** to initiate the search. It searches for an I^2C ACK by attempting I^2C commands with incrementing slave address from 0x00 to 0x7F. If the ISL9122 family device is found, a message box with the I^2C slave address appears (see Figure 9). If the evaluation software cannot find the ISL9122 family device on the bus, a message box with an error message appears (see Figure 16).

ISL9122	×
ISL9122 Family I2C slave address Not found. Verify IC And interface connections.	
ОК	

Figure 16. Slave Address Not Found



3.5.5.1 Auto-Read IC Information

If the Auto-read IC Information check-box is set, the evaluation software updates itself to the ISL9122 family device found on the bus. If the check-box is not set, the evaluation software does not update itself to match the device found on the bus.

3.5.6 Write All

The **Write All** button writes data to all the valid registers of the IC based on the settings shown on the evaluation software GUI. The currently selected slave address(es) in the GUI are used for the operation. If an incorrect slave address is chosen in the drop-down menu or an I^2C NACK is encountered the routine terminates immediately.

3.5.7 Read All

The **Read All** button reads all the valid registers and updates the evaluation software GUI settings based on the read-back data from the IC registers - this synchronizes the IC/device under test with the GUI display. It is always recommended to read-all whenever the evaluation software is first run and/or a device under test is (re)started (e.g. power cycled, reset or memory recalled). The currently selected slave address from the Device/Slave address drop-down is used for the operation. If an incorrect slave address is chosen in the drop-down menu or an I²C NACK is encountered and the routine terminates immediately.

3.5.8 I²C Log

The evaluation software has a live I^2C log that updates when an I^2C read or write transaction is performed (see Figure 17).

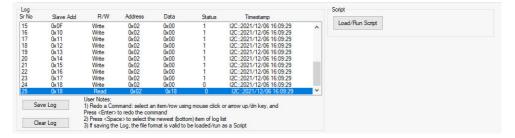


Figure 17. I²C Log

To redo a given command select the row using a mouse click or using the arrow up/down keys and press <Enter>. press <Space> to jump to the most recently logged item (bottom of the list).

3.5.8.1 Save Log

All the transactions in the log are saved into a specifically formatted text file using the Save Log option.

3.5.8.2 Clear Log

The transactions in the log can be cleared by using the **Clear Log** option.

3.5.8.3 Load/Run Script

A saved log file (see Save Log) can be loaded and executed as a script. The I²C transactions execute in sequential order as provided by the log file. Both I²C read and write is supported. *Note:* If the log file has an incorrect slave address or line format, the script routine is terminated and a message box notifies the user of the error encountered (see Figure 18).



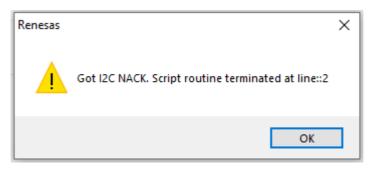


Figure 18. Script Termination

3.6 General Tab

The General tab contain ancillary controls to help user interaction with the IC, dongle and GUI (see Figure 19). The following sections describe the available controls.

ISB Don	gle Setup								Manual Transaction
uC: 120			00kHz	E	⊡ Set to for rea	Enable I ad-back s	2C repeat s equence	tart	b7 b6 b5 b4 b3 b2 b1 b0 Reg Val (HEX) 0 0 0 0 0 0 0 0 0 0 0 € Register Address (HEX) Register Address (HEX) Address (HEX) Addres (HEX) Addres (HEX) Addres (HEX) Addres (HEX) Addres (HEX) Addres
HID In	fo								WRITE REG READ Copy Addr
Get	DLL Info						Cle	ar	Dump
Get	HID Info								Read all registers, and
uC: GF	PIO Statio	Set							save values to dumpfile Reg Dump
							cked=HIG	H'1'	- 16-bit Converter
P1.7	P1.6	P1.5	P1.4	P1.3	P1.2	P1.1	P1.0	Read	HEXto-D
	P2.6	P2.5	P2.4	P2.3	P2.2	P2.1	P2.0	Write	0 🛓
P2.7		_							DEC-to-F
P2.7									

Figure 19. General Tab

3.6.1 Manual Transaction

The General tab includes an independent Manual Transaction section that is used to perform a user configured, standard I²C transaction, see Figure 20. This feature operates independently from the main (IC specific) GUI tabs and controls. Register data read and written though this mechanism is not automatically reflected in the main GUI controls. Specific GUI register control(s) can subsequently be updated (synchronized) by a register specific read or read-all operation on the main GUI, see Read All.

The Manual transaction is a useful debug tool and can be used to communicate with the Renesas device under test, and/or other I²C compliant devices on the bus.

Manual Transaction	b7	b6	b5	b4	b3	b2	b1	ь0	Reg Val (HEX)	7-bit I2C Address(Hex) 40
	0 WPI	0 TE REG	0	0	0 READ	0	0	0	0 🔹	Register Address (HEX) 00
	WRI	TE REG		ſ	READ		Cop	y Addr		

Figure 20. Manual Transaction

3.6.1.1 7-Bit I²C Address Text Box

The slave address of the target IC is entered into the 7-bit I²C Address box. Valid entries are hexadecimal 0 to 7F, other values or characters are not accepted.

3.6.1.2 Register Address Text Box

The 8-bit register address is entered into the Register Address box. Valid entries are hexadecimal 0 to FF, other values or characters are not accepted.

3.6.1.3 Reg Val (HEX) Numeric Text Box

The 8-bit register value is entered into Register Val box. Valid entries are hexadecimal 0 to FF, other values or characters are not accepted. The Reg Val (HEX) box work bi-directionally with the bit buttons.

3.6.1.3.1 Bit Buttons

The **Bit Buttons** beneath each b7~b0 labels show '0' or '1' and gray or green, respectively, to visually represent the value shown in the Reg Val (HEX) box. The user may also click the bit buttons to toggle the bit values, and the updated total register value will be updated accordingly in the Reg Val (HEX) box.

3.6.1.4 Copy Addr Button

The **Copy Addr** button copies the currently selected slave address from the Device Address (Main) and sets it to the 7-bit I²C address box.

3.6.1.5 Read Button

The **Read** button performs an I²C read transaction from the register address entered into the Register Address box and populates the value in the Reg Val box. The slave address for the transaction is used from the 7-bit I²C Address box. A message box appears if the I²C transaction encounters NACK (see Figure 15).

3.6.1.6 Write Reg Button

The **Write Reg** button performs an I²C write transaction to the register address entered into the Register Address box with the value in the Reg Val box. The slave address for the transaction is used from the 7-bit I²C Address box. A message box appears if the I²C transaction encounters NACK (see Figure 15).



3.6.2 USB Dongle Setup

The USB Dongle Setup section, located on the General tab, provides setup and control of the micro-controller (MCU) on the HID dongle board (see Figure 21).

	C Freque 0kHz	ncy	00kHz	E		Enable I2 d-back se	2C repeat equence	start
HID Ir Get	nfo : DLL Info						Cle	ar
Get	t HID Info							
uC: G	PIO Statio	c Set				Che	cked=HIG	iH '1'
P1.7	P1.6	P1.5	P1.4	P1.3	P1.2	P1.1	P1.0	Read
P2.7	P2.6	P2.5	P2.4	P2.3	P2.2	P2.1	P2.0	Write



3.6.2.1 I²C Frequency Radio Button

The I²C frequency radio buttons allow changing the SCL frequency.

3.6.2.2 Enable I²C Repeat Start Check box

The dongle supports both I²C Stop-Start and Repeated Start read transactions. Checking the box (default) enables repeated start option.

3.6.2.3 HID Info

This is an informational section that reports the Renesas DLL and HID dongle firmware details.

3.6.2.4 GPIO Static Set

The MCU has general purpose open-collector output port pins with weak pull-up to 3.3V. These output pins are controlled manually from the GPIO Static Set GUI controls. A checked box for a pin (or pins) chooses logic HIGH. An unchecked box for a pin (or pins) chooses logic LOW. To apply the check box selections to the HID dongle click the **Write** button. The **Read** button reads back the current state of all the dongle pins and updates the GUI. *Note:* More than one pin can be checked or unchecked at the same time.



Only ports P1.0 to P1.7, P2.0 and P2.1 are branched out from the dongle connector (see Figure 22).

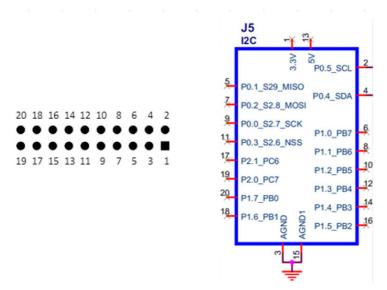


Figure 22. General Purpose Outputs

3.6.3 16-Bit Converter Tool

This tool converts 16-bit hexadecimal and decimal numbers as shown in Figure 23. The maximum hexadecimal allowed is 0xFFFF, and the maximum decimal allowed is 65536.



Figure 23. 16-bit Converter Tool

3.6.4 Register Dump

The register dump button reads back all necessary registers from the IC and creates a text file with a dump of register data. The text file generated has 8-bit data values in hexadecimal format for the 0x00 to 0xFF register space. Each line of the text file corresponds to register address (n - 1). For example, line one of the text file contains data from register address 0, and line 256 contains data from register address 255 (0xFF). The dump file format is compatible with the File > Open operation (see File Menu), thus it can be (re)loaded to the GUI.

Dump						
Read all registers, and						
save values to dumpfile						
Reg Dump						

Figure 24. Register Dump



4. Software Uninstall Process

The evaluation software can be uninstalled in a few simple steps. Navigate to the program in the Windows Start menu under the "Renesas" folder. Right-click on the program name and choose **Uninstall**. This opens the Windows Control Panel "Programs and Features", where the program can be selected an uninstalled.

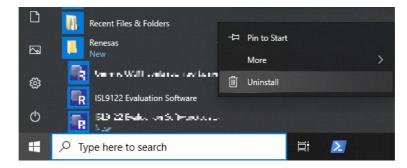


Figure 25. Evaluation Software - Uninstall

5. Troubleshooting Tips

Listed below are common troubleshooting tips for users experiencing issues while installing or using the evaluation software.

- Issues with installing or running program
 - Try running installer and/or opening the program with administrative rights (right-click and Run As Administrator)
 - If Run As Administrator does not resolve the issue, the issue could be related to a third-party restriction such as corporate security policy or anti-virus. Contact your local IT administrator for further help.
 - Ensure the proper .NET frameworks are installed on the machine. Both .NET common runtime language (CLR) 4 and CLR 2 are required. This assembly targets .NET 4.5. .NET framework 4.5 or greater is required, which inherently runs CLR 4. Additionally, newer OS (such as Windows 10) also need to have .NET framework 3.5 enabled.
- · Evaluation software text is enlarged, cut-off or missing
 - The evaluation software does not support non-native scaling or dynamic resizing, therefore, verify that the PC display settings are set to a 100% scale factor. This setting is in the Display Settings of a Windows PC.
 - The evaluation software is sized optimally for display resolution of 1366x768 or higher.
- Evaluation software cannot connect to the Renesas HID dongle
 - The USB cable is not connected to the PC USB port and/or Renesas HID dongle. Connect the cable to both the PC USB port and Renesas HID dongle.
 - The USB cable is damaged. Replace the USB cable.
 - Bad USB port. Ensure the USB device is recognized by the machine. This can be checked in the Windows Device Manager. The connection/cable and port are good if different audible tones can be heard from PC when plugging the cable in and out.
 - Another Renesas HID dongle is already connected to the machine.
- Issues with updating to newer version of the evaluation software
 - Uninstall the existing software as described in the Software Uninstall Process; next, reinstall the newer version.
- · Evaluation software text looks different than the images in the manual



• The evaluation software was developed in Windows 10 with a default system font for the English (United States) language setting. Other regions might use a different default system font for English, which affects the appearance of the evaluation software but not its operation.

• HID communication error message:

- The GUI can report various HID USB communication errors using a message box (see Figure 26). The message box will report an error code number. Check all physical connections, restart the IC, and exit/reopen GUI.
- If the issue persists, contact Renesas support to help diagnose the problem.



Figure 26. HID Communication Error Example

6. Revision History

Revision	Date	Description						
1.01	Mar 23, 2023	Updated System Requirements section. Updated Troubleshooting Tips section.						
1.00	Apr 1, 2022	Initial release						



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