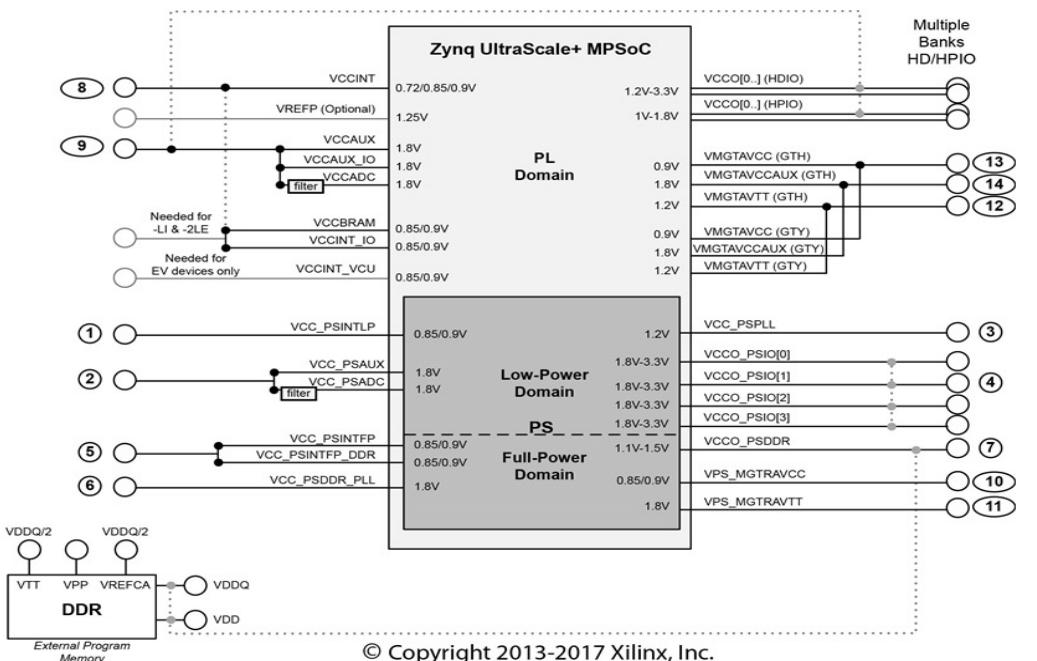
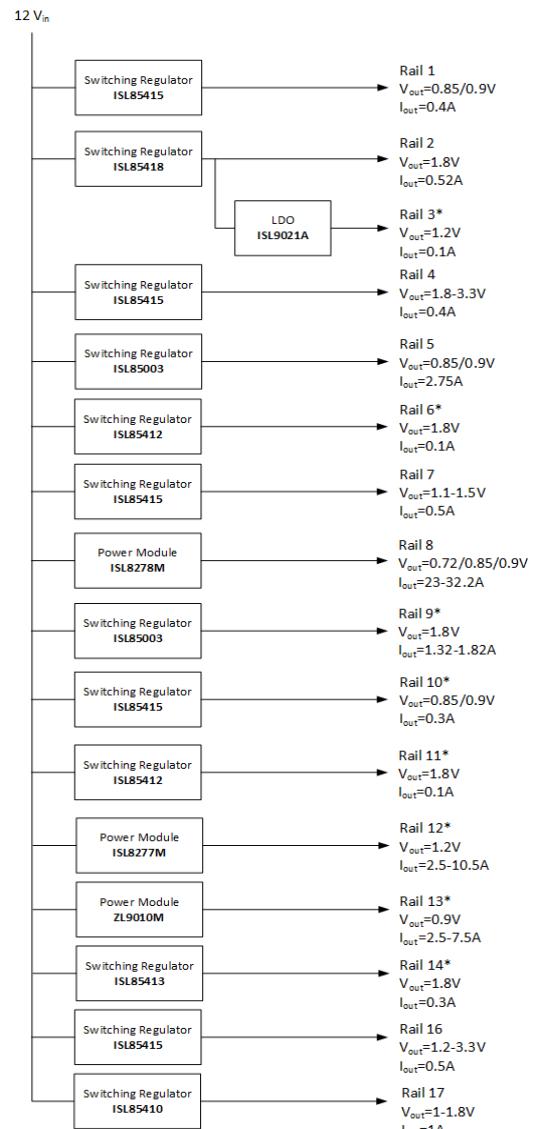


USECASE 4 #4



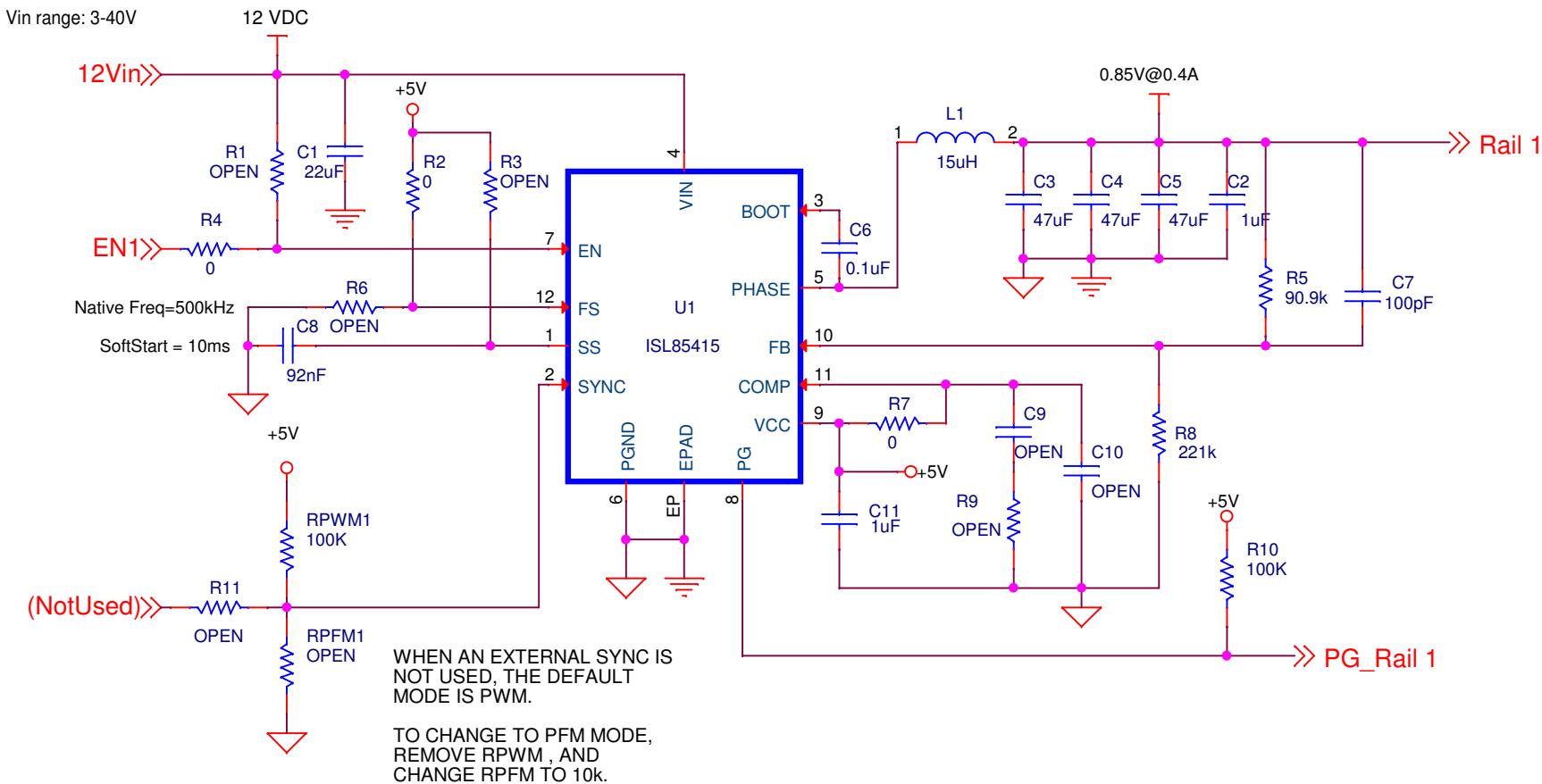
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Rail	Voltage	Power Supply Rails
Rail 1	0.85V	VCC_PSINTLP
Rail 2	1.8V	VCC_PSAUX, VCC_PSADC
Rail 3 *	1.2V *	VCCO_PSPLL
Rail 4	1.8-3.3V	VCCO_PSI0
Rail 5	0.85/0.9V	VCC_PSINTFP, VCC_PSINTFP_DDR
Rail 6 *	1.8V *	VCC_PSDDR_PLL
Rail 7	1.1-1.5V	VCCO_PSDDR
Rail 8 *	0.85V	VCCINT_VCU, VCCINT, VCCINT_I, VCBRAM
Rail 9 *	1.8V	VCCAUX, VCCAUX_IO, VCCADC, DDR_VPP1
Rail 10 *	0.85V *	VPS_MGTRAVCC
Rail 11 *	1.8V *	VPS_MGTRAVTT
Rail 12 *	1.2V	VMGTAVTT(GTH), VMGTYAVTT(GTY)
Rail 13 *	0.9V	VMGTAVCC(GTH), VMGTYAVCC(GTY)
Rail 14 *	1.8V	VMGTVCCAUX(GTH), VMGTYVCCAUX(GTY)
Rail 16	1.2-3.3V	HDO_VCCO
Rail 17	1-1.8V	HPIO_VCCO



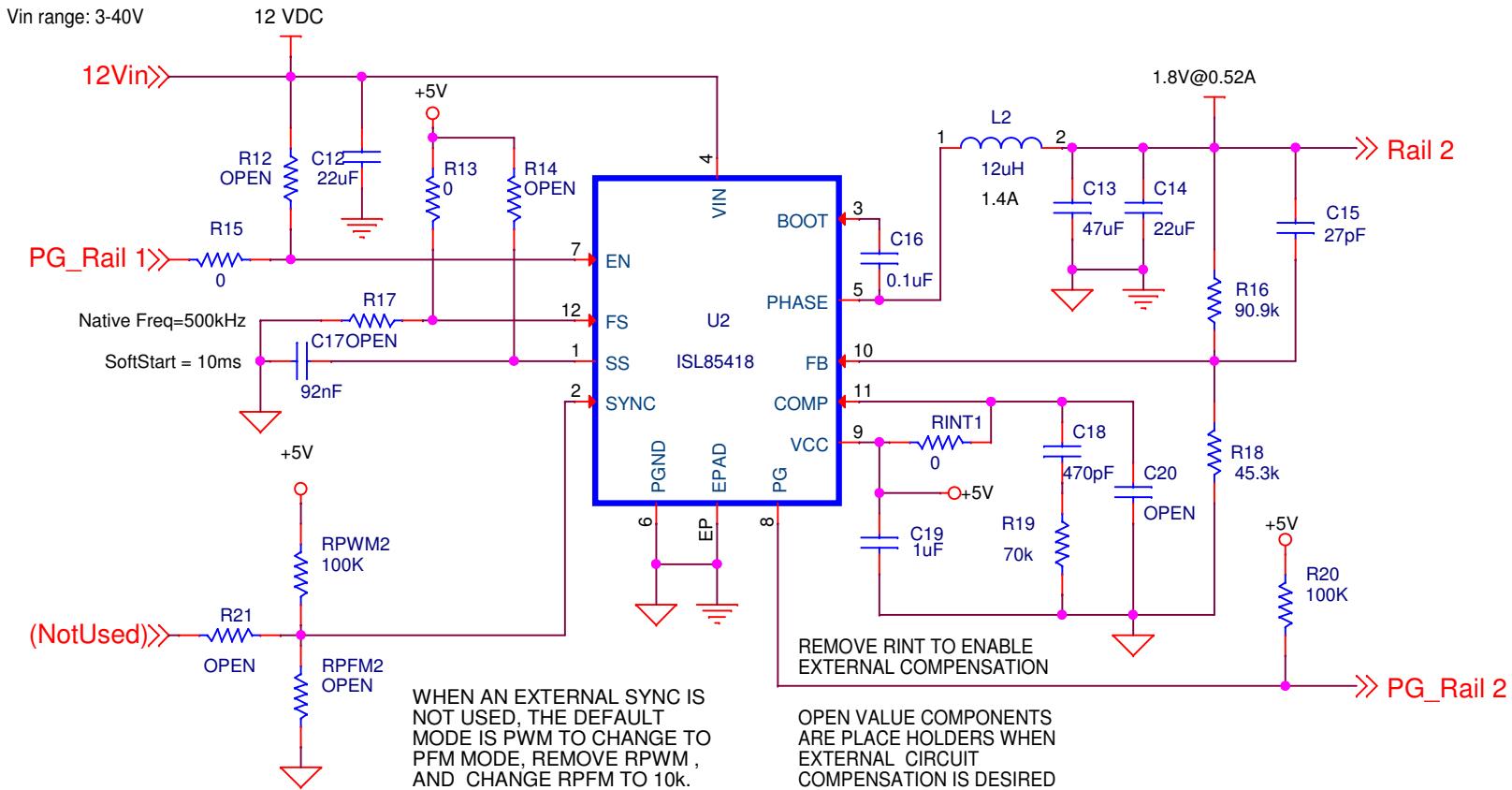
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Title: ZYNQ US+ MPSoC Power Rails		
Size	Document Number	Rev
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Date: Friday, December 01, 2017	Sheet 1	of 17

ISL85415 SINGLE OUTPUT (500mA)



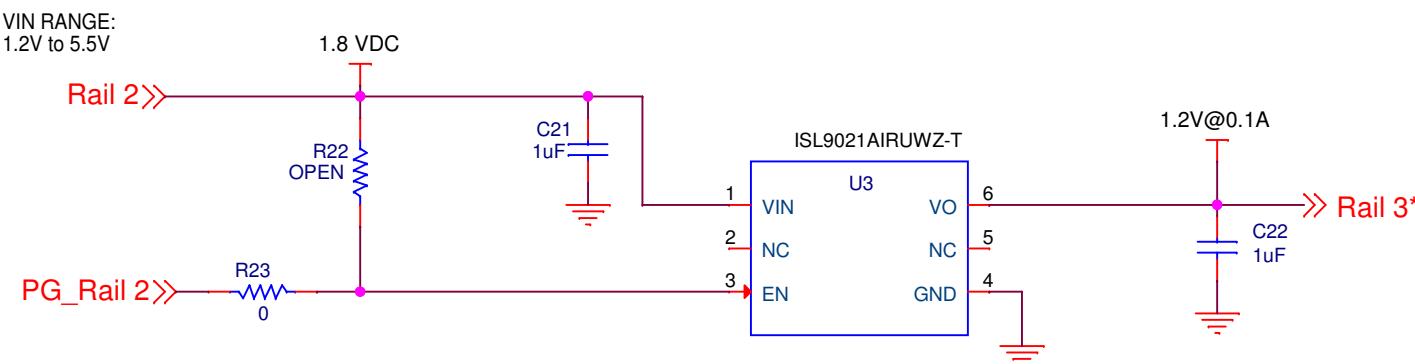
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Size A	Document Number
Date: Thursday, March 09, 2017	Sheet 2 of 17

ISL85418 SINGLE OUTPUT (800mA)



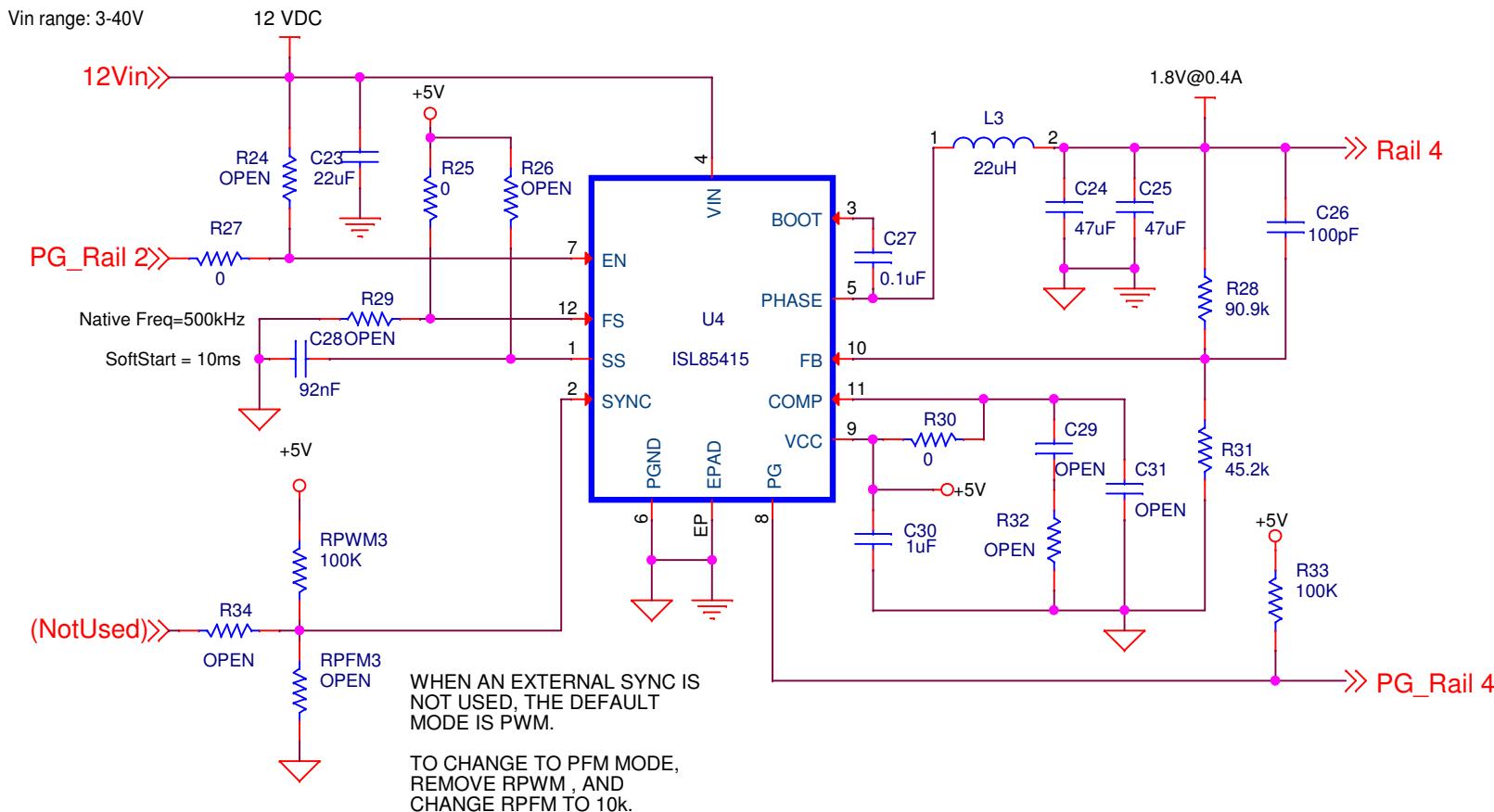
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Size A	Document Number
Date: Thursday, March 09, 2017	Sheet 3 of 17

ISL9021A Single Output (250mA)



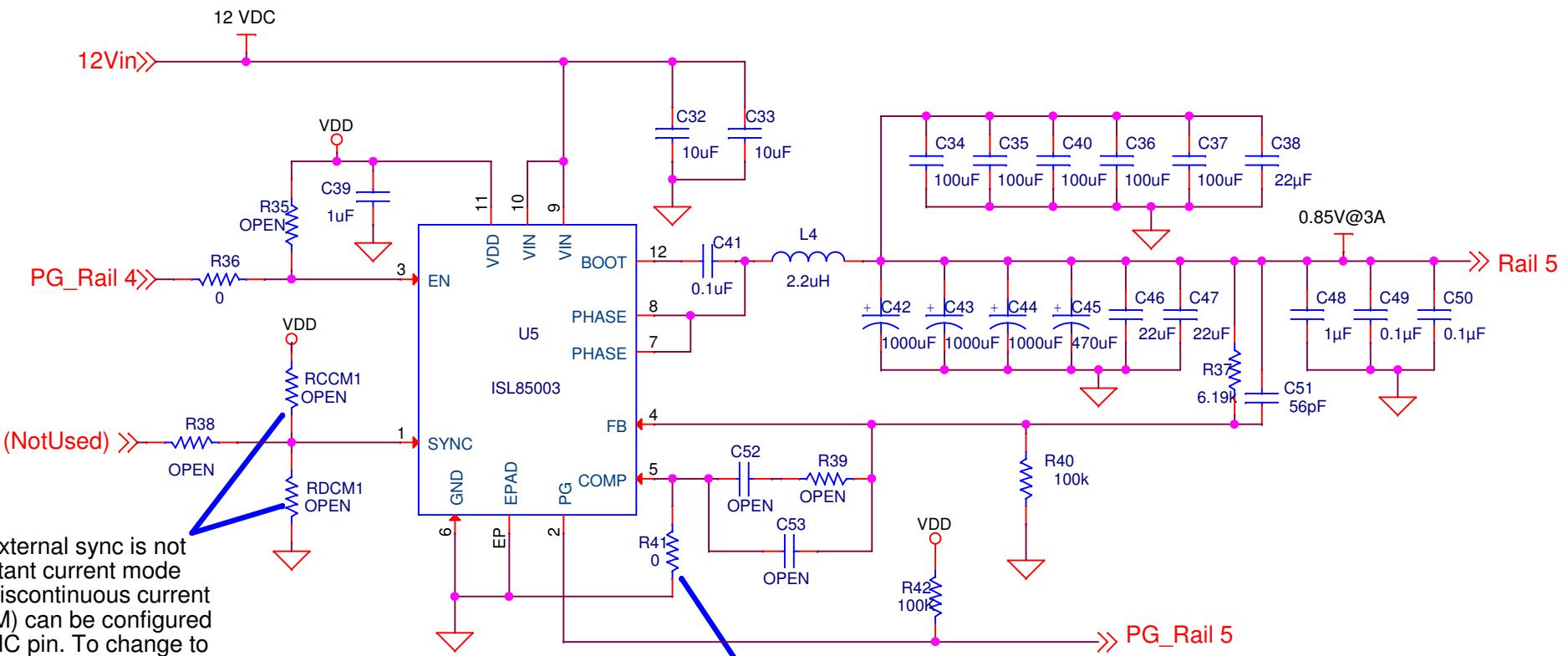
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Size	Document Number
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Date:	Rev
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ISL85415 SINGLE OUTPUT (500mA)



Title	
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Size A	Document Number
Date: Thursday, March 09, 2017	Sheet 5 of 17

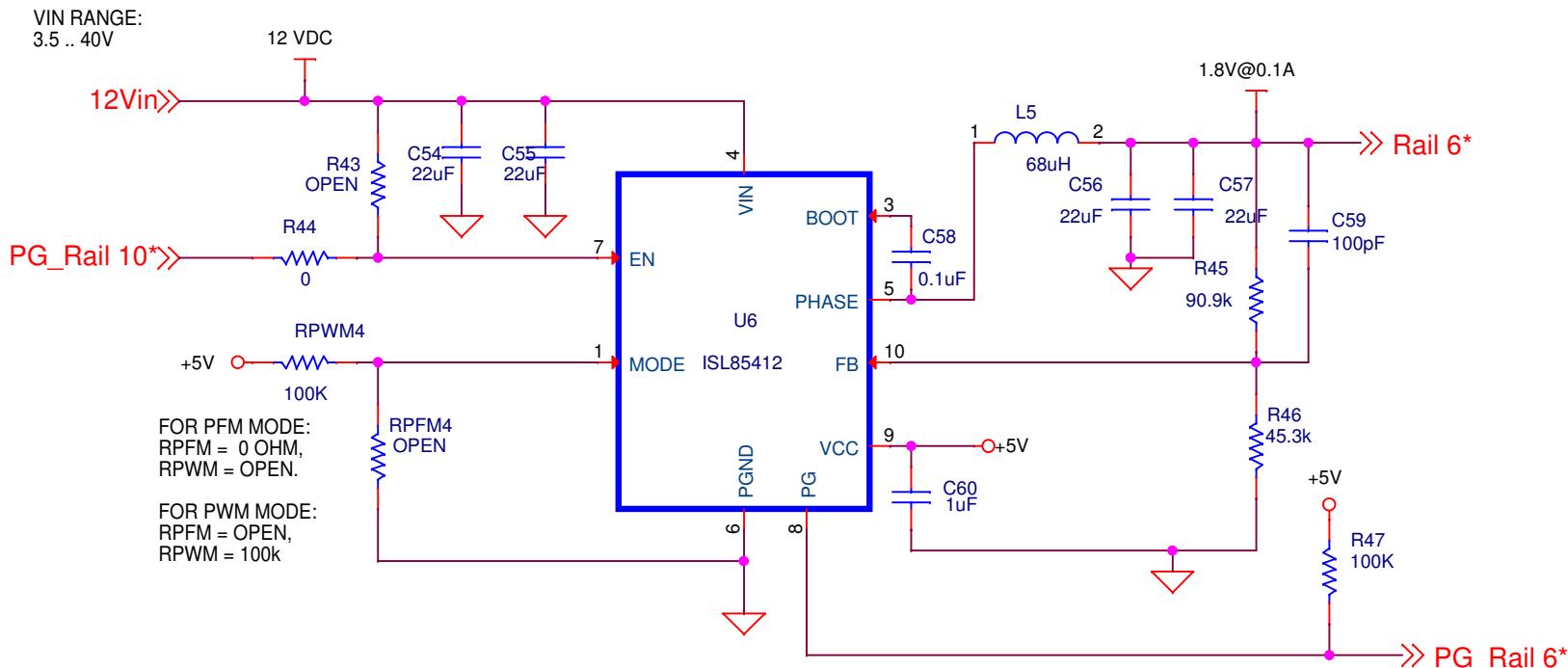
ISL85003 SINGLE OUTPUT (3A)



Programmable Soft Start is available in the ISL85003A (without synchronization)

Title	
ISL85003-SINGLE-1	
Size A	Document Number <Doc>
Date: Friday, February 05, 2021	Sheet 6 of 17

ISL85412 SINGLE OUTPUT (150mA)



Title ISL85412-SINGLE-1

Size A Document Number
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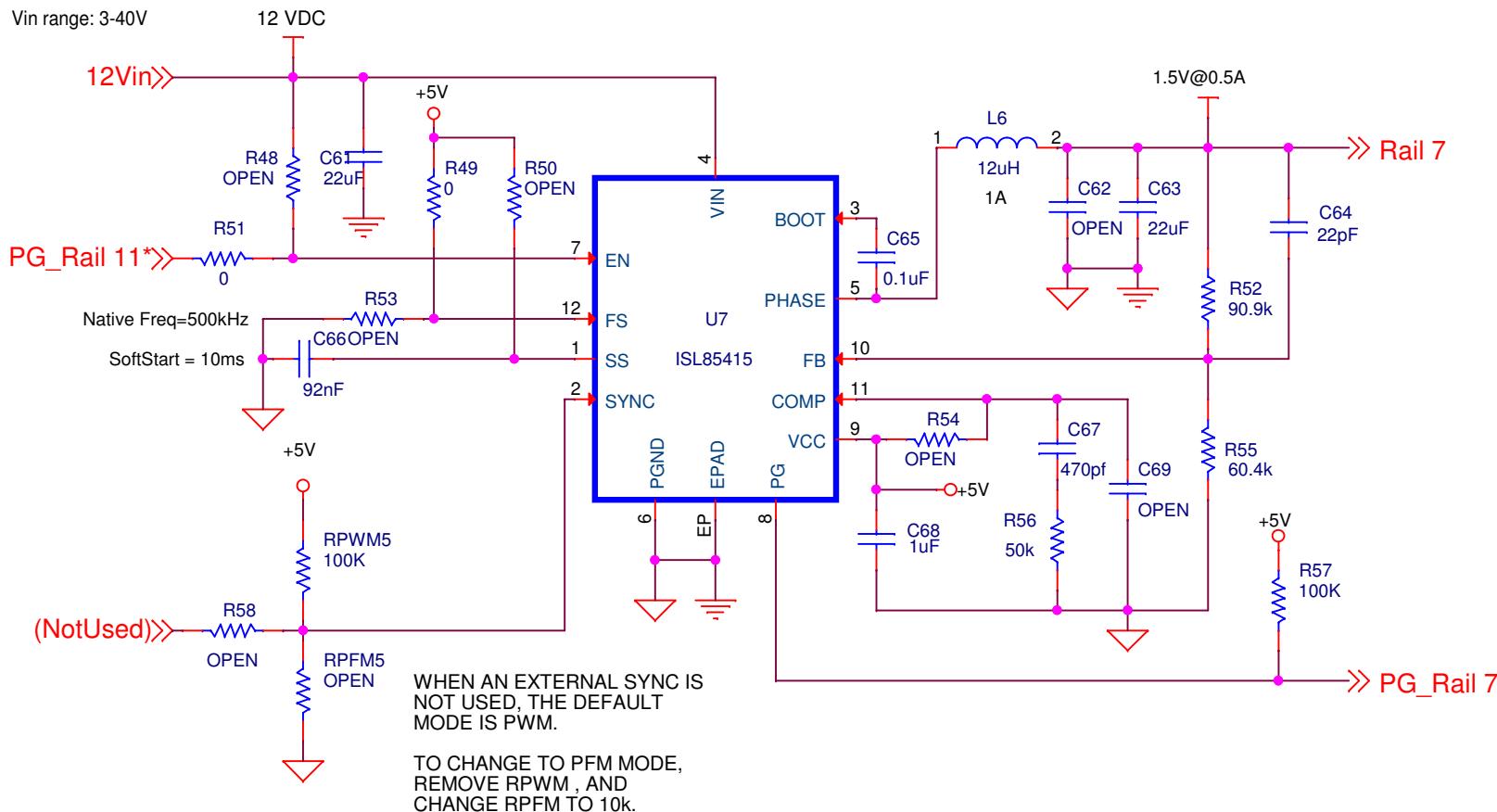
Rev

Date: Thursday, March 09, 2017

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ISL85415 SINGLE OUTPUT (500mA)

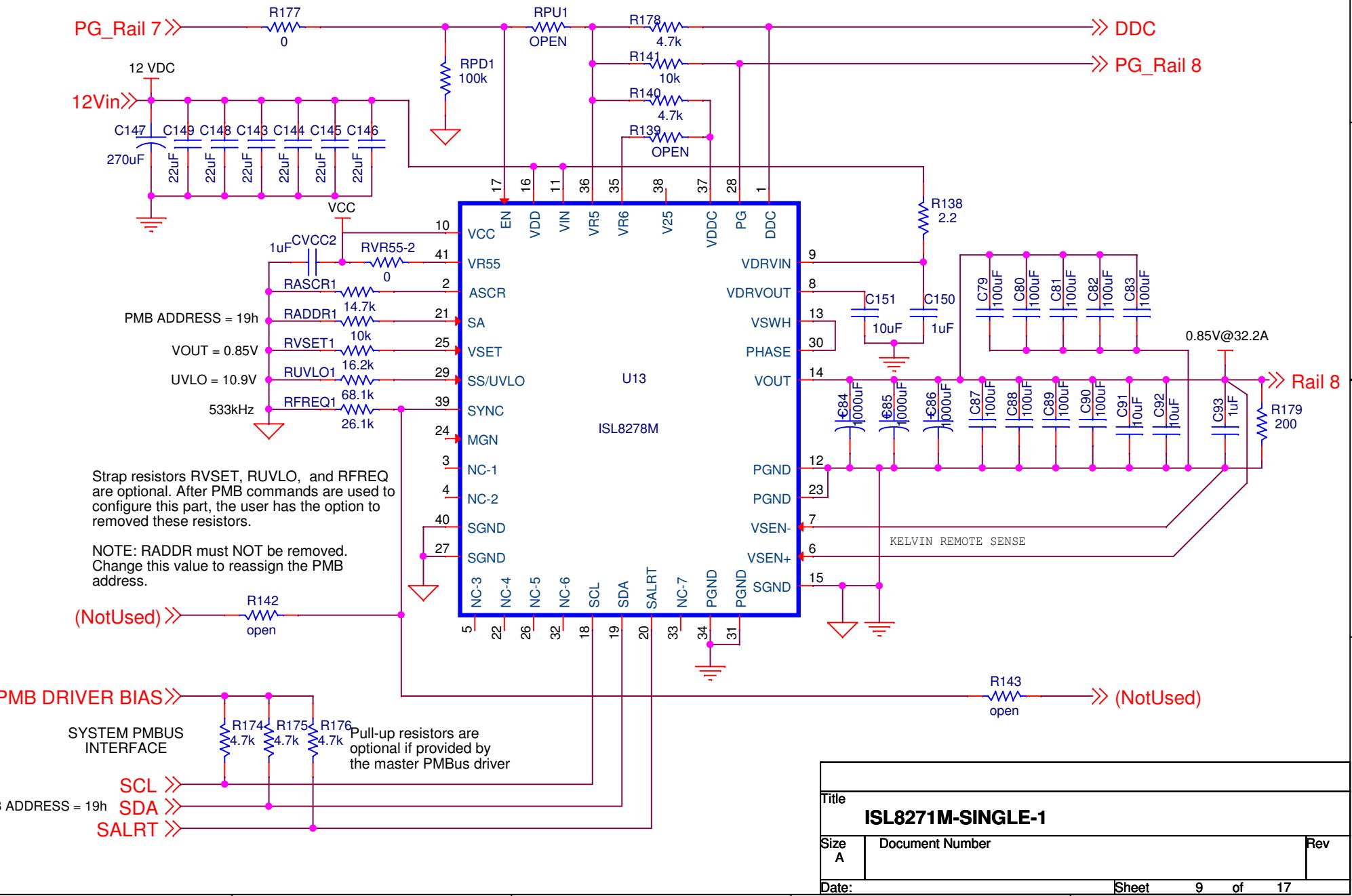


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Size A	Document Number
Date: Thursday, March 09, 2017	Sheet 8 of 17

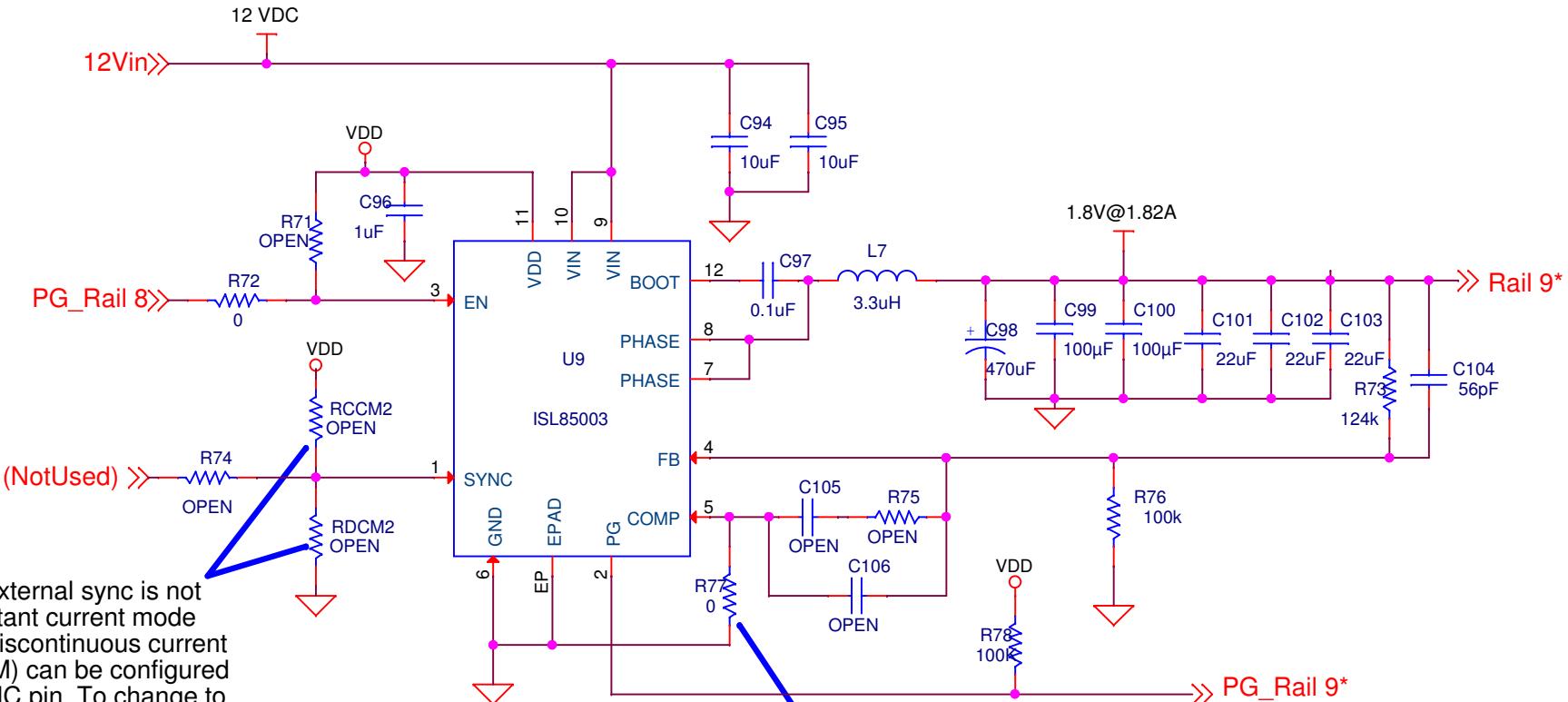
ISL8278M SINGLE OUTPUT (33A)

If an external ENABLE is not used, resistor RPU is a
Enable pullup for initial evaluation without PMB commands.

Remove RPU and install RPD = 100k to make EN low to
allow the use of PMB commands.



ISL85003 SINGLE OUTPUT (3A)



Programmable Soft Start is available in the ISL85003A (without synchronization)

Title **ISL85003-SINGLE-1**

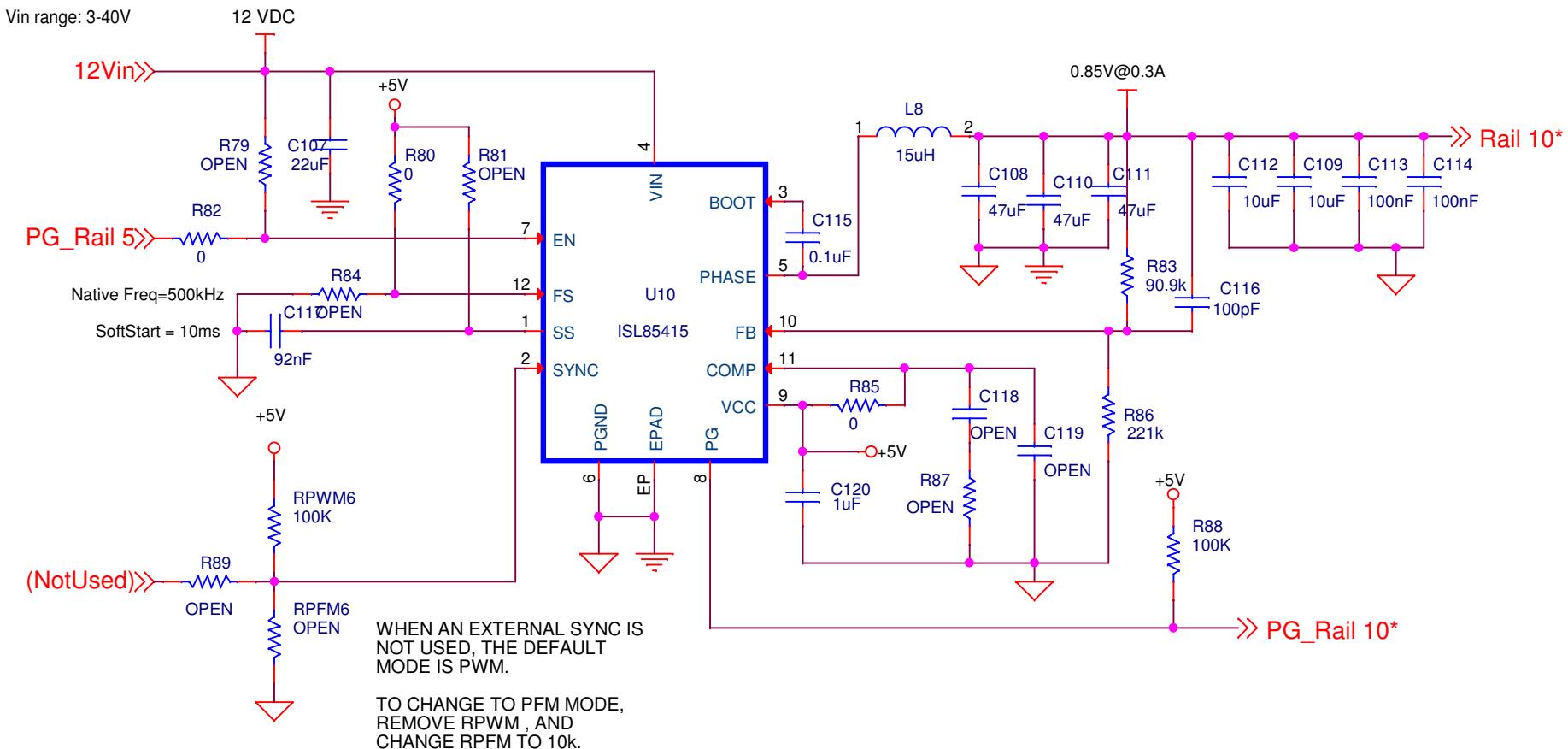
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Date: Thursday, March 09, 2017

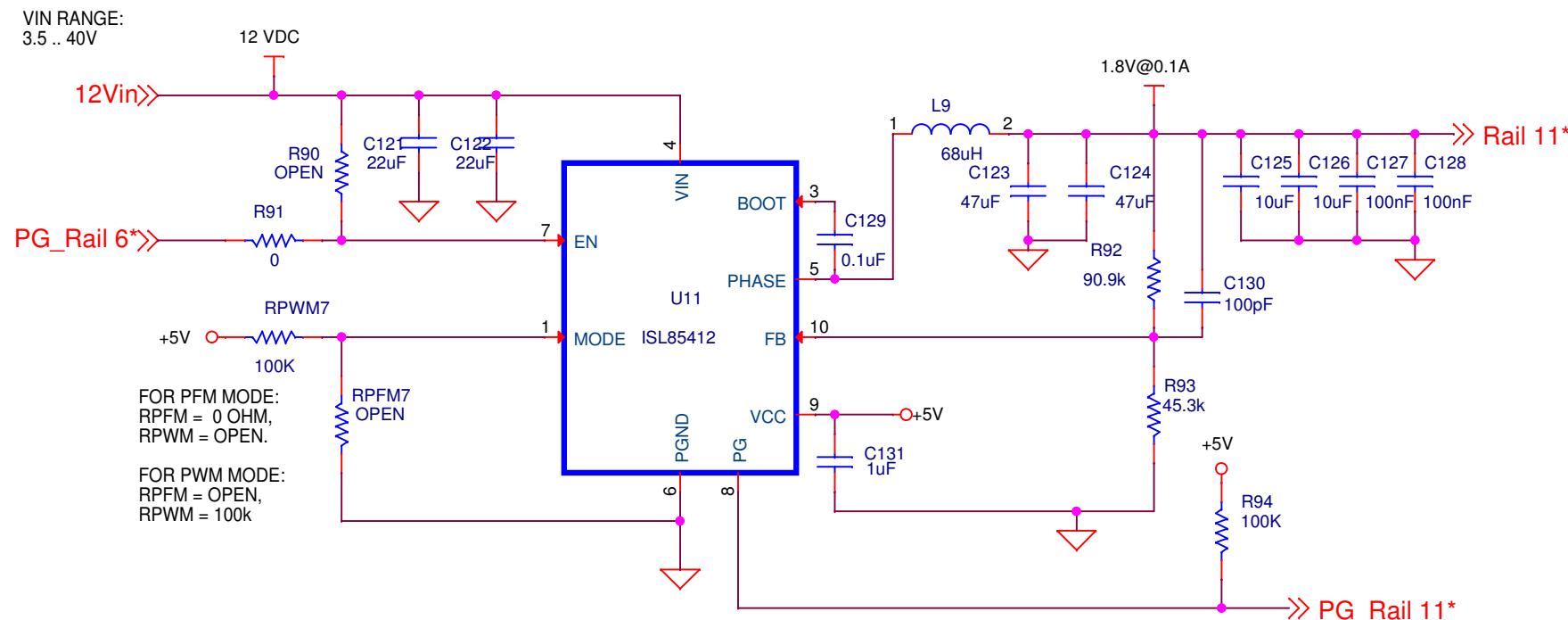
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ISL85415 SINGLE OUTPUT (500mA)



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ISL85412 SINGLE OUTPUT (150mA)



Title ISL85412-SINGLE-1

Size A Document Number
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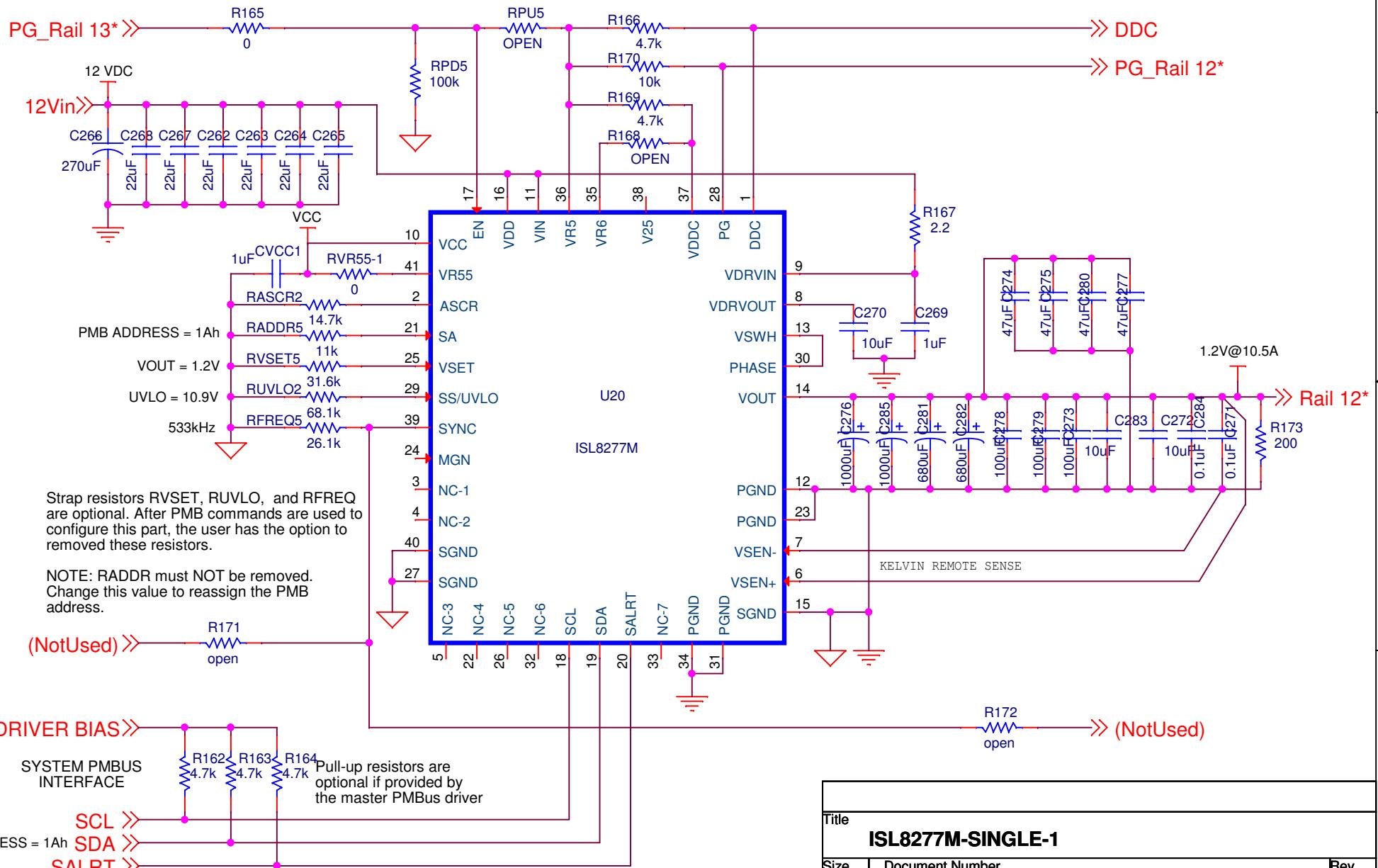
Date: Thursday, March 09, 2017

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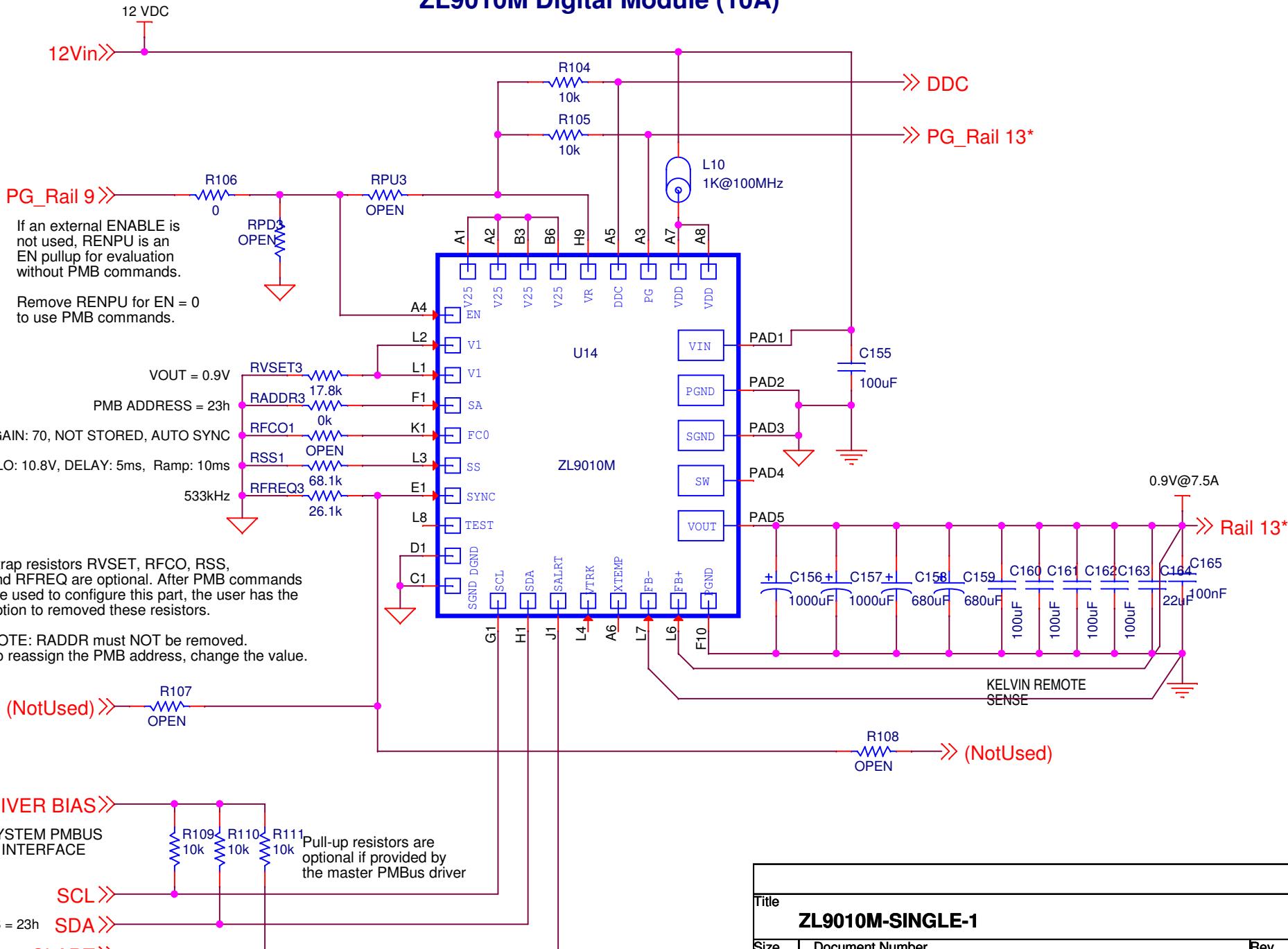
ISL8277M SINGLE OUTPUT (25A)

If an external ENABLE is not used, resistor RPU is a Enable pullup for initial evaluation without PMB commands.

Remove RPU and install RPD = 100k to make EN low to allow the use of PMB commands.



ZL9010M Digital Module (10A)



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ZL9010M-SINGLE-1

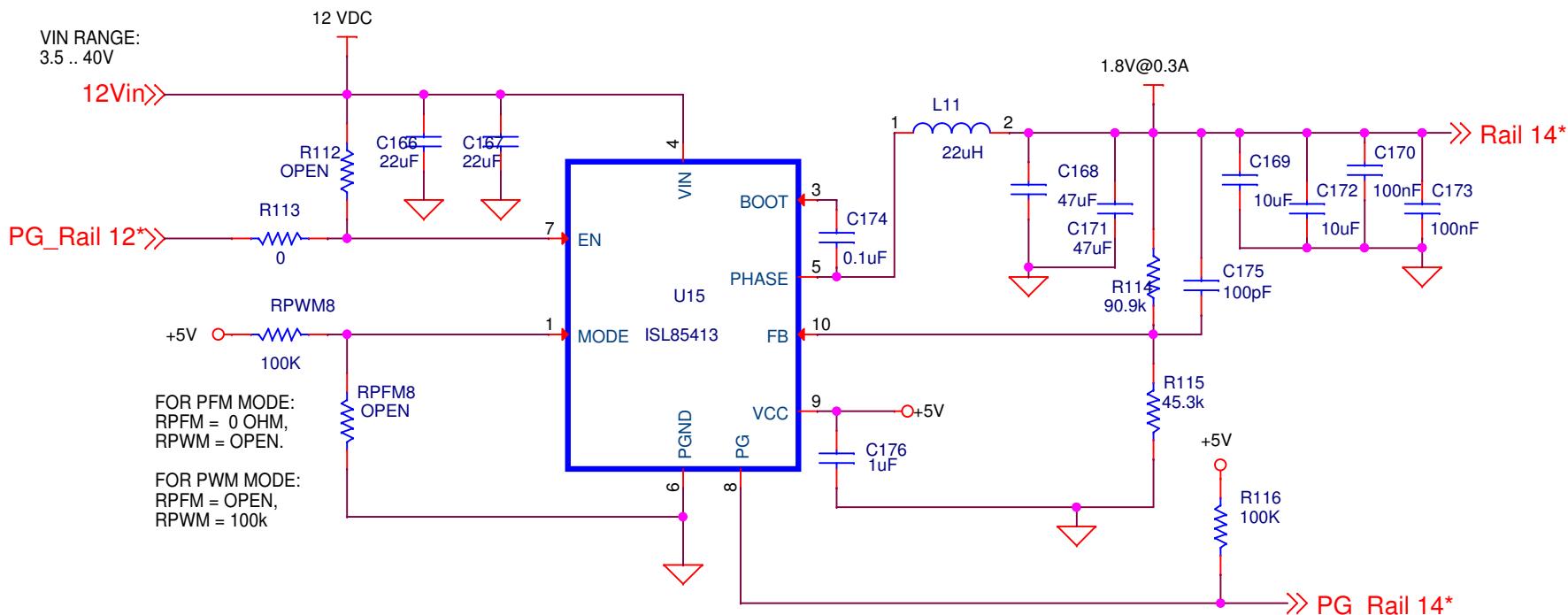
Size A Document Number

Date:

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ISL85413 SINGLE OUTPUT (300mA)



Title ISL85413-SINGLE-1

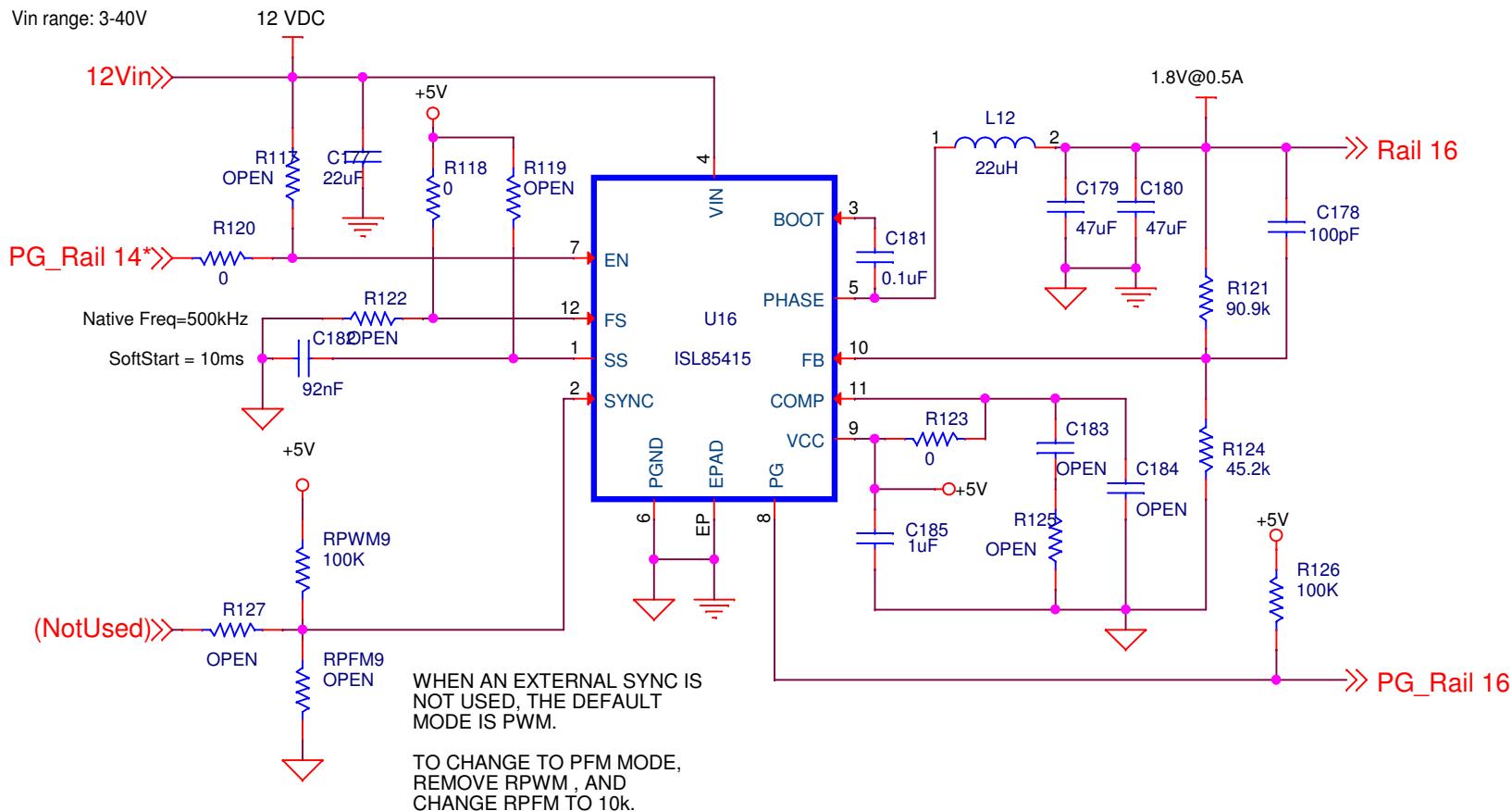
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Rev

Date: Thursday, March 09, 2017

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ISL85415 SINGLE OUTPUT (500mA)



Title	ISL85415-SINGLE-1
Size	Document Number
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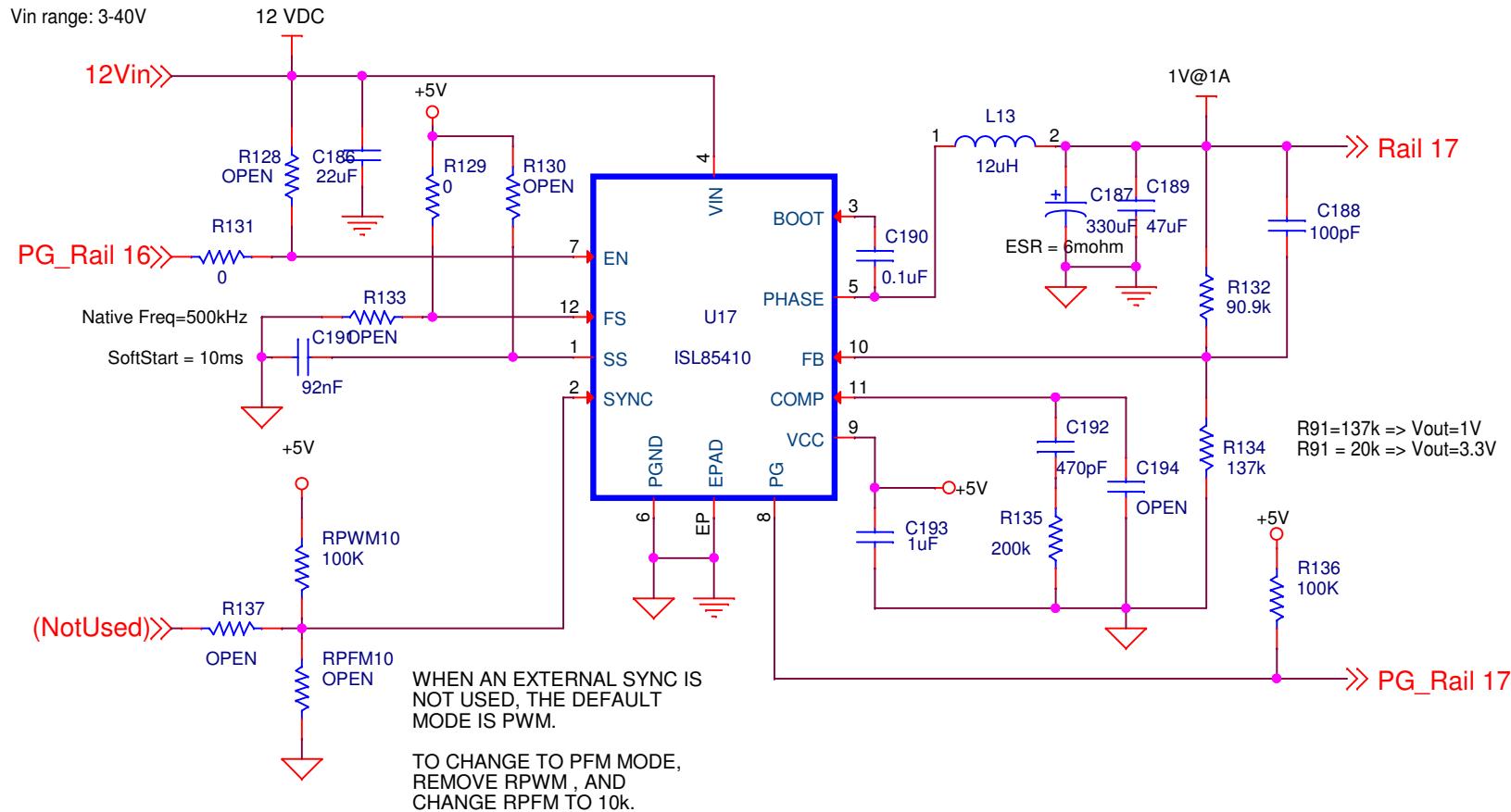
Date: Thursday, March 09, 2017

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ISL85410 SINGLE OUTPUT (1A)



Title	ISL85410-SINGLE-1
Size	Document Number
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Date: Tuesday, March 14, 2017

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