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M16C/62P Group

Watchdog Timer Operation

1. Abstract

This documents describes the watchdog timer operation.

2. Introduction

The application example described in this document is applied to the following MCU and parameter(s):
MCU: M16C/62P Group

This program can be used with other M16C Family MCUs which have the same special function registers (SFRs) as the M16C/62P Group MCU. Check the manual for any additions and modifications to functions. Careful evaluation is recommended before using this application note.

3. Example Description

3.1 Operation

1. When the WDTS register is written, the watchdog timer is initialized to 7FFFh and starts counting.
2. When the WDTS register is rewritten during a count operation, the watchdog timer is initialized to 7FFFh and continues counting.
3. The watchdog timer holds its value and stops when the WAIT instruction is executed or when in stop mode. After the WAIT instruction is executed or is in stop mode, the watchdog timer restarts counting from its hold value.
4. When the watchdog timer underflows, it is initialized to 7FFFh and continues counting. At the same time, a watchdog timer interrupt is generated.

If 1 (watchdog timer reset) is selected by the watchdog timer function select bit in processor mode register 0 (PM0 register), a watchdog timer underflow will initialize the pins, CPU, and SFRs, and the MCU executes a program at the address pointed to by the reset vector.

Figure 1 shows the Watchdog Timer Operation.

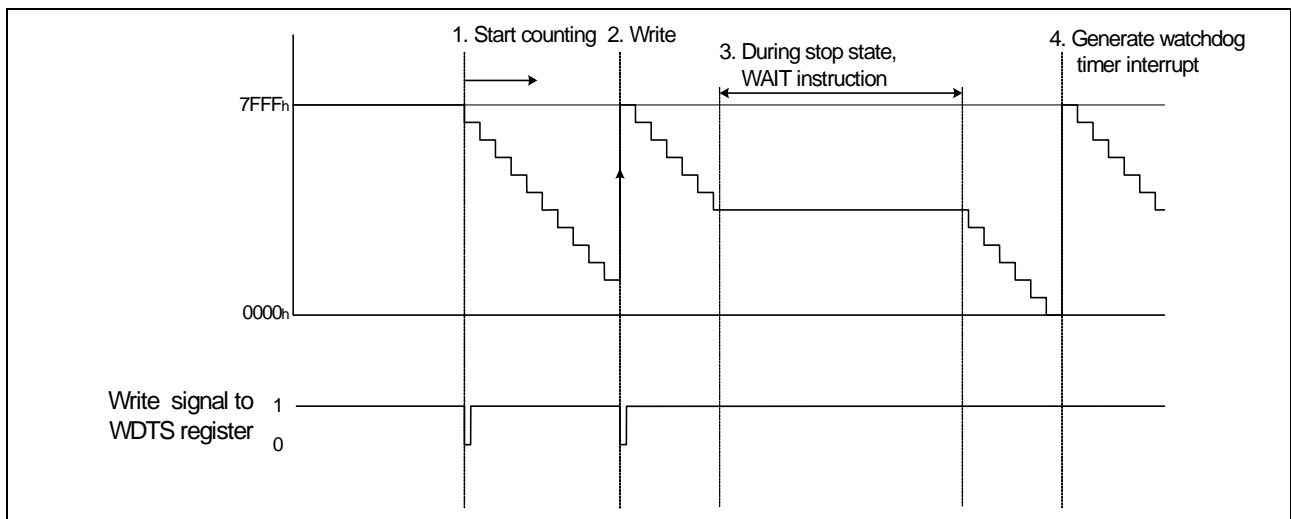


Figure 1 Watchdog Timer Operation

3.2 Setup

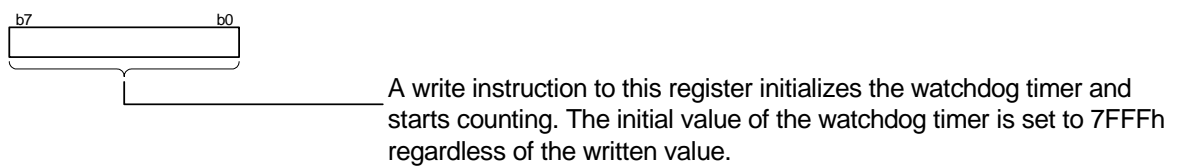
This section shows the setup sequence and values to perform the application example described in **3. Example Description**.

Refer to the M16C/62P Group (M16C/62P, M16C/62PT) Hardware Manual for details of individual registers.

1. Set the watchdog timer control register



2. Set the watchdog timer start register



4. Sample Program

Increment the display on the P10 port by writing to the WDTS register. When the P10 port output reaches 40h, finish writing to the WDTS register to stop updating the display on the P10 port.

If a watchdog timer underflow occurs, decrement the display on the P10 port in the watchdog timer interrupt handler by writing to the WDTS register. When the P10 port output reaches 00h, stop updating the display on the P10 port.

4.1 To Use Watchdog Timer Interrupt

```

/*****"FILE COMMENT"*****/
* System Name : M16C/62P Program Collection
* File Name   : interrupt_src.c
* Version     : 1.10
* Contents    : Application of Watchdog Timer
* CPU        : M16C/62P Group
*****/

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*****/

* History   : 2005.06.16 Ver 1.00
*           : 2006.01.25 Ver 1.10
*****/
*****"FILE COMMENT END"*****/

/*****
* include file
*****/
#include "sfr62p.h"

/*****
* Function Definition
*****/
void wdt_int(void);
#pragma INTERRUPT wdt_int

/*****
* main
*****/
unsigned int i;

void main(void) {

    p10 = 0;

```

```

pd10 = 0xff;

ta0mr = 0x40;      /* Selection of timer mode
                   Pulse output function select bit (0: Pulse is not output)
                   Gate function select bit (00: Gate function not available)
                   Count source (01: f8) */

pd10 = 0xff;

ta0 = 2000-1;     /* Set counter value (1 msec @ 16 MHz, f8) */

cpsrf = 0;       /* Set clock prescaler reset flag (0: No effect) */

ta0ic = 0x00;    /* Set interrupt priority levels in timer A0 */

ta0s = 1;       /* Timer A1 count start */

wdc = 0;        /* Set watchdog timer control register
                 Prescaler select bit is set to 0 (0: Divided by 16) */

i = 0;

wdts = 1;       /* Set watchdog timer start register */

while (1) {
    while (!ir_ta0ic) {
    }
    ta0ic = 0x00;
    i++;
    if ( i == 500 ) {
        i = 0;
        p10++;
    }
    if ( p10 >= 64 ) {
        p10 = 64;
    } else {
        wdts = 1; /* Set watchdog timer start register */
    }
}
}

/*****
 * Watchdog timer interrupt routine *
 *****/

void wdt_int() {
    wdts = 1; /* Set the WDTS register in the beginning of the */
             /* watchdog timer interrupt routine */
             /* Newly added in Ver 1.10 */

    while (1) {

```

```

while (lir_ta0ic) {
}
ta0ic = 0x00;
i++;

wdts = 1;      /* Set watchdog timer start register */

if ( i == 500 ) {
    i = 0;
    if ( p10 != 0 ) {
        p10--;
    }
}
}
}

```

4.2 To Reset on Watchdog Timer Underflow

If a watchdog timer underflow occurs, the MCU is reset. Increment the display on the P10 port by writing to the WDTS register. When the P10 port output reaches 40h, finish writing to the WDTS register to stop updating the display on the P10 port.

```

/*****"FILE COMMENT"*****/
* System Name   : M16C/62P Program Collection
* File Name     : reset_src.c
* Version       : 1.10
* Contents      : Application of Watchdog Timer
* CPU           : M16C/62P Group
*****/

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* History      : 2005.06.16 Ver 1.00
*              : 2006.01.25 Ver 1.10
*****/
/*****"FILE COMMENT END"*****/

/*****
*   include file
*****/

#include "sfr62p.h"

/*****
*   Function Definition
*****/

```

```

void    wdt_int(void);
#pragma INTERRUPT wdt_int

/*****
 *    main                                *
 *****/
    unsigned int i;

void main(void) {

    p10 = 0;
    pd10 = 0xff;

    ta0mr = 0x40;    /* Selection of timer mode
                     Pulse output function select bit (0: Pulse is not output)
                     Gate function select bit (00: Gate fuction not available)
                     Count source (01: f8) */

    ta0 = 2000-1;    /* Set counter value (1m sec @16 MHz, f8) */

    cpsrf = 0;      /* Set clock prescaler reset flag (0: No effect) */

    ta0ic = 0x00;   /* Set interrupt priority levels in timer A0 */

    ta0s = 1;       /* Timer A1 count start */

    wdc = 0;        /* Setting watchdog timer control register
                     Prescaler select bit is set to 0 (0: Divided by 16) */

    i = 0;

    prc1 = 1;       /* Write enabled */
    pm12 = 1;       /* Watchdog timer function is selected to "Watchdog timer reset" */
    prc1 = 0;       /* Write protected */

    wdts = 1;       /* Set watchdog timer start register */

    while (1) {
        while (!ir_ta0ic) {
        }
        ta0ic = 0x00;
        i++;
        if ( i == 500 ) {
            i = 0;
            p10++;
        }
        if ( p10 >= 64 ) {

```



```

        p10 = 64;
    } else {
        wdts = 1;    /* Set watchdog timer start register */
    }
}
}

/*****
* Watchdog timer interrupt routine *
*****/
void    wdt_int() {
    wdts = 1;        /* Set the WDTS register in the beginning of the */
                    /* watchdog timer interrupt routine */
                    /* Newly added in Ver 1.10 */

    while (1) {
        while (!lr_ta0ic) {
        }
        ta0ic = 0x00;
        i++;

        wdts = 1;    /* Set watchdog timer start register */

        if ( i == 500 ) {
            i = 0;
            if ( p10 != 0 ) {
                p10--;
            }
        }
    }
}
}

```

5. Reference Documents

Hardware Manual

M16C/62P Group (M16C/62P, M16C/62PT) Hardware Manual

The latest version can be downloaded from the Renesas Technology website.

Technical Update/Technical News

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REVISION HISTORY	M16C/62P Group Watchdog Timer Operation
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Rev.	Date	Description	
		Page	Summary
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