RX600 Series

CAN Application Programming Interface

Introduction

This application note introduces the Renesas CAN Application Programming Interface and explains how to use it to send, receive, and monitor data on the CAN bus. It also explains briefly some features of the CAN peripheral.

Bundled with this application note are the CAN API driver source code files *config_r_can_rapi.h, r_can_api.h*, and *r_can_api.c*. Note that there are alternative ways to write the driver functions. For example, you may want to write your own driver functions to tailor a specific behavior.

The RX CAN peripheral has 32 CAN mailboxes to read from and write to for communication over CAN. These mailboxes are message 'buffers' and will hold the CAN data frame until it is overwritten by another incoming frame, or rewritten by the application. Each mailbox can be configured dynamically to transmit or receive. Usually, most are configured to receive and a few to transmit.

The term 'mailbox', or in some literature 'message box' or 'message buffer' refers to the physical location where messages are stored inside the MCU's CAN peripheral. In this document, we will use the term 'mailbox'.

Target Device

The following is a list of devices that are supported by this driver:

- RX62N, RX62T, RX62G Groups
- RX630, RX631, RX63N Groups.

Contents

1. Overview

CAN was designed to provide reliable, error-free network communication for applications in which safety and real-time operation cannot be compromised. Its main attributes can be summarized as follows:

• High reliability and noise immunity

• Fewer connections

• Flexible architecture

• Error handling through peripherals

• Low wiring cost

• Scalability

The MCU and bus connectors need only two pins. Therefore, a CAN network is more reliable than other networking schemes that need more wires and connections. Adding new nodes is simple; just tap the bus wire at any point.

CAN is based on a "multiple master, multiple slave" topology. Message or Data Frames transmitted do not contain the addresses of either the transmitting node or of any intended receiving node. This means that any node can act as master or slave at any time. Messages can be broadcast, or sent between nodes, depending on which nodes are listening to a specific ID at that moment. New nodes can be added without having to update others. Such design flexibility makes it practical for building intelligent, redundant, and easily reconfigured systems.

Bit rate determines the number of nodes that can be connected and cable length. Allowed CAN data bit rates are: 62.5, 125, 250, 500 Kbps and 1 Mbps. At the highest speed, the network can support 30 nodes on a 40-meter cable. At lower speeds, the network can support more than 100 nodes on a 1000-meter cable.

The basic building blocks of a CAN network are a CAN microcontroller, the firmware to run it, a CAN transceiver to drive and read the bus signal, and a physical bus media (2 wires). Choose a CAN MCU with enough mailboxes to fit your applications.

1.1 The CAN Peripheral

The CAN peripheral in your CAN MCU must be connected to a bus transceiver located outside the chip, via the CAN Tx and Rx MCU pins. The Protocol Controller reads and writes to the peripheral mailboxes depending on how they are configured. The configuration is done through the CAN Special Function Registers described in your MCU's HW manual. As the registers in the CAN peripheral must be configured and read in the proper sequence to achieve useful communication, a CAN API greatly simplifies this. The API takes numerous tedious issues and does them for you.

After initializing the peripheral, all you need to do is use the receive and transmit API calls, and on a regular basis check for any CAN error states. If an error state is encountered the application can just wait and monitor for the peripheral to recover, as the CAN peripheral takes itself on or off line depending on its state. After a recovery is discovered, the application should restart.

1.2 Communication Layers

The figure below shows the CAN communication layers, with the application layer at the top and the hardware layer at the bottom.
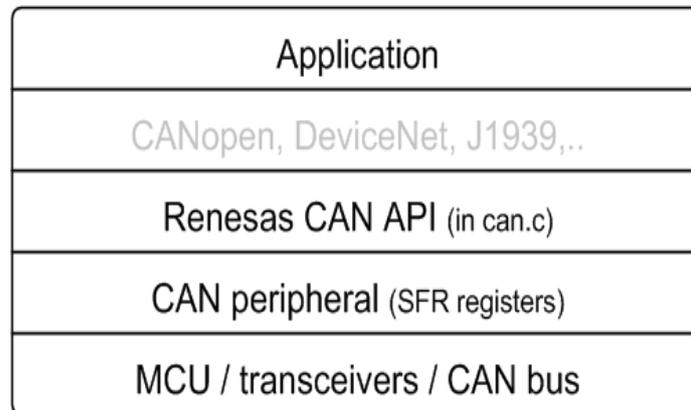

Figure 1. CAN physical and source code layers.

In this document, we will not discuss any higher-level protocols such as CANopen or DeviceNet. (For some Renesas CAN MCUs there is a CANopen solution. Contact your sales representative.)

1.3 The Mailbox

When a CAN message is to be sent, it must first be written to a mailbox by the application firmware. It will then be sent automatically as soon as the bus becomes idle, unless a message of lower ID is sent by another node. If a mailbox is configured to receive, the message is written to the mailbox by the Protocol Controller and must be copied by the user, using the API, to user memory area quickly to free the mailbox for the next message coming from the network.

The API calls will do all the writing to and from the mailbox for you. All you must do is provide application data frame structures which the API functions can write incoming messages to and copy outgoing messages from. It is recommended to have a least one structure for outgoing messages, and one for incoming. For outgoing messages, this could be a local variable (on the stack). For incoming messages one for each mailbox is recommended. This CAN data frame structure, of type can_std_frame_t, is provided by the API header file and has the following structure:

```
typedef struct
{
    uint32_t   id;
    uint8_t dlc;
    uint8_t data[8];
} can_frame_t;
```

Note that the timestamp is not included in this structure, but can easily be added.

Aside from CAN bus arbitration, priority is determined using the lowest mailbox number - except for SH (RCAN-ET) where the highest mailbox has priority. This is true for both transmit and receive operations. If two mailboxes have been set with the same CAN ID, the lowest mailbox number has the highest priority. Because of this, if two mailboxes are configured to receive with the same ID, one mailbox will never receive a message.

1.4 Using Extended ID

To use extended ID, the FRAME_ID_MODE must be set as explained0 below.

When Extended CAN is enabled, the API functions ending in 'Xid' can be called. These functions will automatically cause the ID field of the CAN mailbox to be formatted to use extended ID. In other words, the user need only call these Xid-functions, and the ID value passed in the can_frame_t structure will be sent as a 29-bit ID (instead of 11-bit).

2.   Adding the CAN API to Your Project

Follow the steps below to add the CAN API code to your project. These steps assume that the Flash API has already been added to your project.

1. Copy the **r_can_api** directory (packaged with this application note) to your project directory.

2. Add the file **r_can_api.c** to your project.

3. Add an include path to the **r_can_api** directory.

4. Add an include path to the **r_can_api\src** directory.

5. Configure the middleware with **config_r_can_rapi.h**.

2.1   The CAN Config File

It may be necessary to make modifications to the config_r_can_rapi.h file to customize the application for the way you wish to run it. For example, the option of running in CAN polled mode or CAN interrupt mode is selected in config_r_can_rapi.h.

Note: Do not change anything in the r_can_api.c file, which contains the Renesas CAN API functions for your Renesas MCU.

2.1.1   Interrupts vs. Polling

USE_CAN_POLL

The macro USE_CAN_POLL determines whether CAN mailboxes will be polled for messages received and sent, or whether CAN interrupts should be used. In some version of the driver, you must include or exclude this macro to choose the mode. In other versions, the macro must be set to 0 or 1. See the comments in config_r_can_rapi.h for the version you are using.

2.1.2   CAN Interrupt Settings

CAN0_INT_LVL

Sets the CAN interrupt priority level.

USE_CAN_API_SEARCH (Deprecated)

Not implemented in the latest code releases to not cause confusion with the existing example code. If you have many mailboxes configured and alot of CAN traffic, you may want to consider using this feature. Please see the MCU hardware manual.

2.1.3   Max Time to Poll a Register

MAX_CANREG_POLLCYCLES

Max loops to poll a CAN register bit for expected value. If you are using polled mode, and If you wish to wait a certain time to check that a mailbox has received a frame, increase this value. This can be set to a very low value, but do not set to zero or the mailbox may not be checked at all.

2.1.4   Transceiver Control Pin Mapping

TRANSCEIVER STANDBY, TRANSCEIVER ENABLE

Specify where you have the transceiver control pins connected. Some transceivers may have other control pins. You would have to configure this yourself.

2.1.5   Using Standard/Extended ID

FRAME_ID_MODE

Select what type of CAN ID type to enable in the driver, that is, usage of 11-bit or 29-bit IDs.

FRAME_ID_MODE can be set to STD_ID_MODE, EXT_ID_MODE, or MIXED_ID_MODE. The first two settings enable only those functions belonging to that ID mode. If it is set to mixed mode, the whole API becomes available. It is preferable to not set mixed mode for the driver if it is to be used in a network using only one ID type. Messages with the wrong ID type for the network will only cause wasted bandwidth (no node will receive them).

2.1.6   CAN I/O Port Settings

The specific I/O ports assigned to the CAN peripheral are set in `config_r_can_rapi.h`.

2.1.7   CAN Bitrate Settings

See API *R_CAN_SetBitrate* below and the file `config_r_can_rapi.h`.

3.   The CAN API

The API is a set of functions that allow you to use CAN without having to commit attention to all the details of setting up the CAN peripheral, to be able to easily have your application communicate with other nodes on the network.

Initialization, Port and Peripheral Control
R_CAN_Create (uint32_t ch_nr);
R_CAN_PortSet (uint32_t ch_nr, uint32_t action_type);
  [action_type = ENABLE, DISABLE]
R_CAN_Control (uint32_t ch_nr, uint32_t action_type);
  [action_type = ENTERSLEEP_CANMODE, EXITSLEEP_CANMODE, RESET_CANMODE,
                 HALT_CANMODE, OPERATE_CANMODE, CANPORT_TEST_LISTEN_ONLY,
                 CANPORT_TEST_0_EXT_LOOPBACK, CANPORT_TEST_1_INT_LOOPBACK,
                 CANPORT_RETURN_TO_NORMAL]
R_CAN_SetBitrate (uint32_t ch_nr);

Send
R_CAN_TxSet (uint32_t ch_nr, uint32_t mbox_nr, can_std_frame_t* frame_p, uint32_t frame_type);
R_CAN_TxSetXid (uint32_t ch_nr, uint32_t mbox_nr, can_frame_t* frame_p, uint32_t frame_type);
R_CAN_Tx (uint32_t ch_nr, uint32_t mbox_nr);
R_CAN_TxCheck (uint32_t ch_nr, uint32_t mbox_nr);
R_CAN_TxStopMsg (uint32_t ch_nr, uint32_t mbox_nr);

Receive
R_CAN_RxSet (uint32_t ch_nr, uint32_t mbox_nr, uint32_t stid,   uint32_t frame_type);
R_CAN_RxSetXid (uint32_t ch_nr, uint32_t mbox_nr, uint32_t xid, uint32_t frame_type);
R_CAN_RxRead (uint32_t ch_nr, uint32_t mbox_nr, can_std_frame_t* frame_p);
R_CAN_RxPoll (uint32_t ch_nr, uint32_t mbox_nr);
R_CAN_RxSetMask (uint32_t ch_nr, uint32_t sid_mask_value, uint32_t mask_reg_nr);

Error Check
R_CAN_CheckErr (uint32_t ch_nr);

Figure 2. The CAN API functions.
Note that the hardware manual is the absolute reference for behavior.

The first group of functions (**orange**) in Figure 2 is used to initialize the CAN peripheral registers and configure the MCU's CAN and transceiver ports. The first function, R_CAN_Create, will by default invoke the rest of the initialization functions.

The Transmit functions (**blue**) are used to set up a mailbox to transmit and to check that it was sent successfully.

The Receive functions (**green**) are used to set up a mailbox to receive and to retrieve a message from it.

The Error function (**red**) checks the CAN bus status of the node.

3.1   API Return Codes

R_CAN_OK	Action completed successfully.
R_CAN_NOT_OK	Action did not complete successfully. Usually a more specific return code is used, see below.
R_CAN_SW_BAD_MBX	Bad mailbox number.
R_CAN_BAD_CH_NR	The channel number does not exist.
R_CAN_BAD_ACTION_TYPE	No such action type exists for this function.
R_CAN_MSGLOST	Message was overwritten or lost.
R_CAN_NO_SENTDATA	No message was sent.
R_CAN_RXPOLL_TMO	Polling for received message timed out.
R_CAN_SW_WAKEUP_ERR	The CAN peripheral did not wake up from Sleep mode.
R_CAN_SW_SLEEP_ERR	The CAN peripheral did enter Sleep mode.
R_CAN_SW_HALT_ERR	The CAN peripheral did not enter Halt mode.
R_CAN_SW_RST_ERR	The CAN peripheral did not enter Reset mode.
R_CAN_SW_TSRC_ERR	Time Stamp error.
R_CAN_SW_SET_TX_TMO	Waiting for previous transmission to finish timed out.
R_CAN_SW_SET_RX_TMO	Waiting for previous reception to complete timed out.
R_CAN_SW_ABORT_ERR	Wait for abort timed out.
R_CAN_MODULE_STOP_ERR	Whole CAN peripheral is in stop state (low power). Perhaps the PRCR register was not used to unlock the module stop register.

3.2   CAN bus status return codes

R_CAN_STATUS_ERROR_ACTIVE	Bus Status: Normal operation.
R_CAN_STATUS_ERROR_PASSIVE	Bus Status: The node has sent at least 127 Error frames for either the Transmit Error Counter, or the Receive Error Counter.
R_CAN_STATUS_BUSOFF	Bus Status: The node's Transmit Error Counter has surpassed 255 due to the node's failure to transmit correctly.

3.3   R_CAN_Create

Initializes the CAN peripheral, sets bitrate, and configures CAN interrupts

This function will initialize CAN peripheral registers to prepare it for use. It also sets the bitrate, default mailbox settings, and CAN interrupt levels.

Format

```
uint32_t R_CAN_Create(const uint32_t ch_nr);
```

Arguments

ch_nr   Which CAN bus to use. The number of available channels
                                       depends on part.

Return Values

R_CAN_OK   Action completed successfully.

R_CAN_SW_BAD_MBX   Bad mailbox number.

R_CAN_BAD_CH_NR   The channel number does not exist.

R_CAN_SW_RST_ERR   The CAN peripheral did not enter Reset mode.

R_CAN_MODULE_STOP_ERR   Whole CAN peripheral is in stop state (low power). Perhaps the PRCR
                                       register was not used to unlock the module stop register.

R_CAN_SW_TSRC_ERR   The time stamp counter count reached 0.

See also R_CAN_Control return values.

Properties

Prototyped in r_can_api.h

Implemented in r_can_api.c

Comments

This function wakes the peripheral from CAN Sleep mode. It configures the mailboxes with these default settings:

- Overwrite an unread mailbox data when new frames arrive
- Sets the device to use ID priority (normal CAN behavior, not the optional mailbox number priority).
- Sets all mailboxes' masks invalid.

R_CAN_Create calls the R_CAN_SetBitrate function and configures CAN interrupts if USE_CAN_POLL is disabled in "config_r_can_rapi.h".

Before returning, it clears all mailboxes, sets the peripheral into Operation mode, and clears any errors.

Example

```
/* Init CAN. */
api_status = R_CAN_Create(0);
```

3.4   R_CAN_PortSet

Configures the MCU and transceiver port pins

This function is responsible for configuring the MCU and transceiver port pins. Transceiver port pins such as Enable will vary depending on design, and this function must then be modified.

The function is also used to enter the CAN port test modes, such as Listen Only.

Format

```
uint32_t R_CAN_PortSet(     const uint32_t    ch_nr,
                            const uint32_t    action_type );
```

Arguments

ch_nr   **0,1,2,3**   Which CAN bus to use. The number of available channels depends on part. 1-4 channels may be available.

action_type   Port actions:

    ENABLE   Enable the CAN port pins and the CAN transceiver.

    DISABLE   Disable the CAN port pins and the CAN transceiver.

    CANPORT_TEST_LISTEN_ONLY   Set to Listen Only mode. No Acks or Error frames are sent. See 6.3.

    CANPORT_TEST_0_EXT_LOOPBACK   Use external bus and loopback. *Not tested!*

    CANPORT_TEST_1_INT_LOOPBACK   Only internal mailbox communication.

    CANPORT_RETURN_TO_NORMAL   Return to normal port usage.

Return Values

R_CAN_OK   Action completed successfully.

R_CAN_SW_BAD_MBX   Bad mailbox number.

R_CAN_BAD_CH_NR   The channel number does not exist.

R_CAN_BAD_ACTION_TYPE   No such action type exists for this function.

R_CAN_SW_HALT_ERR   The CAN peripheral did not enter Halt mode.

R_CAN_SW_RST_ERR   The CAN peripheral did not enter Reset mode.

See also R_CAN_Control return values.

Properties

Prototyped in r_can_api.h

Implemented in r_can_api.c

Comments

Make sure this function is called before and after any default port set up function is used (e.g. 'hwsetup'). Otherwise, an output high/low on an MCU CAN port pin could affect the bus. (You may discover when debugging that a hard reset on a node could cause other nodes to go into error mode. The reason may be that all ports were set as default output hi/low before CAN reconfigures the ports. For a brief period, the ports will then be output low and disrupt the CAN bus voltage level.)

You may have to change/add transceiver port pins according to your transceiver.

Example

```
/* Normal CAN bus usage. */

R_CAN_PortSet(0, ENABLE);
```

3.5   R_CAN_Control

Sets CAN operating modes

Controls transition to CAN operating modes determined by the CAN Control register. For example, the Halt mode should be used to later configure a receive mailbox.

Format

```
uint32_t R_CAN_Control(    const uint32_t    ch_nr,
                           const uint32_t    action_type );
```

Arguments

ch_nr   **0,1,2,3**   Which CAN bus to use. The number of available channels depends on part. 1-4 channels may be available.

action_type   Peripheral actions:

    EXITSLEEP_CANMODE   Exit CAN Sleep mode, the default state when the
      peripheral starts up. See 8.

    ENTERSLEEP_CANMODE   Enter CAN Sleep mode to save power.

    RESET_CANMODE   Put the CAN peripheral into Reset mode.

    HALT_CANMODE   Put the CAN peripheral into Halt mode. CAN peripheral
      is still connected but stops communicating.

    OPERATE_CANMODE   Put the CAN peripheral into normal Operation mode.

Return Values

R_CAN_OK   Action completed successfully.

R_CAN_SW_BAD_MBX   Bad mailbox number.

R_CAN_BAD_CH_NR   The channel number does not exist.

R_CAN_BAD_ACTION_TYPE   No such action type exists for this function.

R_CAN_SW_WAKEUP_ERR   The CAN peripheral did not wake up from Sleep mode.

R_CAN_SW_SLEEP_ERR   The CAN peripheral did not enter Sleep mode.

R_CAN_SW_HALT_ERR   The CAN peripheral did not enter Halt mode.

R_CAN_SW_RST_ERR   The CAN peripheral did not enter Reset mode.

See also *R_CAN_PortSet* return values.

Properties

Prototyped in r_can_api.h

Implemented in r_can_api.c

Comments

Other than calling this API to enter Halt mode, CAN mode transitions are called via the other API functions automatically. For example, the default mode when starting up is CAN Sleep mode. Use the API to switch to other operating modes, for example first 'Exit Sleep' followed by 'Reset' to initialize the CAN registers for bitrate and interrupts, then enter 'Halt' mode to configure mailboxes.

Example

```
/* Normal CAN bus usage. */
result = R_CAN_Control(0, OPERATE_CANMODE); //Check that result is = R_CAN_OK.
```

3.6   R_CAN_SetBitrate

Sets the CAN bitrate (communication speed)

The baud rate and bit timing must always be set during the configuration process. It can be changed later if reset mode is entered.

Format
```
void R_CAN_SetBitrate(const uint32_t ch_nr);
```

Arguments

ch_nr   **0,1,2,3**   Which CAN bus to use. 1-4 channels may be available.

Return Values

-

Properties

Prototyped in r_can_api.h

Implemented in r_can_api.c

Comments

A Time quanta, Tq, is one bit-time of the CAN system clock, fcanclk. This is not the CAN bit-time but the internal clock period of the CAN peripheral. This CAN system clock in turn is determined by (twice) the Baud Rate Prescaler value and the peripheral bus clock, fclk, to create the CAN system clock.

Setting the baud rate or data speed on the CAN bus requires some understanding of CAN bit timing and MCU frequency, as well as reading hardware manual figures and tables. The default bitrate setting of the API is 500kB, and unless the MCU clock or peripheral frequencies are changed, it is sufficient to just call the function.

One bit time is divided into a number of Time Quanta, Tqtot. One Time Quantum is equal to the period of the CAN clock. Each bitrate register is then given a certain number of Tq of the total of Tq that make up one CAN bit period.

<u>Formulas to calculate the bitrate register settings</u>.

PCLK is the peripheral clock frequency.

fcan = PCLK

The prescaler scales the CAN peripheral clock down with a factor.

fcanclk = fcan/prescaler

One Time Quantum is one clock period of the CAN clock.

Tq =1/fcanclk

Tqtot is the total number of CAN peripheral clock cycles during one CAN bit time and is by the peripheral built by the sum of the "time segments" and "SS" which is always 1.

Tqtot = TSEG1 + TSEG2 + SS   (TSEG1 must be > TSEG2)

The bitrate is then

Bitrate = fcanclk/Tqtot

SS is always 1. SJW is often given by the bus administrator. Select $1 <= SJW <= 4$.

See CONFIG_R_CAN_RAPI.H for more details.

Example
```
/* Set bitrate as defined in config_r_can_rapi.h. */

R_CAN_SetBitrate(0);
```

3.7   R_CAN_TxSet and R_CAN_TxSetXid

Sets up a mailbox to transmit

R_CAN_TxSet will write to a mailbox the specified ID, data length and data frame payload, then set the mailbox to transmit mode and send a frame onto the bus by calling R_CAN_Tx().

R_CAN_TxSetXid does the same, except if this function is used, the ID will be a 29-bit ID.

Format

```
uint32_t   R_CAN_TxSet(     const uint32_t         ch_nr,
                           const uint32_t         mbox_nr,
                           const can_frame_t*     frame_p,
                           const uint32_t         frame_type        );
```

Arguments

ch_nr	**0,1,2,3**		Which CAN bus to use. 1-4 channels may be available.
mbox_nr	**0-32**		Mailbox to use.
frame_p*			Pointer to a data frame structure in memory. It is an address to the data structure containing the ID, DLC and data that constitute the dataframe the mailbox will transmit.
frame_type	**DATA_FRAME**		Send a normal data frame.
	REMOTE_FRAME		Send a remote data frame request.

Return Values

R_CAN_OK	The mailbox was set up for transmission.
R_CAN_SW_BAD_MBX	Bad mailbox number.
R_CAN_BAD_CH_NR	The channel number does not exist.
R_CAN_BAD_ACTION_TYPE	No such action type exists for this function.

Properties

Prototyped in r_can_api.h

Implemented in r_can_api.c

Comments

This function first waits for any previous transmission of the specified mailbox to complete. It then disables mailbox interrupts temporarily when setting up the mailbox: Sets the ID value for the mailbox, the Data Length Code indicated by frame_p, selects dataframe or remote frame request and finally copies the data frame payload bytes (0-7) into the mailbox. The mailbox is interrupt enabled again unless USE_CAN_POLL was defined. Finally, R_CAN_Tx is called to deliver the message.

Example

```
#define MY_TX_SLOT        7
can_std_frame_t          my_tx_dataframe;
my_tx_dataframe.id = 1;
my_tx_dataframe.dlc = 2;
my_tx_dataframe.data[0] = 0xAA;
my_tx_dataframe.data[1] = 0xBB;
/* Send my frame. */
api_status = R_CAN_TxSet(0, MY_TX_SLOT, &my_tx_dataframe, DATA_FRAME);
```

3.8   R_CAN_Tx

Starts actual message transmission onto the CAN bus

This API will wait until the mailbox finishes handling a prior frame, then set the mailbox to transmit mode.

Format

```
uint32_t  R_CAN_Tx(  const uint32_t    ch_nr,
                     const uint32_t    mbox_nr    );
```

Arguments

ch_nr   **0,1,2,3**   Which CAN bus to use. 1-4 channels may be available.

mbox_nr   **0-32**   Which CAN mailbox to use.

Return Values

R_CAN_OK   The mailbox was set to transmit a previously configured mailbox.

R_CAN_SW_BAD_MBX   Bad mailbox number.

R_CAN_BAD_CH_NR   The channel number does not exist.

R_CAN_SW_SET_TX_TMO   Waiting for previous transmission to finish timed out.

R_CAN_SW_SET_RX_TMO   Waiting for previous reception to complete timed out.

Properties

Prototyped in r_can_api.h

Implemented in r_can_api.c

Comments

R_CAN_TxSet must have been called at least once for this mailbox after system start to set up the mailbox content, as this function only tells the mailbox to send its content.

Example

```
#define MY_TX_SLOT      7

/* Send mailbox content. This mailbox is presumed to have been set up to send some time
in the past. */
R_CAN_Tx(0, MY_TX_SLOT);
```

3.9   R_CAN_TxCheck

Check for successful data frame transmission.

Use to check a mailbox for a successful data frame transmission.

Format

```
uint32_t  R_CAN_TxCheck(   const uint32_t    ch_nr,
                           const uint32_t    mbox_nr    );
```

Arguments

ch_nr	**0,1,2,3**	Which CAN bus to use. 1-4 channels may be available.
mbox_nr	**0-32**	Which CAN mailbox to use.

Return Values

R_CAN_OK	Transmission was completed successfully.
R_CAN_SW_BAD_MBX	Bad mailbox number.
R_CAN_BAD_CH_NR	The channel number does not exist.
R_CAN_MSGLOST	Message was overwritten or lost.
R_CAN_NO_SENTDATA	No message was sent.

Properties

Prototyped in r_can_api.h

Implemented in r_can_api.c

Comments

This function is only needed if an application needs to verify that a message has been transmitted for example so that it can progress a state machine, *or if messages are sent back-to-back*. With CAN's level of transport control built into the silicon, it can reasonably be assumed that once a mailbox has been asked to send with the API that the message will indeed be sent. Safest if of course to use this function after a transmission.

Example

```
/*** TRANSMITTED a particular frame? */
api_status = R_CAN_TxCheck(0, CANBOX_TX);

if (api_status == R_CAN_OK)
{
  /* Notify main application. */
  message_x_sent_flag = TRUE;
}
```

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3.10   R_CAN_TxStopMsg

Stop a mailbox that has been asked to transmit a frame

Format

```
uint32_t R_CAN_TxStopMsg(  const uint32_t    ch_nr,
                           const uint32_t    mbox_nr    );
```

Arguments

ch_nr	**0,1,2,3**	Which CAN bus to use. 1-4 channels may be available.
mbox_nr	**0-32**	Which CAN mailbox to use.

Return Values

R_CAN_OK	Action completed successfully.
R_CAN_SW_BAD_MBX	Bad mailbox number.
R_CAN_BAD_CH_NR	The channel number does not exist.
R_CAN_SW_ABORT_ERR	Waiting for an abort timed out.

Properties

Prototyped in r_can_api.h

Implemented in r_can_api.c

Comments

This function clears the mailbox control flags so that a transmission is stopped (TrmReq is set to 0.) A software counter then waits for an abort a maximum period.

If the message was not stopped, R_CAN_SW_ABORT_ERR is returned. Note that the cause of this could be that the message was already sent.

Example

```
R_CAN_TxStopMsg(0, MY_TX_SLOT);
```

3.11   R_CAN_RxSet and R_CAN_RxSetXid

Set up a mailbox to receive

R_CAN_RxSet The API sets up a given mailbox to receive data frames with the given CAN ID. Incoming data frames with the same ID will be stored in the mailbox.

R_CAN_RxSetXid does the same thing, except if this function is used, the ID will be a 29-bit ID.

Format

```
uint32_t   R_CAN_RxSet(    const uint32_t    ch_nr,
                           const uint32_t    mbox_nr,
                           const uint32_t    id,
                           const uint32_t    frame_type  );
```

Arguments

ch_nr	**0,1,2,3**	Which CAN bus to use. 1-4 channels may be available.
mbox_nr	**0-32**	Which CAN mailbox to use.
sid	**0-7FF_h**	The standard CAN ID which the mailbox should receive.
frame_type	**DATA_FRAME**	Send a normal data frame.
	REMOTE_FRAME	Send a remote data frame request.

Return Values

R_CAN_OK	Action completed successfully.
R_CAN_SW_BAD_MBX	Bad mailbox number.
R_CAN_BAD_CH_NR	The channel number does not exist.
R_CAN_SW_SET_TX_TMO	Waiting for previous transmission to finish timed out.
R_CAN_SW_SET_RX_TMO	Waiting for previous reception to complete timed out.

Properties

Prototyped in r_can_api.h

Implemented in r_can_api.c

Comments

The function will first wait for any previous transmission/reception to complete, then temporarily interrupt disable the mailbox. It sets the mailbox to the given standard ID value, and whether to receive normal CAN dataframes or remote frame requests.

Example

```
#define MY_RX_SLOT       8
#define SID_FAN_SPEED    0x10

R_CAN_RxSet(0, MY_RX_SLOT, SID_FAN_SPEED, DATA_FRAME);
```

3.12   R_CAN_RxPoll

Checks if a mailbox has received a message

Format

```
uint32_t  R_CAN_RxPoll(    const uint32_t    ch_nr,
                           const uint32_t    mbox_nr    );
```

Arguments

ch_nr	**0,1,2,3**	Which CAN bus to use. 1-4 channels may be available.
mbox_nr	**0-32**	Which CAN mailbox to check.

Return Values

R_CAN_OK	There is a message waiting.
R_CAN_NOT_OK	No message waiting or pending.
R_CAN_RXPOLL_TMO	Message pending but timed out.
R_CAN_SW_BAD_MBX	Bad mailbox number.
R_CAN_BAD_CH_NR	The channel number does not exist.

Properties

Prototyped in r_can_api.h

Implemented in r_can_api.c

Comments

When a mailbox is set up to receive certain messages, it is important to determine when it has finished receiving successfully. There are two methods for doing this:

1. Polling. Call the API regularly to check for new messages. USE_CAN_POLL must be defined in the CAN configuration file. If there is a message use R_CAN_RxRead to fetch it.

2. Using the CAN receive interrupt (USE_CAN_POLL *not* defined): Use this API to check which mailbox received. Then notify the application.

The function returns R_CAN_OK if new data was found in the mailbox.

Example

See example in R_CAN_RxRead.

3.13   R_CAN_RxRead

Read the CAN data frame content from a mailbox

The API checks if a given mailbox has received a message. If so, a copy of the mailbox's dataframe will be written to the given structure.

Format

```
uint32_t  R_CAN_RxRead( const uint32_t          ch_nr,
                        const uint32_t          mbox_nr,
                        can_std_frame_t * const  frame_p      );
```

Arguments

ch_nr	**0, 1, 2, 3**	Which CAN bus to use. 1-4 channels may be available.
mbox_nr	**0-32**	Which CAN mailbox to check.
frame_p	*	Refers to a pointer to a data frame structure in memory. It is an address to the data structure into which the function will place a copy of the mailbox's received CAN dataframe.

Return Values

R_CAN_OK	There is a message waiting.
R_CAN_SW_BAD_MBX	Bad mailbox number.
R_CAN_BAD_CH_NR	The channel number does not exist.
R_CAN_MSGLOST	Message was overwritten or lost.

Properties

Prototyped in r_can_api.h

Implemented in r_can_api.c

Comments

Use R_CAN_PollRxCAN() first to check whether the mailbox has received a message.

This function is used to fetch the message from a mailbox, either when using polled mode or from a CAN receive interrupt.

Example

```
#define MY_RX_SLOT    8
can_std_frame_t       my_rx_dataframe;

api_status = R_CAN_RxPoll(0, CANBOX_RX_DIAG);
if (api_status == R_CAN_OK)
  R_CAN_RxRead(0, CANBOX_RX_DIAG, &my_rx_dataframe);
```

3.14   R_CAN_RxSetMask

Sets the CAN ID Acceptance Masks

To accept only one ID, set mask to all ones. To accept all messages, set mask to all zeros. To accept a range of messages, set the corresponding ID bits to zero.

Format

```
void R_CAN_RxSetMask(      const uint32_t    ch_nr,
                           const uint32_t    mbox_nr,
                           const uint32_t    sid_mask_value    );
```

Arguments

ch_nr	**0,1,2,3**	Which CAN bus to use. 1-4 channels may be available.	
mbox_nr	**0-32**	Which mailbox to mask. Four mailboxes will be affected within its group.	
sid_mask_value	**0-7FF$_h$**	Mask value.	

Return Values

-

Properties

Prototyped in r_can_api.h

Implemented in r_can_api.c

Comments

Receive mailboxes can use a mask to filter out one message, or expand receiving to a range of messages (CAN IDs). The mask enables this using the mailbox group's ID field. There is one mask for mailbox 0-3, one for 4-7, etc. <u>Changing a mask will therefore affect the behavior of adjacent mailboxes.</u>

- Each '0' in the mask means "mask this bit", or "don't look at that bit"; accept anything.

- Each '1' means check if the CAN-ID bit in this position matches the CAN-ID of the mailbox.

How to set a mask

Let's say that the range of CAN-IDs that you want to receive in a mailbox is 700-704$_h$. Using standard 11-bit IDs we then have the following Ids in hex and binary:

Hex representation	Bit representation
0x700	11100000000b
0x701	11100000001b
0x702	11100000010b
0x703	11100000011b
0x704	11100000100b

Normally, the mailbox will only accept frames whose ID matches the set receive ID. But if a bit position's MASK value is 0, an ID bit of both 0 and 1 will be accepted.

 If we want to accept all the above, we then set the mask as

11111111000b, or 07F8$_h$.

The CAN receive filter will only look at bit positions b11 (MSB), to b3 (LSB) and whether these match the receive ID of the mailbox.

If we then set a mailbox to the receive ID 0x700 (0x700-0x707 will give the same result) it will accept all IDs from 0x700 to 0x707. Because of this, 0x705 to 0x707 must later be ignored 'manually' by the application software.

Fast filtering of messages with Acceptance Filter Support

If you have used a mask to receive a broad range of message IDs, you must filter for the actual desired messages with firmware. To increase the speed of this search one may use the Acceptance Filter Support instead.

The Acceptance Filter Support Unit (ASU) provides a faster search compared to software filtering of messages using a mask (with the R_CAN_RxSetMask API). Software filtering can be time consuming as the Standard ID bits are rearranged and not stored as a normal word in memory. Another problem could be that the acceptance mask may not be able to be set to receive the particular combination of messages you want. If you set the mask to accept all messages you may have to 'waste' time by checking a long list of the messages using software for each incoming ID. This manual filtering' would also involve having all the IDs in a readable format. An efficient solution in such cases is to use the Acceptance Filter Support Unit.

To use it, one writes the CAN-ID as it is stored in the message box into the ASU. When reading back from the ASU register, the data word is used to search through a table. The data readout has the following parts.
Bit 0-7 = Table 'Address Search Info", ASI, SID10 -3.
Bit 8-15 = "Bit Search Information", BSI, SID0-3 has now been converted to a bit position to enable faster table searches.
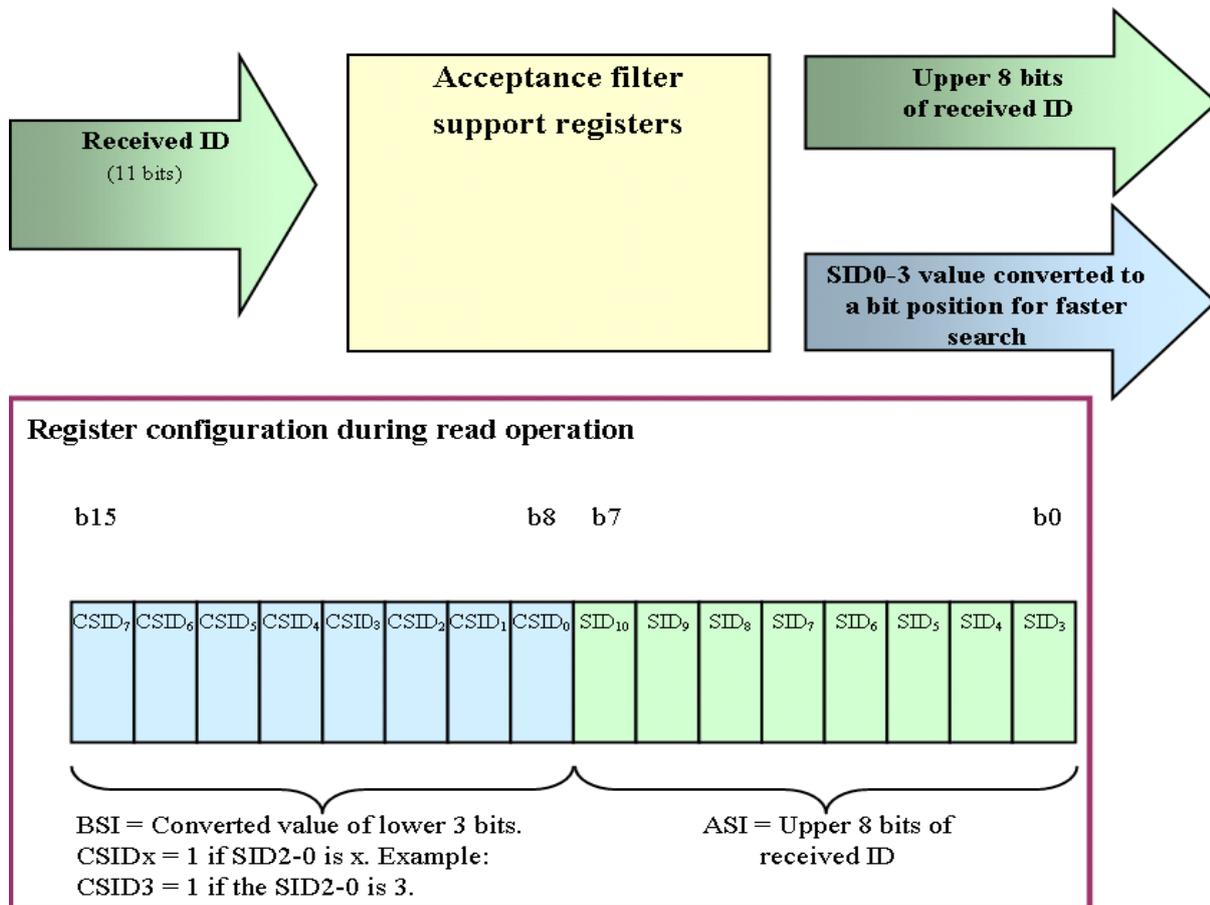
Figure 3. The Acceptance Filter Support Unit (ASU). When read, the representation of the ID is formatted to enable a fast search through a table. This provides a faster response than a search through a 'normal' array of CAN IDs.

The search table. A table must be prepared by the user to check whether an ID is of interest to the application. The firmware must search the table at each byte address ASI and bit position BSI. If a bit BSI-value is set in the user's table, the bit pattern matches the BSI pattern of the register which means the address is of interest to the node, and the frame should be processed by the application.

See REJ05B0276 "CAN Application Note" for more information on how to use the ASU.
Download from www.renesas.com

3.15   R_CAN_CheckErr

Check for bus errors

The API checks the CAN status, or Error State, of the CAN peripheral.

Format

```
uint32_t  R_CAN_CheckErr(const uint32_t  ch_nr);
```

Parameters

ch_nr   **0,1,2,3**   Which CAN bus to use. 1-4 channels may be available.

urn Values

CAN_STATE_ERROR_ACTIVE   CAN Bus Status: Normal operation.

CAN_STATE_ERROR_PASSIVE   CAN Bus Status: The node has sent at least 127 Error frames for either the
                                  Transmit Error Counter, or the Receive Error Counter.

CAN_STATE_BUSOFF   CAN Bus Status: The node's Transmit Error Counter has surpassed 255
                                  due to the node's failure to transmit correctly.

Properties

Prototyped in r_can_api.h

Implemented in r_can_api.c

Comments

The API checks the CAN status flags of the CAN peripheral and returns the status error code. It tells whether the
node is in a functioning state or not and is used for *application* error handling.

It should be polled either routinely from the main loop, or via the CAN error interrupt. Since the peripheral
automatically handles retransmissions and Error frames it is usually of no advantage to include an error
interrupt routine.

If an error state is encountered the application can just wait and monitor for the peripheral to recover, as the CAN
peripheral takes itself on or off line depending on its state. After a recovery is discovered, the application
should restart.

Bus States

CAN is designed to protect network communication in the event that any CAN network node becomes faulty.
Every time the transmitter sees an Error flag, the Transmit Error Counter is increased, and when an error in a
received frame is detected, the Receive Error Counter is increased. The Transmit and Receive Error Counters
are respectively decreased with every successfully transmitted or received frame. In both the Error Active state
(the normal operating state) and the Error Passive State, messages can be transmitted and received.
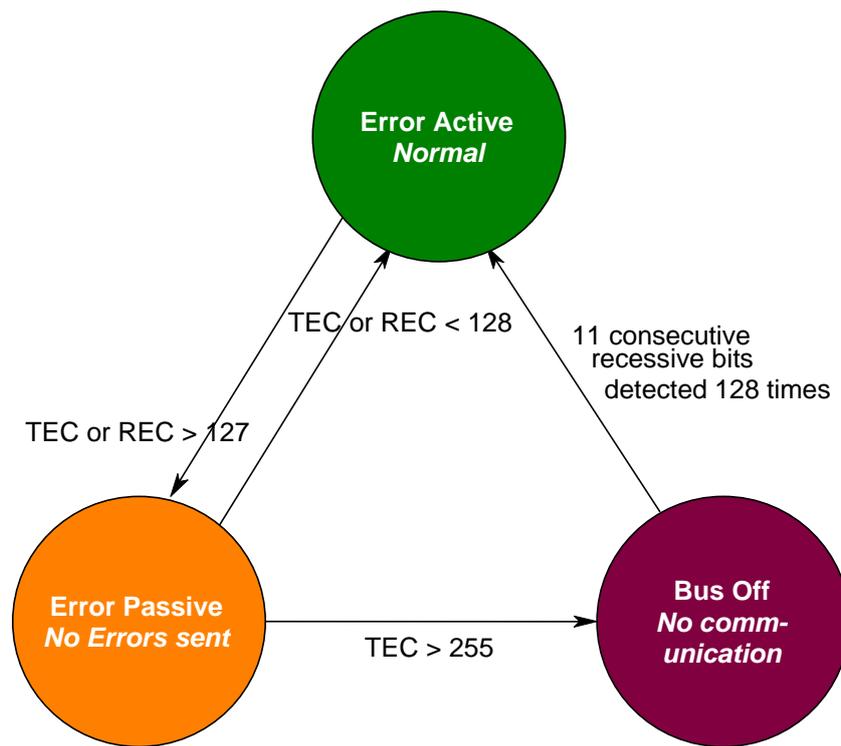
Figure 4. CAN Error States.

Error Active

    When a node is in Error Active state it communicates with the bus normally. If the unit detects an error, it transmits an active Error flag. Once it counts 127 errors, it switches to the Error Passive state.

Error Passive

    When either error counter exceeds 128, the CAN status for that node changes to state Error passive, and messages can still can be transmitted and received, but the node will not send Error frames. Error frames are invisible to the user and are taken care of by the peripheral silicon.

Bus Off

    If the transmit error counter exceeds 255, the CAN node enters the Bus Off state. This prevents a faulty node from causing a bus failure. When serious problems cause a CAN node to enter the Bus Off state, no messages can be transmitted or received by that node until it detects 11 consecutive 'recessive' bits 128 times, or until the peripheral is reset. When the application detects a recovery from Bus Off, the user should reinitialize all registers of the CAN module, and restart the application.

4. Using CAN Polling

Call the API regularly to check the CAN state for the application, so it does not try to communicate if the node is Bus Off. In the following, it is assumed that HandleCanBusState is called once every loop of the main application.
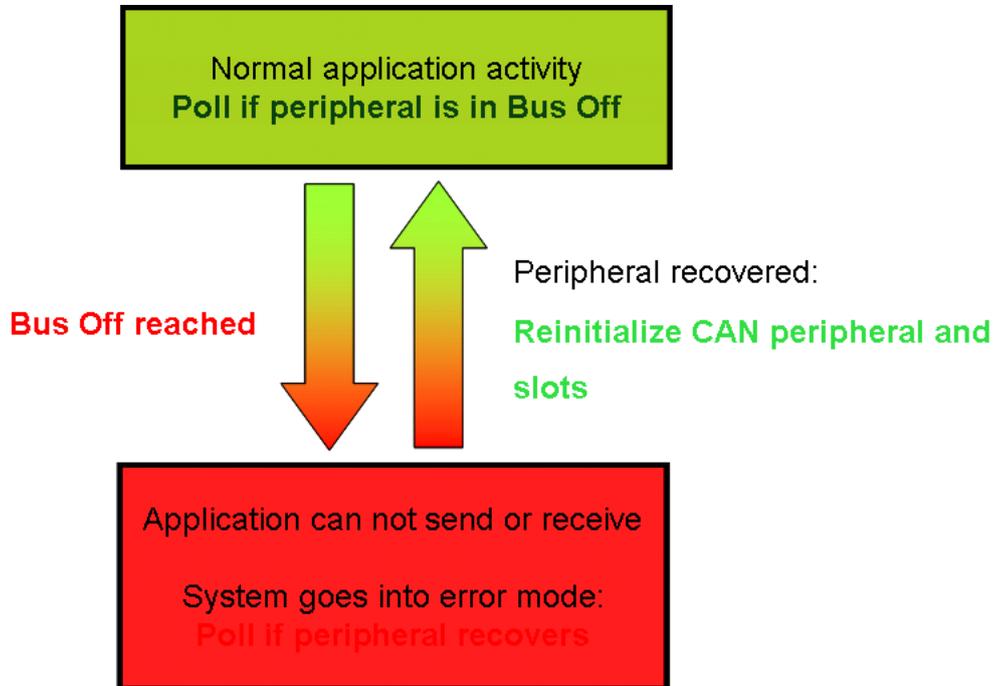
Figure 5. Handling recovery from Bus Off for the application.
(The MCU detects recovery of the bus on its own.)

A node will automatically resume the normal Error Active state again after seeing 11 consecutive recessive bits on the bus 128 times. Note that the time a node spends in Bus Off could be very short, e.g. less than a millisecond.

Poll with the Check Error function once every cycle in the main routine what state the node is in (or use the CAN error interrupt). If the node has reached Bus Off a certain number of times within a certain period, you may want to send a warning message, light an LED etc.

The minimum action required of a node if Bus Off is reached is shown above. Stop trying to communicate and poll the peripheral with the Check Error function to see when the peripheral has returned to the normal Error Active state. When the node has recovered, it is important to reinitialize the CAN peripheral and the application to make sure the slots are in a known state.

Example

```
uint8_t error_bus_status;
/**************************************************************************
Name:                    HandleCanBusState

Parameters:         Bus number, 0 or 1.

Returns:                 -

Description:        Check CAN peripheral bus state.
**************************************************************************/
static void HandleCanBusState(uint8_t  ch_nr)
{
    can_std_frame_t err_tx_dataframe;

    /* Has the status register reached error passive or more? */
    if (ch_nr == 0){
        error_bus_status[ch_nr] = R_CAN_CheckErr(0);
    }
    else {
        error_bus_status[ch_nr] = R_CAN1_CheckErr(1);
    }


    /* Tell user if CAN bus status changed. All Status bits are read only. */
    if (error_bus_status[ch_nr] != error_bus_status_prev[ch_nr]){
        switch (error_bus_status[ch_nr]){
            /* Error Active. */
            case R_CAN_STATUS_ERROR_ACTIVE:
                /* Only report if there was a previous error. */
                if (error_bus_status_prev[ch_nr] > R_CAN_STATUS_ERROR_ACTIVE){
                    if (ch_nr == 0){
                        DisplayString(LCD_LINE1, "bus0: OK");
                    }
                    else {
                        DisplayString(LCD_LINE2, "bus1: OK");// Show user
                    }
                    Delay(0x400000);
                }
```

```
                /* Restart if returned from Bus Off. */
                if (error_bus_status_prev[ch_nr] == R_CAN_STATUS_BUSOFF){
                    /* Restart CAN */
                    if (R_CAN_Create(0) != R_CAN_OK) {
                        app_err_nr |= APP_ERR_CAN_PERIPH;
                    }
                    /* Restart CAN demos even if only one channel failed. */
                    InitCanApp();
                }
            break;
            /* Error Passive. */
            case R_CAN_STATUS_ERROR_PASSIVE: //Continue to Bus off case
            /* Bus Off. */
            case R_CAN_STATUS_BUSOFF:
            default:
                /* The application should take note of the following state and
                Stop communication. */
                app_state[ch_nr] = R_CAN_STATUS_BUSOFF;
                if (ch_nr == 0){
                    DisplayString(LCD_LINE1,"bus0:   ");
                }
                else {
                    DisplayString(LCD_LINE2,"bus1:   ");
                }

                /* Show user */
                LcdShow2DigHex((uint8_t)error_bus_status[ch_nr], ch_nr*16 + 6);
                Delay(0x400000);
                nr_times_reached_busoff[ch_nr]++;
            break;
        }
    }
    error_bus_status_prev[ch_nr] = error_bus_status[ch_nr];
}/* end HandleCanBusState() */
```

5.   Using CAN Error Interrupts.

The CAN error interrupt can be used to check the error state of the node, although polling with the API regularly is usually sufficient since low level error handling is done by the peripheral.

The API can be called from the error ISR to determine the error state, and then flag the application if a state transition has occurred. Most often the Transmit or Receive Error Counter will have just incremented.

Interrupts can be enabled separately for each of: A single error, transition to Error Passive, and transition to Bus Off. If the first of these, the CAN Error interrupt is enabled, an interrupt is generated each time an error is detected. Again, generating this interrupt is usually unnecessary as CAN handles errors on its own.

5.1   CAN Interrupt Checklist

1. Use this checklist to make sure interrupts are set up correctly, and if you are having problems getting the interrupts to trigger.

2. Tell the compiler to do a 'return from interrupt' for your ISR with the #pragma directive. This must be present in the file where the function is defined. Example:
```
#pragma interrupt CAN0_RXM0_ISR(vect=VECT_CAN0_RXM0, enable)
void CAN0_RXM0_ISR(void)
{…
```

3. Are interrupts enabled globally with e.g. the ENABLE_IRQ macro somewhere? If your interrupt does not occur, the flag may have been disabled (by e.g. DISABLE_IRQ). Check by doing this: At a point in the code where your interrupt is expected, set a breakpoint and check that the I-flag is '1'. Check the CPU flag registers for the I-flag.

4. Is the interrupt priority level for the interrupt set to a non-zero value? Check the main Interrupt chapter of the HW manual.

5. Is the respective mailbox's interrupt flag enabled?

6. Is the CAN interrupt mask register set to mask off the interrupt?

7. To nest CAN interrupts, that is, enable other interrupts to preempt an ongoing CAN interrupt, add the "enable" argument to the vector declaration as in the example above.

8. Make sure that the interrupt ISR function is pointed to correctly by the vector table. Check the interrupt base register and count the offset from there to see the CAN interrupt ISR address in the interrupt table.

6.   Test Modes

There are test modes that may be useful for example during product development. There are two loopback modes "Internal" and "External", and a Listen only mode.

6.1   Internal Loopback - Test Node Without CAN Bus

Internal Loopback mode, or Self Test mode, allows you to communicate via the CAN mailboxes without connecting to a bus. This can be useful for testing an application, or self-diagnosis during application debug.

The node acknowledges its own data with the ACK bit in the data frame. The node also stores its own transmitted messages into a receive mailbox if it was configured for that CAN ID. This is normally not possible.
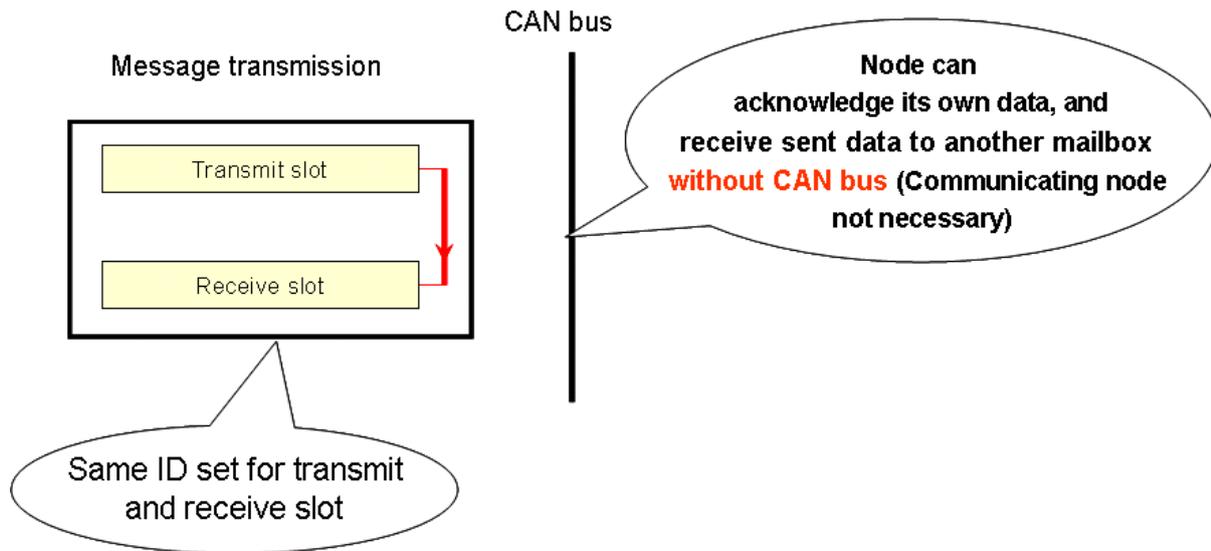
Figure 6. CAN Internal Loopback mode.
This lets you test the functionality of a node without having a CAN bus connected.

Internal Loopback can be convenient when testing as this mode allows the CAN controller to run without sending CAN errors due to no Acks received when the node is alone on the bus, it acknowledges transmitted frames itself.

6.2 External Loopback - Test Lone Node On Bus

External Loopback is like Internal Loopback with the only difference that there must be a CAN bus connected to the node. The messages are acknowledged by the node so the node can be alone on the bus. This is an advantage as nodes can be tested standalone.
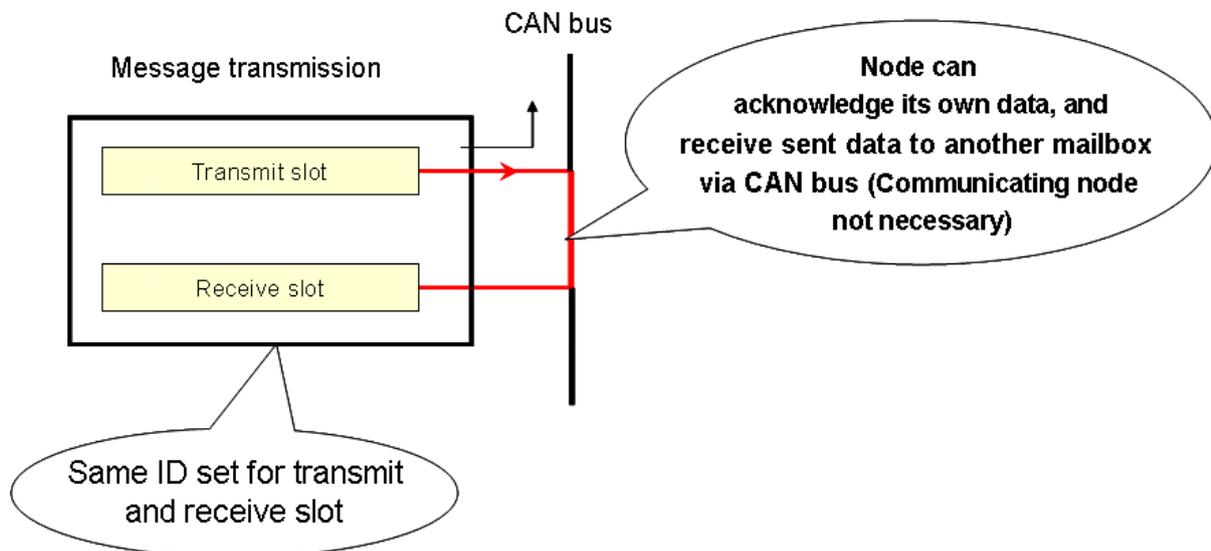
Figure 7. External Loopback. The message is transmitted onto the CAN bus and can be received back on the same node. This is convenient when testing code and when a node is alone on the bus.

6.3 Listen Only (Bus Monitoring) - Test a Node Without Affecting Bus

In Listen Only, or Bus Monitoring, the node is quiet. A node in Listen Only mode will not acknowledge messages or send Error frames etc.

Caution: Mark entering listen only mode clearly in your code so you remember to disable Listen Only mode again! If you only have two nodes on the network and one of them goes into Listen Only, the other node will not get any Acks and will eventually go Bus Off. Also, don't request any transmissions in Listen Only as that is not a correct behavior and the CAN module has not been designed for this.
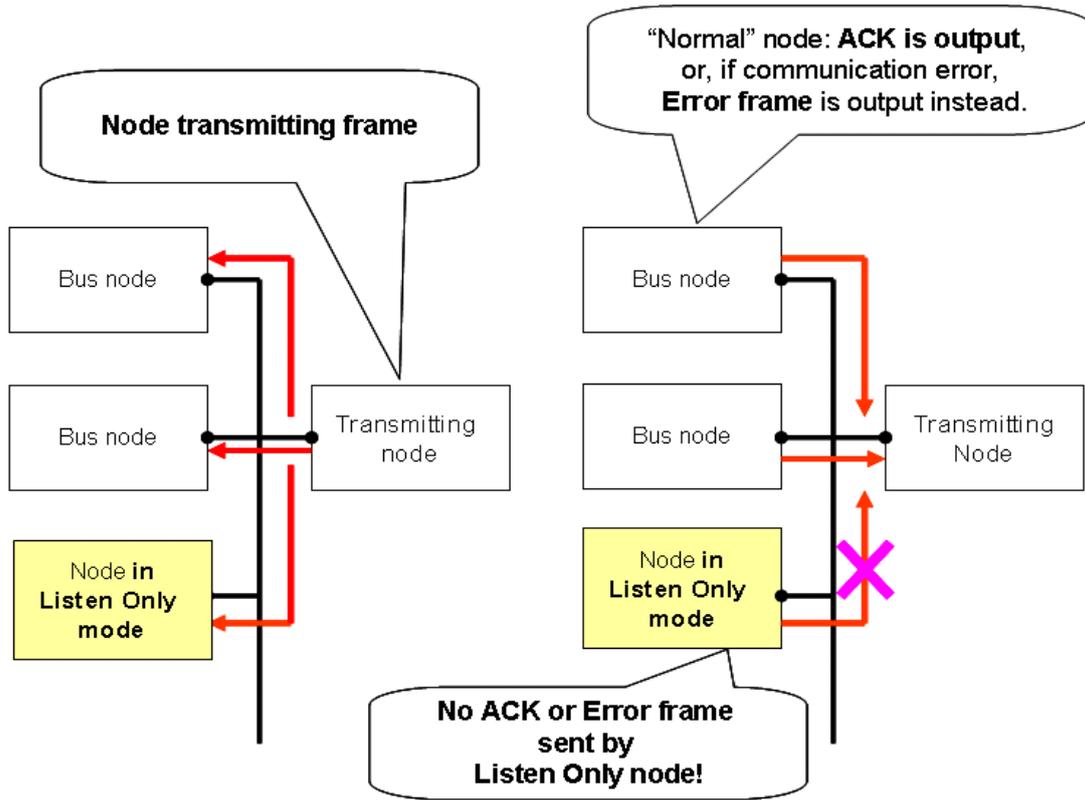
Figure 8. A node in Listen Only mode will not acknowledge messages or send Error frames etc

Listen Only is useful for bringing up a new node that has been added to an existing CAN bus. The mode can be used for a recently connected node's application to ensure that frames have properly been received before going live.

A common usage is to detect a bus's communication speed before letting the new unit go 'live'. Listen Only is not a part of the Bosch CAN specification, but is required by ISO-11898 for bitrate detection.

7.   Time Stamp

The timestamp function captures the value of the on-chip time stamp to a mailbox when a message is received. By examining the time stamp you can for example determine the sequence of messages if they are spread out over multiple receive mailboxes to determine the order of the messages. Time stamp reading is not done by the API, so you will have to poll the mailbox, and if the return value is R_CAN_OK (a message waiting) you can then go in and read the timestamp.
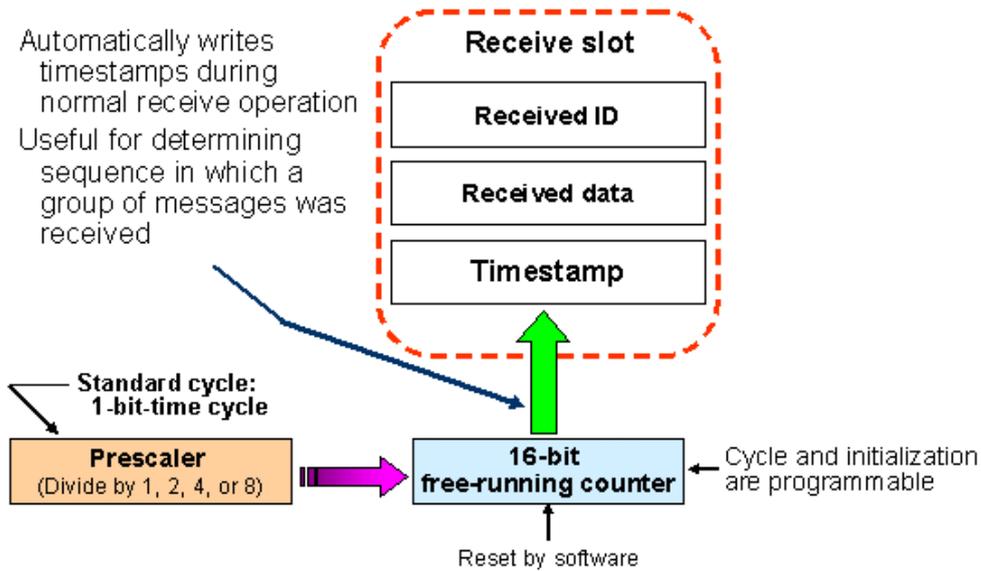
Figure 9. CAN Timestamp is available in each mailbox.

8.   CAN Sleep Mode

The default mode after an MCU reset is CAN Sleep mode. Use the API to switch to other operating modes, see the R_CAN_Control API. Entering the CAN Sleep mode instantly stops the clock supply to the module and thereby reduces power dissipation. All registers remain unchanged when the CAN module enters CAN sleep mode.

9.   CAN FIFO

CAN FIFO buffering is available for the RX. 24 mailboxes can be configured for either transmission or reception. The FIFO can be used by polling or with interrupts, however the drivers accompanying this application note do not provide support for this feature.

Website and Support

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    http://www.renesas.com/

Inquiries
    http://www.renesas.com/inquiry

Revision History

Rev.	Date	Description Page	Summary
1.00	Jun 1, 2010	—	First edition issued
1.10	Jun 6, 2011		AN# changed from REU05B0145 to R01AN0339EU.
			Added RX62T and RX630 projects.
1.11	Oct 2, 2011	4	Changed CAPI_CFG_CAN_ISR to CAPI_CFG_CANx_ISR.
		6	Added API return code "R_CAN_NOT_OK".
		All	Changed "config_r_can_rap.h" to "config_r_can_rapi.h".
2.00	Jan 10, 2011	1	Changed Properties to RX600 Series
		All	Added Extended CAN.
		5	RX63N workspace added.
			Macros to be discontinued going forward (not needed):
			- Changed text under USE_CAN_API_SEARCH
			- Removed CAPI_CFG_CANx_ISR
2.01	May 16, 2012	All	Changes after RSKRX630 CAN App Note review.
			(v.1.10 was reviewed, many were comments already fixed).
	Oct 10, 2012	Code	62G RSK CAN API Demo added.
		AN	Went through all figures to increase visibility. Changed heading Chapter 3.
2.02	Nov 13, 2012	Code	Changed R_CAN_Control() sleep mode, to ensure that Halt mode is entered before exiting sleep.
			can_frame_t to cover both Standard and Ext frames.
2.03	Mar 23, 2013	Code	For RSK63N SW1 changed from P32 to P02.
2.04	Jun 7, 2016	Code	R_CAN_Control() calls had 0 instead of ch_nr in some cases.
2.05	Feb 7, 2018	Code	Fixed incorrect use of mailbox IDE bit when in Extended ID or Mixed ID modes.
			Demo projects ported to e2studio. HEW support dropped.
		1	Identified specific supported target devices.
		1	Removed paragraph with extraneous reference to other legacy MCUs
		28	Noted that FIFO operation is not supported by driver.
		5,6	Changes to config_r_can_rapi.h settings.
		8	Corrections to R_CAN_Create() description.
		All	Corrected Renesas logo colors in header and footer
			Various formatting and renumbering of sections

General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Handling of Unused Pins

   Handle unused pins in accordance with the directions given under Handling of Unused Pins in the manual.

   - The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

2. Processing at Power-on

   The state of the product is undefined at the moment when power is supplied.

   - The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.
     In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.

3. Prohibition of Access to Reserved Addresses

   Access to reserved addresses is prohibited.

   - The reserved addresses are provided for the possible future expansion of functions. Do not access these addresses; the correct operation of LSI is not guaranteed if they are accessed.

4. Clock Signals

   After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.

   - When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

5. Differences between Products

   Before changing from one product to another, i.e. to a product with a different part number, confirm that the change will not lead to problems.

   - The characteristics of Microprocessing unit or Microcontroller unit products in the same group but having a different part number may differ in terms of the internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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