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## M16C/60 Series and M16C/20 Series

# General-purpose Program for Multiplying BCD

#### 1. Abstract

This program multiplies 4-digit BCD using registers.

#### 2. Introduction

This program multiplies 4-digit BCD together by using registers. Set the multiplicand in R1 and the multiplier in R3, respectively. The multiplication result is output to R2 and R0 beginning with the upper half.

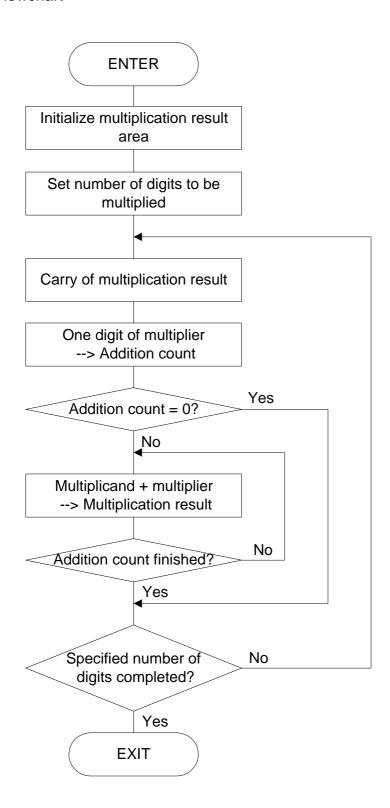
In this program, data for BCD calculation is loaded from the multiplier 4 high-order bits at a time to set an addition count and the multiplicand is added to the multiplication result. The carry deriving from multiplication is shifted in units of 4 bits to the next high-order digit.

Subroutine name : BCD_MULTIPLE4	ROM capacity : 36 bytes
Interrupt during execution : Accepted	Number of stacks used : None

Register/memory	Input	Output	Usage condition
R0	-	Lower part of	<b>←</b>
		multiplication result	
R1	Multiplicand	Does not change	<b>←</b>
R2	-	Upper part of	<b>←</b>
		multiplication result	
R3	Multiplier	Indeterminate	<b>←</b>
A0	-	0000 <sub>16</sub>	Number of digits
			counter
A1	-	0000 <sub>16</sub>	Addition count
Usage precautions	The multiplier is destroy	red on a requit of program av	coution
	The multiplier is destroy	red as a result of program ex	eculion.



### 3. Flowchart





#### 4. The example of a reference program

```
; M16C General-purpose Programs *
; CPU : M16C *
.EQU
              0F0000H
                              ; Declares start address of ROM
; Title : Multiplying 4-digit BCD
; Outline : Multiplies 4-digit BCD using registers.
; Input
       : -----> Output:
                     RO (Lower half of multiplication result)
; R0 ( )
; R1 (Multiplicand)
                         R1 (Does not change)
                         R2 (Upper half of multiplication result)
; R2 ( )
; R3 (Multiplier)
                         R3 (Indeterminate)
                         A0 (Indeterminate)
; A0 ( )
                          A1 (Indeterminate)
; A1 ( )
; Stack amount used: None
; Notes:
.SECTION PROGRAM, CODE
        .ORG VromTOP
                              ; ROM area
BCD_MULTIPLE4:
  MOV.W #0,R0
                              ; Clears multiplication result area
  MOV.W
          #0,R2
  MOV.B #4,A0
                              ; Sets number of digits to be multiplied
BCD MULTIPLE4 10:
  SHL.L #4,R2R0
                              ; Carry processing
         #0001000000000000B,A1
                              ; Specifies for 4 bits to be loaded
  MOV.W
BCD_MULTIPLE4_20:
         #1,R3
                              ; Loads 4 bits
  SHL.W
         A1
  ROLC.W
                               ; Loads addition count
  JNC
       BCD MULTIPLE4 20
                              ; --> Taking 4 bits not completed
  JEQ
       BCD MULTIPLE4 40
                              ; --> Zero (no addition)
BCD_MULTIPLE4_30:
  DADD.W R1,R0
         R2,R0
                              ; Moves high-order data
  XCHG.W
       #0,R0
                              ; Adds C flag to next high-order digit
  DADC.W
                              ; for carry
         R2,R0
                              ; Moves high-order data
  ADJNZ.W #-1,A1,BCD_MULTIPLE4_30
                              ; --> Specified addition count not
                               ; completed
BCD_MULTIPLE4_40:
  ADJNZ.W #-1,A0,BCD_MULTIPLE4_10
                              ; --> Specified digit count to be
                               ; multiplied not completed
  RTS
;
         .END
```



### 5. Reference

SOFTWARE MANUAL
M16C/60 M16C/20 Series SOFTWARE MANUAL
(Acquire the most current version from Renesas web-site)

### 6. Web-site and contact for support

Renesas Web-site

http://www.renesas.com

Contact for Renesas technical support

Mail to: support\_apl@renesas.com



## **REVISION HISTORY**

Rev.	Date	Description		
		Page	Summary	
1.00	Jul 08, 2002	-	First edition issued	



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