RENESAS Tool News

Further Modifying of Specification in Upgraded Revision of Software Tools for Flash Memory Programmer PG-FP5

We announced the revising software tools for the flash memory programmer PG-FP5 in RENESAS TOOL NEWS issued on September 30, 2013. Note that the specification listed in Section 2 was also modified in the revision referred to on September 30.
Please go to the URL below to view the RENESAS TOOL NEWS issued on September 30, 2013.
https://www.renesas.com/search/keyword-search.html#genre=document&q=130930tn3

1. Software Tools and Versions Concerned

The following software tools, which are used for the PG-FP5, are concerned with this modification:
- Firmware for PG-FP5 V2.11
- Programming GUI for PG-FP5 V2.11

To check to see the version numbers of yours, see PG-FP5 Flash Memory Programmer User's Manual.

2. Descriptions of Revision

2.1 MCUs Involved

The RX, SuperH and R8C families of MCUs with data flash

2.2 Specification modified

The sum calculation method for the file checksum function was modified.
Previous method:
   The sum is calculated for the code and data flash areas together
   at one time.
New method:
   After the sum is calculated separately for the code flash area and
   data flash area, the results are added.

   NOTE:
   When the 32-bit arithmetic mode is selected, the checksum calculation
   results are the same between the previous and new methods,

but when the 32-bit CRC mode is selected, the results will differ.

You can check to see the Checksum calculation mode that you have
selected in the Checksum dialog box.
For the method to check it, see Checksum calculation mode selection
in Chapter 4 of PG-FP5 Flash Memory Programmer User's Manual.
Download the manual from here.
https://www.renesas.com/products/software-tools/tools/
programmer/pg-fp5.html#documents