[Notes] PG-FP6 Flash Memory Programmer (RH850/U2A16 Group)

R20TS0775EJ0100 Rev.1.00 Dec. 01, 2021

Outline

When using the PG-FP6 flash memory programmer, note the following point.

1. RH850/U2A16 group

1. RH850/U2A16 Group

1.1 Applicable Product

Programming GUI for PG-FP6 (FP6 Terminal) V1.06.00

1.2 Applicable Devices

RH850 family RH850/U2A16 group: R7F702300

RH850 family: R7F702Z19A

1.3 Details

If "Override SVR parameters" of "RH850 SVR Settings" is unchecked, the SVR parameters, which are written to the option bytes while establishing a connection to the device, are not applied to the device properly. Therefore, execution of a command might cause an error.

RH850 SVR Settings			
Override SVR parameters			
Using program files			
O Disable SVR			

1.4 Workaround

Use Programming GUI for PG-FP6 (FP6 Terminal) V1.05.03.

1.5 Schedule for Fixing the Problem

The problem will be fixed in Programming GUI for PG-FP6 (FP6 Terminal) V1.06.01.

(Scheduled to be released on Jan. 20th, 2022)

Dec.01.21

Revision History

		Description	
Rev.	Date	Page	Summary
1.00	Dec.01.21	1	First edition issued

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