

# RENESAS TECHNICAL UPDATE

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Product Category	System LSI		Document No.	TN-RIN-A026A/E	Rev.	1.00
Title	Ethernet MAC issue and note		Information Category	Technical Notification		
Applicable Product	R-IN32M4-CL2	Lot No.	Reference Document	R-IN32M4-CL2 User's Manual: Peripheral Modules R9J03G019GBG Rev2.00 (R18UZ0035EJ0200)		
	Please refer to [1. Applicable Product] described below for detail.	All lot				

This document describes information about issue and note for the Ethernet MAC. Please use the Ethernet MAC considering those issue and note.

## 1. Applicable Product

Product Type	Model Marking	Product Code
R-IN32M4-CL2	R9J03G019GBG	R9J03G019GBG

## 2. Issue

### 2.1. Transmitting issue concerning with [29] bit of TX mode register

There is a possibility of an unintended TX FIFO underflow interrupt (INTETHTXFIFO) due to [29] (SF) bit setting of the TX Mode register (GMAC\_TXMODE).

### 2.2. Condition

In case bit [29] of TX Mode register (GMAC\_TXMODE) is 0.

### 2.3. Workaround

Bit [29] of TX Mode register (GMAC\_TXMODE) should be 1.

## 3. Note

### 3.1 Notes on transmission/reception of the Jumbo Frame

This product which is described [1.Applicable Product] does not support transmission/reception of the frame with over 1,518bytes what is called "Jumbo Frame".

The contents of this document will be described in the "User's Manual: Peripheral Modules" at the next revision.