Date: May 29, 2018

RENESAS TECHNICAL UPDATE

TOYOSU FORESIA, 3-2-24, Toyosu, Koto-ku, Tokyo 135-0061, Japan Renesas Electronics Corporation

Product Category	System LSI		Document No.	TN-RIN-A025A/E	Rev.	1.00
Title	Ethernet MAC issue and note		Information Category	Technical Notification		
Applicable Product	R-IN32M3 Series	Lot No.	R-IN32M3 Series User's Manual: Peripheral Modules		al:	
	Please refer to [1. Applicable Products] described below for detail.	All lot	Document	R-IN32M3-EC, R-IN32M3-CL Rev10.00 (R18UZ0007EJ1000)		

This document describes information about issue and note for the Ethernet MAC. Please use the Ethernet MAC considering those issue and note.

1. Applicable Products

Product Type	Model Marking	Product Code		
R-IN32M3-EC	MC-10287F1	MC-10287F1-HN4-A		
		MC-10287F1-HN4-M1-A		
	MC-10287BF1	MC-10287BF1-HN4-A		
		MC-10287BF1-HN4-M1-A		
R-IN32M3-CL	D60510F1	UPD60510F1-HN4-A		
		UPD60510F1-HN4-M1-A		
	D60510BF1	UPD60510BF1-HN4-A		
		UPD60510BF1-HN4-M1-A		

RENESAS TECHNICAL UPDATE TN-RIN-A025A/E Date: May 29, 2018 2. Issue 2.1. Transmitting issue concerning with [29] bit of TX mode register There is a possibility of an unintended TX FIFO underflow interrupt (INTETHTXFIFO) due to [29] (SF) bit setting of the TX Mode register (GMAC_TXMODE). 2.2. Condition In case bit [29] of TX Mode register (GMAC_TXMODE) is 0. 2.3. Workaround Bit [29] of TX Mode register (GMAC_TXMODE) should be 1. 3. Note 3.1 Notes on transmission/reception of the Jumbo Frame These products which are described [1.Applicable Products] do not support transmission/reception of the frame with over 1,518bytes what is called "Jumbo Frame". The contents of this document will be described in the "User's Manual: Peripheral Modules" at the next revision.