To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS TECHNICAL UPDATE

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Product Category	MPU & MCU		Document No.	TN-SH7-A735A/E	Rev.	1.00
Title	Error correction about explanation of A/D Control/Status Register（ADCSR） of A/D converter		Information Category	Technical Notification		
Applicable Product	SH7262 Group SH7264 Group	Lot No. ALL	Reference Document	SH7262 Group, SH7264 Group Hardware Manual (REJ09B0445-0200)		

We would like to inform you of the following error corrections about ADC in the above-mentioned applicable products.

Error correction

1. The description of MDS bit in A/D Control/Status Register (ADCSR) is corrected as follows.

[Error]

Multi-scan Mode

These bits select the operating mode for A/D

conversion.

00x: Single mode

100: Multi mode: A/D conversion on 1 to 4 channels

101: Multi mode: Setting prohibited*3

110: Scan mode: A/D conversion on 1 to 4 channels

111: Scan mode: Setting prohibited*3

[Correction]

Multi-scan Mode

These bits select the operating mode for A/D

conversion.

0xx: Single mode

100: Multi mode: A/D conversion on 1 to 4 channels

101: Multi mode: **A/D conversion on 1 to 8 channels** *3

110: Scan mode: A/D conversion on 1 to 4 channels

111: Scan mode: **A/D conversion on 1 to 8 channels** *3

2. The description of CH bit in A/D Control/Status Register (ADCSR) is corrected as follows.

[Error]

Channel Select

These bits and the MDS bits in ADCSR select the analog input channels.

MDS1 = 0	MDS2 = 1, MDS0 = 0	MDS2 = 1, MDS0 = 1
000: AN0	000: AN0	000: AN0
001: AN1	001: AN0, AN1	001: AN0, AN1
010: AN2	010: AN0 to AN2	010: AN0 to AN2
011: AN3	011: AN0 to AN3	011: AN0 to AN3
100: AN4*3	100: AN4*3	100: AN0 to AN4*3
101: AN5*3	101: AN4, AN5*3	101: AN0 to AN5*3
110: AN6*3	110: AN4 to AN6*3	110: AN0 to AN6*3
111: AN7*3	111: AN4 to AN7*3	111: AN0 to AN7*3

[Correction]

Channel Select

These bits and the MDS bits in ADCSR select the analog input channels.

MDS = 0xx	MDS = 100, MDS = 110	MDS = 101, MDS = 111
000: AN0	000: AN0	000: AN0
001: AN1	001: AN0, AN1	001: AN0, AN1
010: AN2	010: AN0 to AN2	010: AN0 to AN2
011: AN3	011: AN0 to AN3	011: AN0 to AN3
100: AN4*3	100: AN4*3	100: AN0 to AN4*3
101: AN5*3	101: AN4, AN5*3	101: AN0 to AN5*3
110: AN6*3	110: AN4 to AN6*3	110: AN0 to AN6*3
111: AN7*3	111: AN4 to AN7*3	111: AN0 to AN7*3