

Brief Description

The ZSSC3027 is a sensor signal conditioner (SSC) integrated circuit for high-accuracy amplification and analog-to-digital conversion of a differential input signal. Designed for high-resolution altimeter module applications, the ZSSC3027 can perform offset, span, and 1st and 2nd order temperature compensation of the measured signal. Developed for correction of resistive bridge sensors, it can also provide a corrected temperature output measured with an internal sensor.

The measured and corrected bridge values are provided at the digital output pins, which can be configured as I^2C^{TM*} ($\leq 3.4 MHz$) or SPI ($\leq 20 MHz$). Digital compensation of signal offset, sensitivity, temperature, and non-linearity is accomplished via an 18-bit internal digital signal processor (DSP) running a correction algorithm. Calibration coefficients are stored on-chip in a highly reliable, non-volatile, multiple-time programmable (MTP) memory. Programming the ZSSC3027 is simple via the serial interface. The IC-internal charge pump provides the MTP programming voltage. The interface is used for the PC-controlled calibration procedure, which programs the set of calibration coefficients in memory.

The ZSSC3027 provides accelerated signal processing in order to support high-speed control, safety, and real-time sensing applications. It complements IDT's ZSSC30x6 products.

Features

- Flexible, programmable analog front-end design; up to 16-bit scalable, charge-balancing twosegment analog-to-digital converter (ADC)
- Fully programmable gain amplifier accepting sensors from 14 to 72 (linear factor)
- Internal auto-compensated temperature sensor
- Digital compensation of individual sensor offset; 1st and 2nd order digital compensation of sensor gain as well as of 1st and 2nd order temperature gain and offset drift
- Layout optimized for stacked-die bonding for high-density chip-on-board assembly
- Typical sensor elements can achieve accuracy of better than ±0.10% FSO** @ -40 to 85°C

Benefits

- Integrated 18-bit calibration math DSP
- Fully corrected signal at digital output
- One-pass calibration minimizes calibration costs
- No external trimming, filter, or buffering components required
- Highly integrated CMOS design
- Excellent for low-voltage and low-power battery applications
- Optimized for operation in calibrated resistive sensor modules

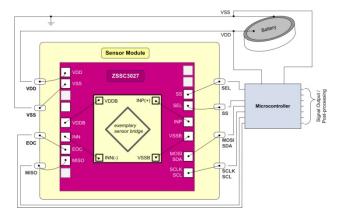
Physical Characteristics

- Supply voltage range: 1.7 to 3.6V
- Operating mode current consumption: 930µA (typical)
- Sleep State current: 20nA (typical)
- Temperature resolution: <0.003K/LSB
- Operation temperatures: -40°C to +85°C
- Small die size
- · Delivery options: die for wafer bonding

Available Support

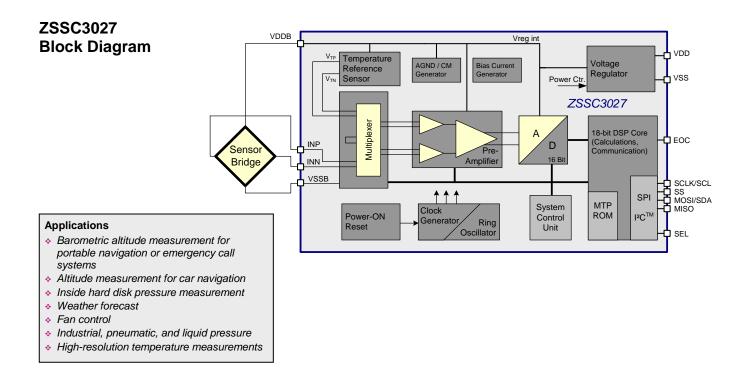
- ZSSC3026 Evaluation Kit can be used to evaluate ZSSC3027 capabilities
- Support Documentation

ZSSC3027 Application Example



- * I^2C^{TM} is a trademark of NXP.
- ** FSO = Full Scale Output.

Low-Power, High-Resolution 16-Bit Sensor Signal Conditioner



Ordering Information (See section 6 in the data sheet for additional options for delivery)

Sales Code	Description	Delivery Package
ZSSC3027AC1B	Die—temperature range: -40°C to +85 °C	Wafer (304µm) unsawn, tested
ZSSC3027AC6B	Die—temperature range: -40°C to +85 °C	Wafer (725µm) unsawn, tested
ZSSC3027AC7B	Die—temperature range: -40°C to +85°C	Wafer (200µm) unsawn, tested
ZSSC3026-KIT	Evaluation Kit for ZSSC3026, including boards, cable, software, and 1 ZSSC3026 PQFN24 sample (equivalent to ZSSC3027—kit is recommended for evaluation of the capabilities of the ZSSC3027)	

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