

## Product Change Notice (PCN)

**Subject:** Datasheet Specification Change for Listed Intersil ISL32173E\* and ISL32177E\* Products

**Publication Date:** 4/26/2016

**Effective Date:** 7/26/2016

**Revision Description:**

Initial Release

**Description of Change:**

This notice is to inform you that Intersil has changed the electrical specification table for Receiver short-circuit current and Receiver input to output delay parameters for the following products.

Intersil Product Number	Intersil Product Number	Intersil Product Number
ISL32173EFBZ	ISL32173EIBZ	ISL32177EFRZ
ISL32173EFBZ-T	ISL32173EIBZ-T	ISL32177EIRZ
ISL32173EFVZ	ISL32173EIVZ	ISL32177EIRZ-T
ISL32173EFVZ-T	ISL32173EIVZ-T	

**Reason for Change:**

The change aligns the data sheet with the product characteristics and is necessary to maintain product manufacturability in support of customer delivery requirements. Details regarding the change are contained on the following page. The updated data sheet is available on the Intersil web site at:

<http://www.intersil.com/content/dam/intersil/documents/isl3/isl32173e-273e-175e-275e-177e-277e.pdf>

**Product Identification:**

There have been no changes to the die/silicon or product itself. There will be no change in the external marking of the packaged parts.

**Qualification status:** Complete

**Sample availability:** 4/26/2016

**Device material declaration:** Available upon request

*Questions or requests pertaining to this change notice, including additional data or samples, must be sent to Intersil within 30 days of the publication date.*

For additional information regarding this notice, please contact your regional change coordinator (below)			
Americas: <a href="mailto:PCN-US@INTERSIL.COM">PCN-US@INTERSIL.COM</a>	Europe: <a href="mailto:PCN-EU@INTERSIL.COM">PCN-EU@INTERSIL.COM</a>	Japan: <a href="mailto:PCN-JP@INTERSIL.COM">PCN-JP@INTERSIL.COM</a>	Asia Pac: <a href="mailto:PCN-APAC@INTERSIL.COM">PCN-APAC@INTERSIL.COM</a>

From:

PARAMETER	SYMBOL	TEST CONDITIONS	TEMP (°C)	MIN (Note 13)	TYP	MAX (Note 13)	UNIT	
Receiver Short-circuit Current, $V_O$ = High or Low	$I_{OS}$	EN = 1, $0V \leq V_O \leq V_{CC}$ (0 to $V_L$ if ISL32177E or ISL32277E)	20Mbps Versions	Full	-	-	±100	mA
			80Mbps Versions	Full	-	-	±155	mA

To:

PARAMETER	SYMBOL	TEST CONDITIONS	TEMP (°C)	MIN (Note 14)	TYP	MAX (Note 14)	UNIT	
Receiver Short-Circuit Current, $V_O$ = High or Low	$I_{OS}$	EN = 1, $0V \leq V_O \leq V_{CC}$ (0 to $V_L$ if ISL32177E or ISL32277E)	20Mbps versions	Full	-	-	±100	mA
			80Mbps versions	Full	-	-	±165	mA

From:

PARAMETER	SYMBOL	TEST CONDITIONS	TEMP (°C)	MIN (Note 13)	TYP	MAX (Note 13)	UNIT
Receiver Input to Output Delay	$t_{PLH}, t_{PHL}$	(Figure 6)	Full	7	11	16	ns

To:

PARAMETER	SYMBOL	TEST CONDITIONS	TEMP (°C)	MIN (Note 14)	TYP	MAX (Note 14)	UNIT
Receiver Input to Output Delay	$t_{PLH}, t_{PHL}$	(Figure 6)	Full	6	11	16	ns