## Old Company Name in Catalogs and Other Documents

On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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740 FAMILY MASK ROM CONFIRMATION FORM
SINGLE-CHIP MICROCOMPUTER M37542M4-XXXSP/FP/GP/HP
RENESAS TECHNOLOGY

Mask ROM number		
	Date :	
eipt	Section head signature	Supervisor signature
Receipt		

Note : Please fill in all items marked ».

		Company name		TEL	ce ure	Submitted by	Supervisor
*	Customer	Date	Date:	( )	Issuan signatu		

Confirmation

Specify the name of the product being ordered.

Three EPROMs are required for each pattern if this order is performed by EPROMs.

One floppy disk is required for each pattern if this order is performed by a floppy disk.

Microcomputer name:

M37542M4-XXXSP M37542M4-XXXHP M37542M4-XXXFP

M37542M4-XXXGP

Ordering by EPROMs

Specify the type of EPROMs submitted.

If at least two of the three sets of EPROMs submitted contain identical data, we will produce masks based on this data. We shall assume the responsibility for errors only if the mask ROM data on the products we produce differs from this data. Thus, extreme care must be taken to verify the data in the submitted EPROMs.

Checksum code for entire EPROM

(hexadecimal notation)

EPROM type (indicate the type used)

27C256     27C512       EPROM address     600016       000016     Area for ASCII       codes of the name     000016       000F16     M37542M4-'       001016     M37542M4-'       000106     C07F16       407F16     Data       ROM (16K-130)     C07F16       PFED16     bytes				-	
000016         Area for ASCII           codes of the name         000016           000F16         'M37542M4-'           001016         'M37542M4-'           4007F16         Data           A08016         Data           ROM (16K-130)         C07F16	27C256				27C512
Area for ASCII     Area for ASCII       codes of the name     of the product       000F16     'M37542M4-'       001016     000F16       407F16     Data       408016     Data       ROM (16K-130)     C07F16	EPROM address		EPF	ROM a	address
7FFE16 7FFF16 FFFF16	000F16 001016 407F16 408016 7FFD16 7FFE16	codes of the name of the product 'M37542M4-' Data ROM (16K-130)	OC C F F	00F16 01016 07F16 08016 FFD16 FFE16	codes of the name of the product 'M37542M4-' Data ROM (16K-130)

- (1) Set the data in the unused area (the shaded area of the diagram) to "FF16".
- (2) The ASCII codes of the product name "M37542M4-" must be entered in addresses 000016 to 000816. And set the data "FF16" in addresses 000916 to 000F16.

The ASCII codes and addresses are listed to the right in hexadecimal notation.

Address 000016 000116 000216 000316 000416 000516	<sup>6</sup> M'=4D16 <sup>6</sup> 3'=3316 <sup>6</sup> 7'=3716 <sup>6</sup> 5'=3516 <sup>6</sup> 4'=3416 <sup>6</sup> 2'=2246	Address 000816 000916 000A16 000B16 000C16	'-' = 2D16 FF16 FF16 FF16 FF16 FF16
000416 000516	'4'=3416 '2'=3216	000C16 000D16	FF16 FF16
000518	'M'=4D16	000D16	FF16 FF16
000716	'4'=3416	000F16	FF16

In the address space of the microcomputer, the internal ROM area is from address C08016 to FFFD16. The reset

vector is stored in addresses FFFC16 and FFFD16.

Mask ROM number

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We recommend the use of the following pseudo-command to set the start address of the assembler source program because ASCII codes of the product name are written to addresses 000016 to 000816 of EPROM.

EPROM type	27C256	27C512
The pseudo-command	∆*=∆\$8000 ∆.BYTE∆ 'M37542M4-'	∆*=∆\$0000 ∆.BYTE∆ 'M37542M4-'

Note : If the name of the product written to the EPROMs does not match the name of the mask confirmation form, the ROM will not be processed.

## Ordering by floppy disk

We will produce masks based on the mask files generated by the mask file generating utility. We shall assume the responsibility for errors only if the mask ROM data on the products we produce differs from this mask file. Thus, extreme care must be taken to verify the mask file in the submitted floppy disk.

The submitted floppy disk must be 3.5-inch 2HD type and DOS/V format. And the number of the mask files must be 1 in one floppy disk.

Microcomputer name:	☐ M37542M4-XXXSP ☐ M37542M4-XXXHP	M37542M4-XXXFP M37542M4-XXXGP
File code		(hexadecimal notation)
Mask file name		.MSK (equal or less than eight characters)
Note: When subm 000016 to 00		o not write data to the product name area (addresses

Write data to only ROM data area (addresses C08016 to FFFD16).

\*2. Mark specification

Mark specification must be submitted using the correct form for the package being ordered. Fill out the appropriate mark specification form (32P4B for M37542M4-XXXSP, 36P2R-A for M37542M4-XXXFP, 32P6U-A for M37542M4-XXXGP, 36PJW-A for M37542M4-XXXHP) and attach it to the mask ROM confirmation form.

Mask ROM number

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\*3. Usage conditions

For our reference of new products, please reply to the following questions about the usage of the products you ordered.

<pre>(1) Which operation source clock you use?     Ceramic resonator     RC oscillation     External clock input     Quartz-crystal oscillation     Other( )     On-chip oscillator</pre> What frequency do you use? f(XIN) =MHz
(2) What is the voltage of power supply (VDD) you use? Typ.= V Min.= V Max.= V
(3) What is the ambient temperature you use? Typ.= °C Min.= °C Max.= °C
(4) Which clock division ratio you use?         □ Double-speed mode (f(φ)=F(XIN))       □ High-speed mode (f(φ)=F(XIN)/2)         □ Middle-speed mode (f(φ)=F(XIN)/8)       □ Applied from on-chip oscillator
(5) Please reply to the following questions about timer function. (i) Which timer you use? Timer1 TimerX TimerA TimerB (ii) Which count source of timer you use? Timer X f(XIN) f(XIN)/2 f(XIN)/16 Timer A f(XIN)/2 f(XIN)/16 f(XIN)/32 f(XIN)/64 f(XIN)/128 f(XIN)/256 On-chip oscillator output Timer B f(XIN)/2 f(XIN)/16 f(XIN)/32 f(XIN)/64 f(XIN)/128 f(XIN)/256 Timer A underflow (iii) Which operating mode you use? Timer X for the fourmer mode Pulse output mode Event counter mode Pulse width measurement mode (iv) Do you use the Output compare? Use () channel Not use (v) Do you use the Input capture? Use () channel Not use
(6) Do you use the Serial I/O? □ Use □ Not use [ Serial I/O1 (□ Clock synchronous Serial I/O1 mode □ Asynchronous Serial I/O1(UART1) mode ) Serial I/O2 (□ Clock synchronous Serial I/O2 mode □ Asynchronous Serial I/O2(UART2) mode )
(7) Do you use the A/D converter?
(8) Do you use the Watchdog timer?
(9) Do you use the oscillation stop detection circuit?
Thank you cooperation.