

Renesas Starter Kit for RL78/G1G

CPU Board Schematics

REV	REF	DATE	DRAWN BY
1.00	Release	15.10.2014	YOI

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Note:

C : Capacitor
D : Diode
R : Fixed Resistor
RV : Potentiometer
U : Integrated Circuit
X : Crystal
RES : Reset Switch
SW : Switch
LED : Light Emitting Diode
PWR : Power Jack
J : Connector, Jumper

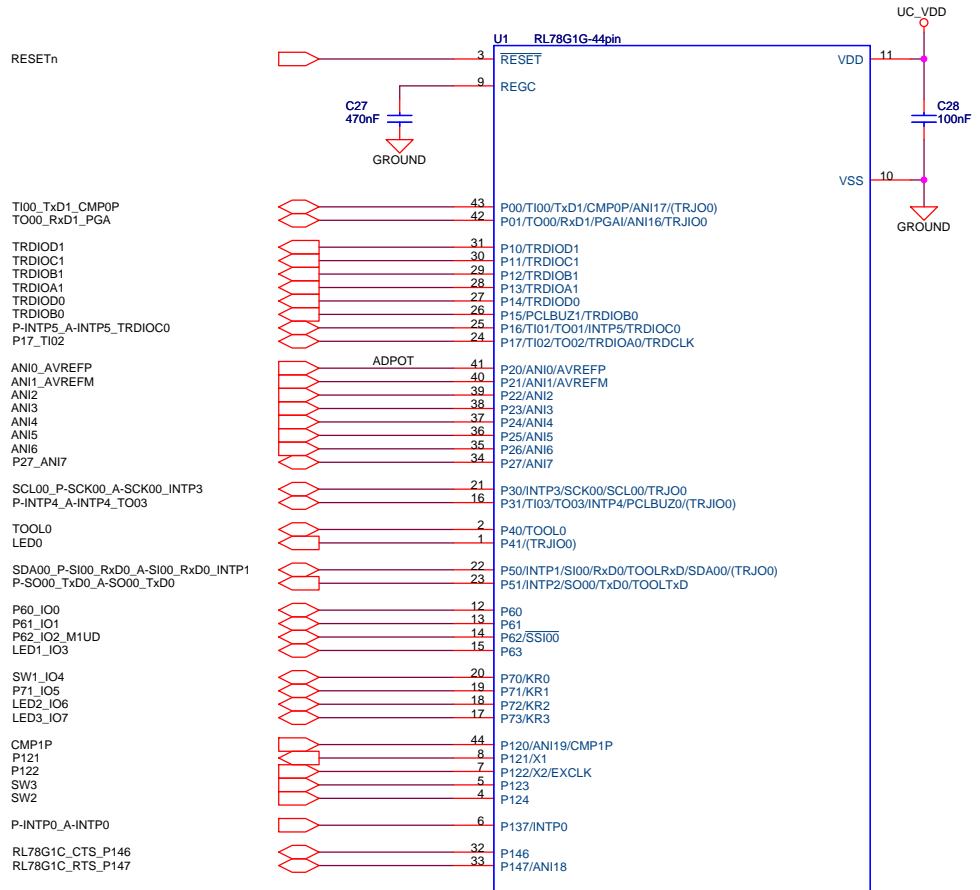
* "DNF" marking means that component does not fit by default.

Board Variation:

R0K5011EFC000BE : RSKRL78G1G MP Board

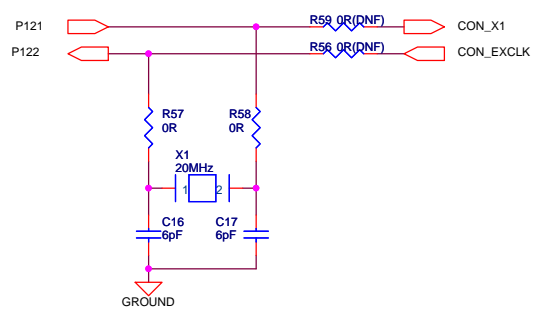
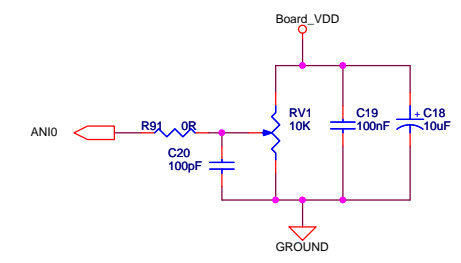
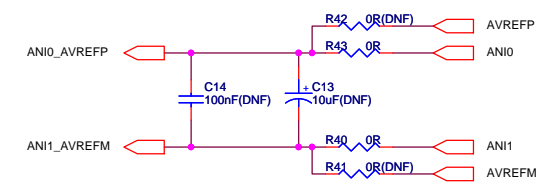
REEL Drawing No. D013202_04

Renesas System Design Co., Ltd.			
Title			
RSKRL78G1G [INDEX]			
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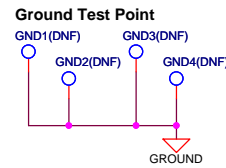
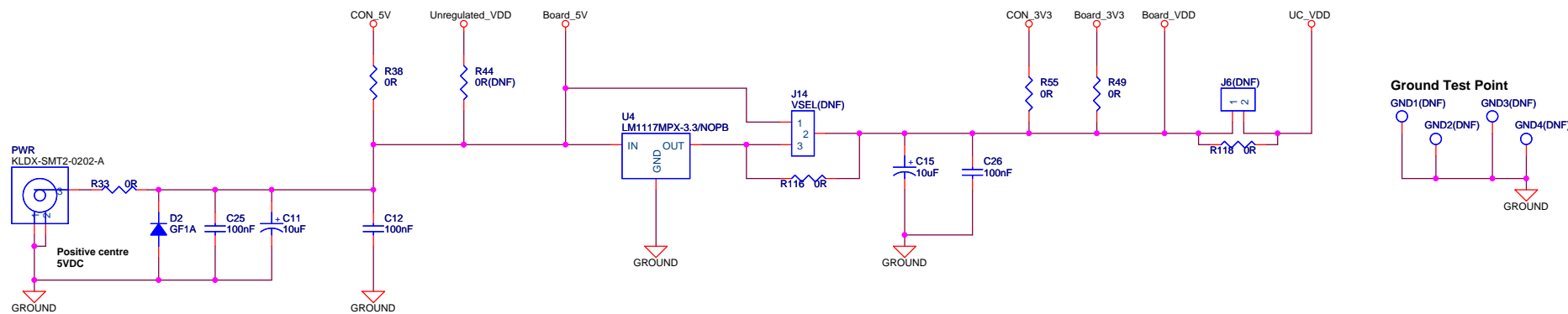


ADC Channel / AVREF Configuration

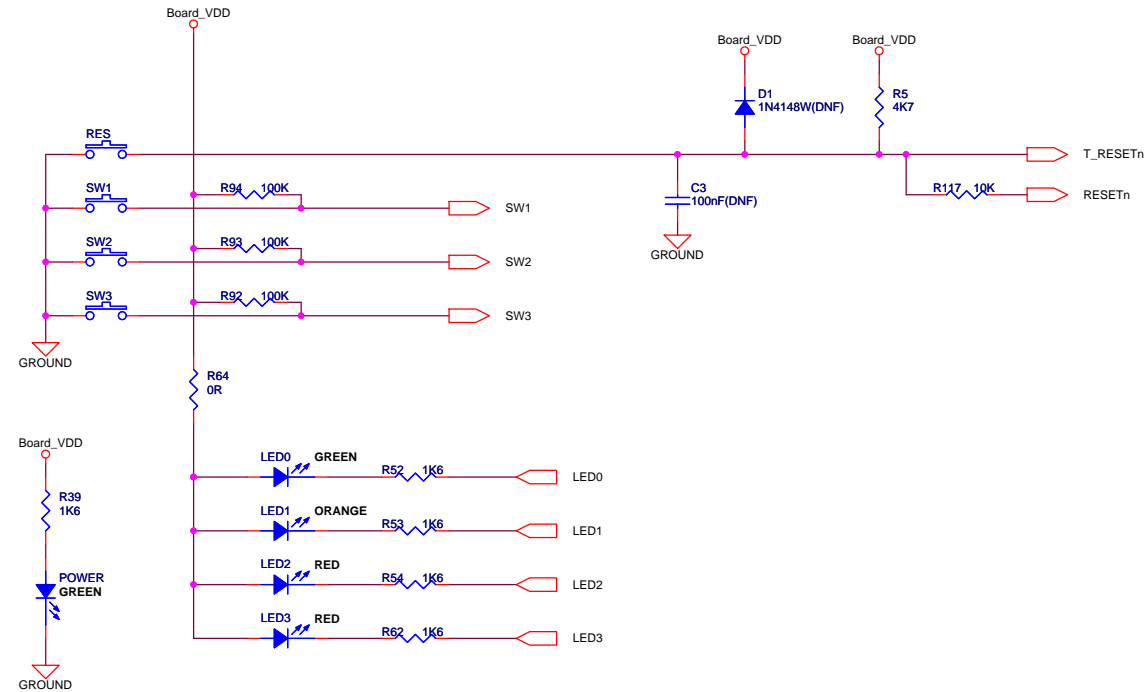
	R41, R42	R40, R43, R91	C13, C14
Enables AD Channel	Remove	Fit	Remove
Enables AVREF	Fit	Remove	Fit



Power Supply Unit

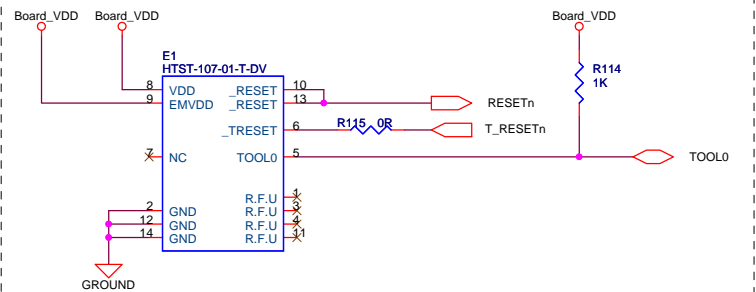


Switches, LEDs, RESET

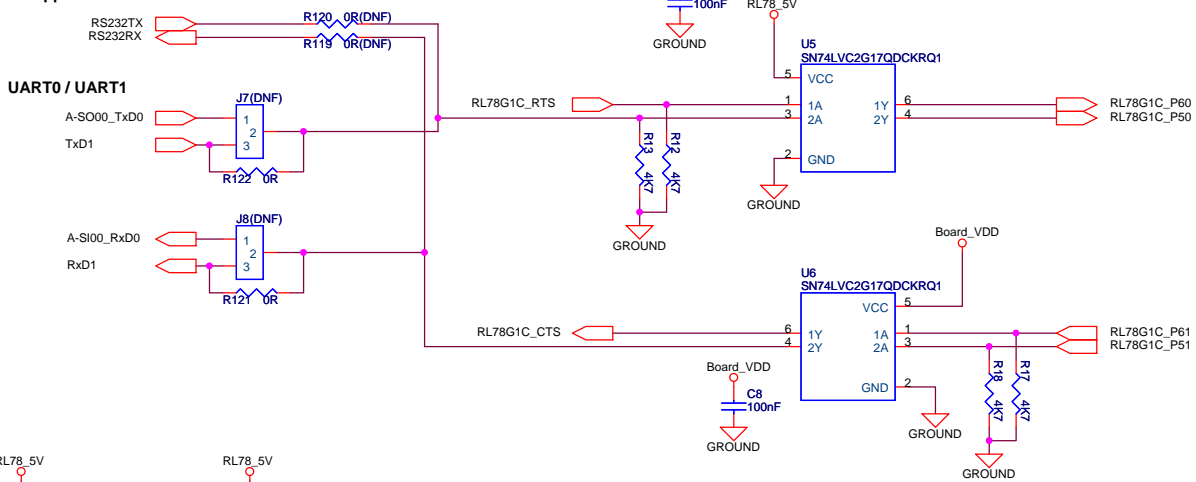


Renesas System Design Co., Ltd.		
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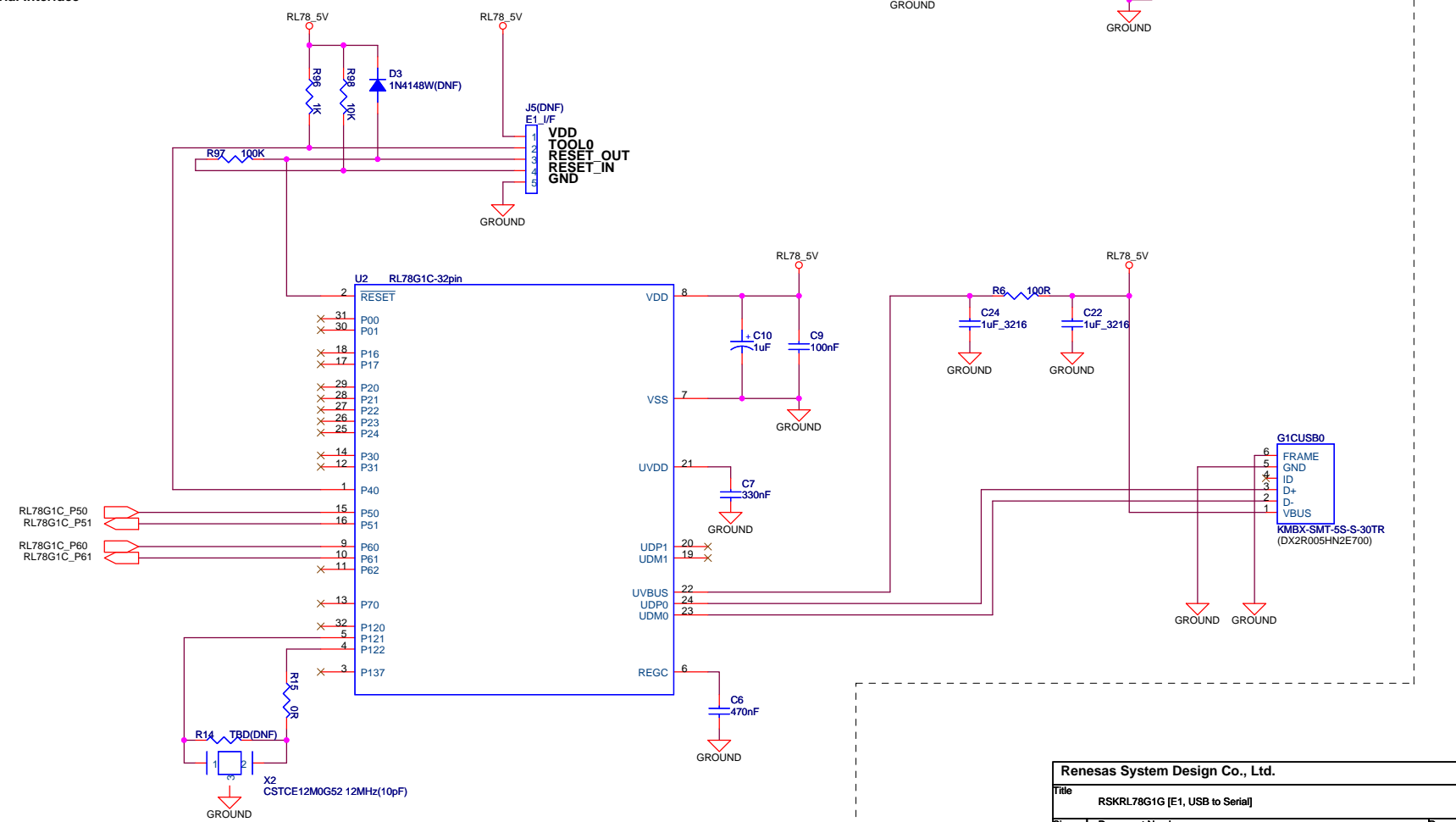
E1 Emulator Interface



To App Header

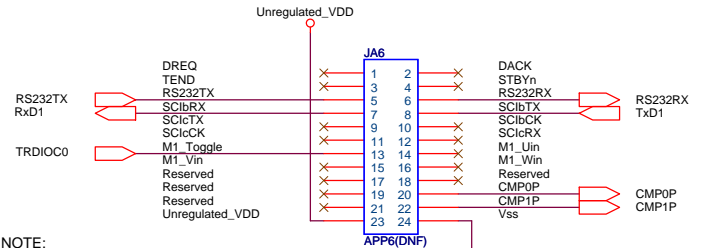
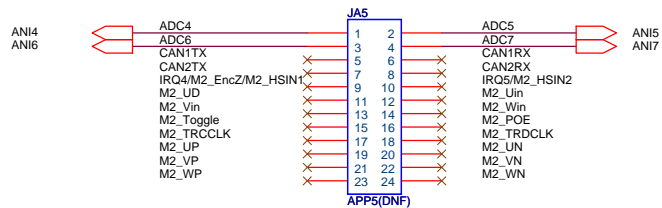
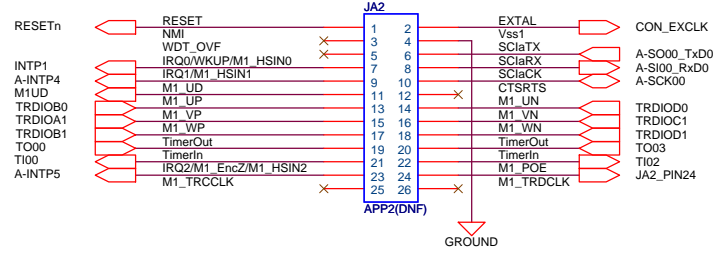
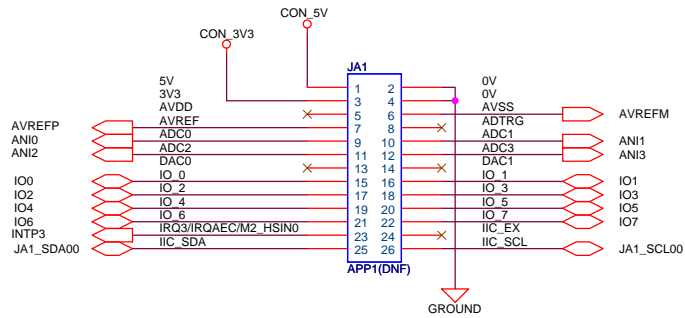


USB to Serial Interface



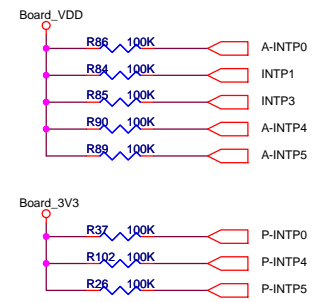
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Application Headers



NOTE:
CMP0P and CMP1P are not standard
RSK App Header Specification.
Normally, JA6.Pin20 and JA6.Pin22 are reserved header.

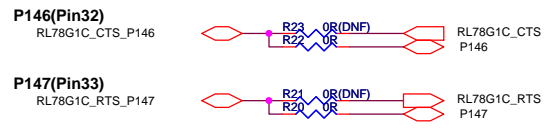
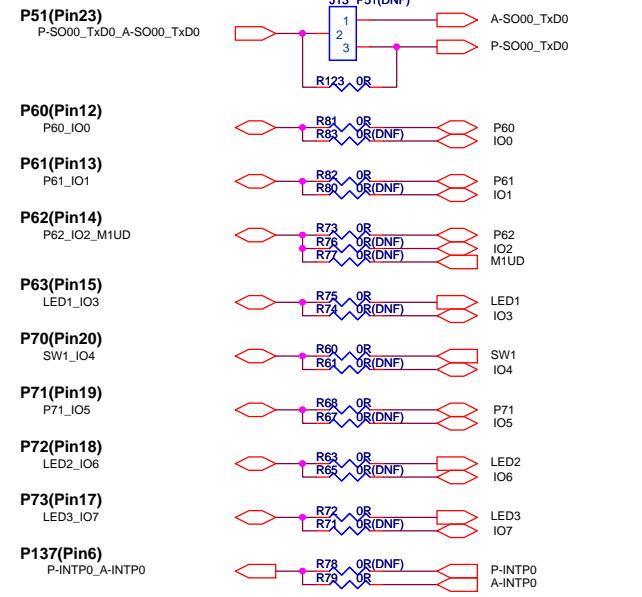
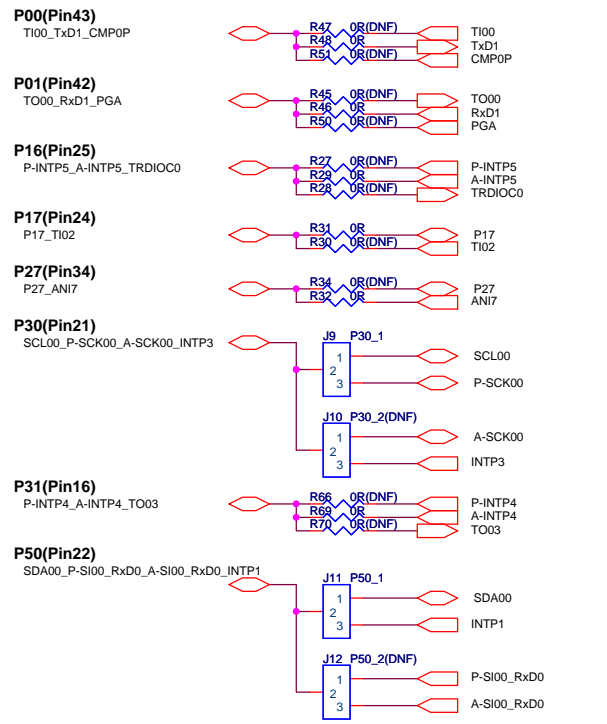
Option pull-up resistor



Header Function Select



MCU Pin Function Select

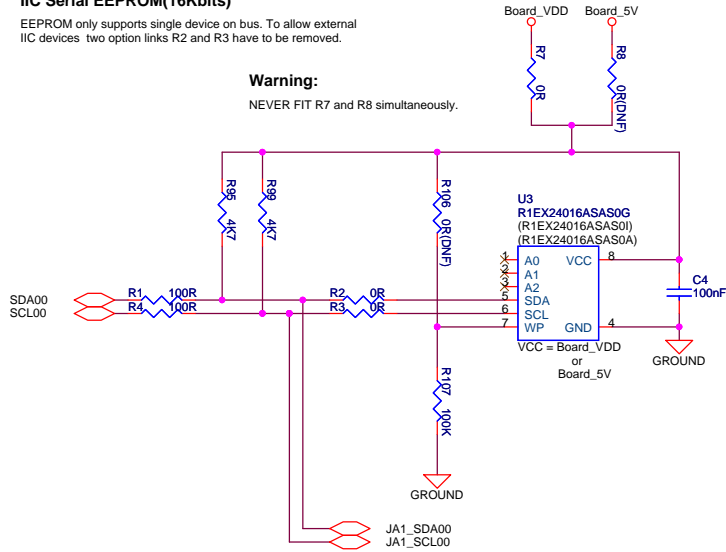


IIC Serial EEPROM(16Kbits)

EEPROM only supports single device on bus. To allow external IIC devices two option links R2 and R3 have to be removed.

Warning:

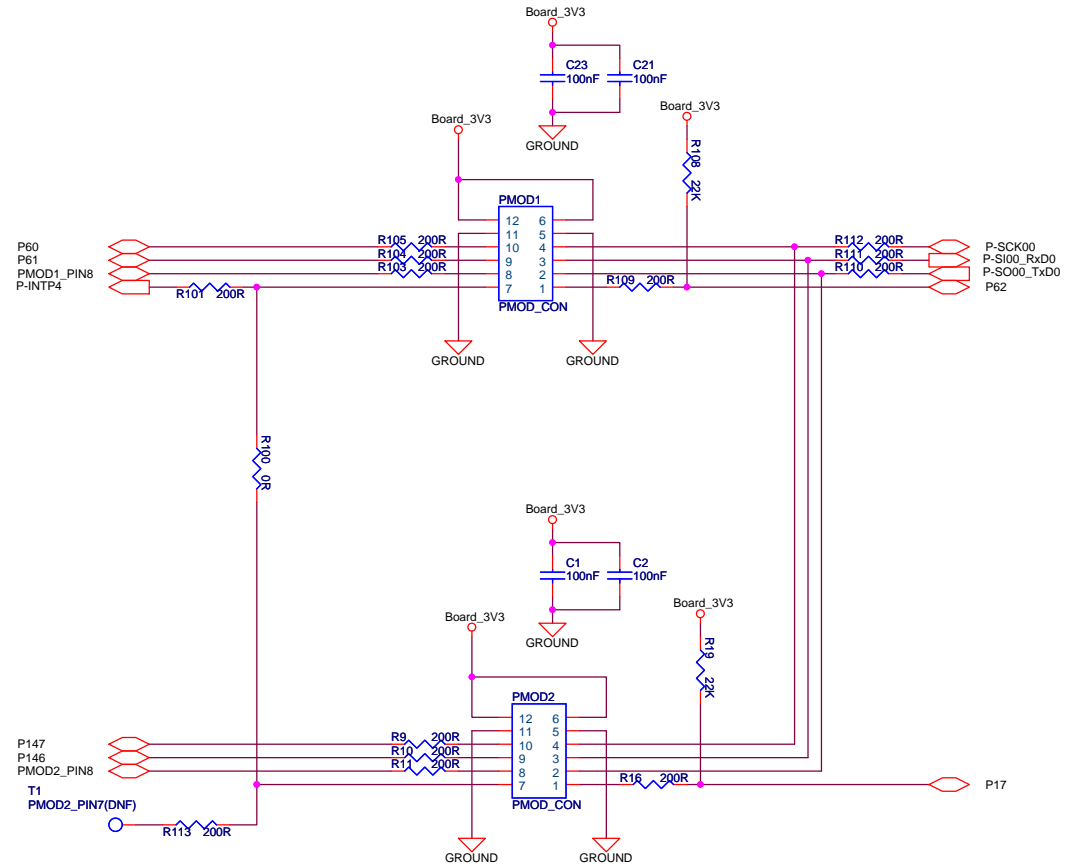
NEVER FIT R7 and R8 simultaneously.



Pmod Interface

PMOD1: Angle type connector (for LCD)

PMOD2: Vertical type connector (Spare)



Pmod Function Select



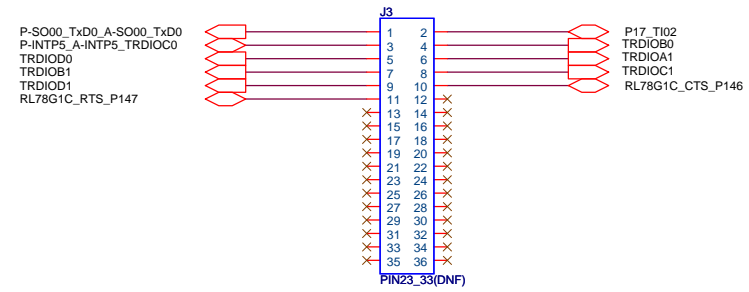
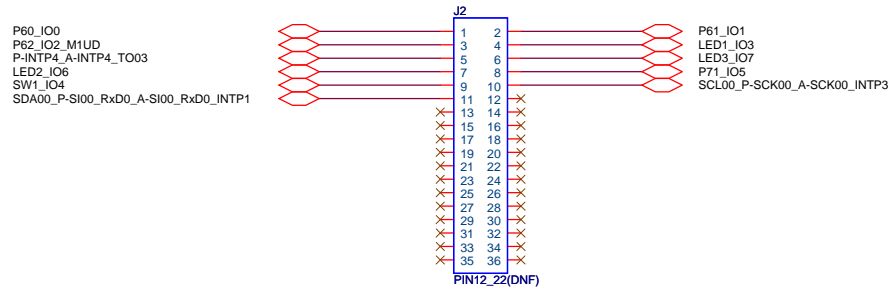
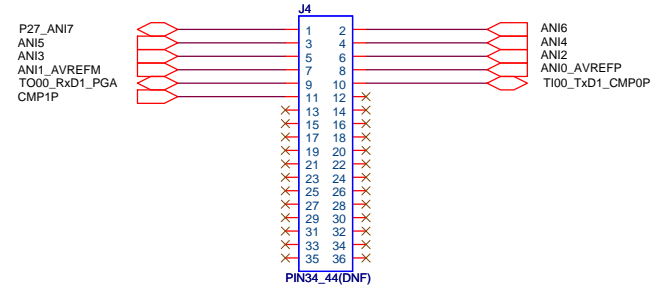
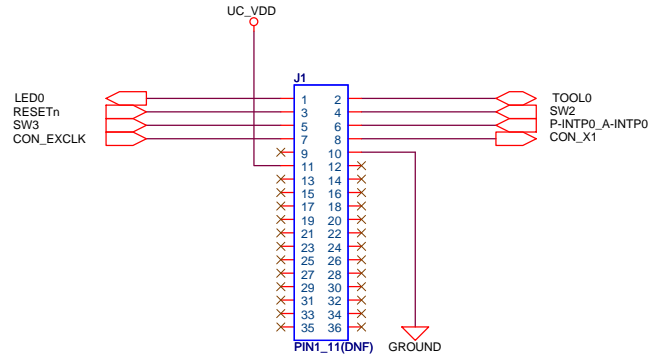
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Title RSKRL78G1G [Pmod, Pmod Header Function Select, IIC EEPROM]

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Microcontroller Pin Headers



Revision History

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1.00	15.10.2014	-	1st release edition.

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