

RJE0607JSP

Silicon P Channel MOS FET Series Power Switching

R07DS0123EJ0200 (Previous: REJ03G1876-0100) Rev.2.00

Sep 01, 2010

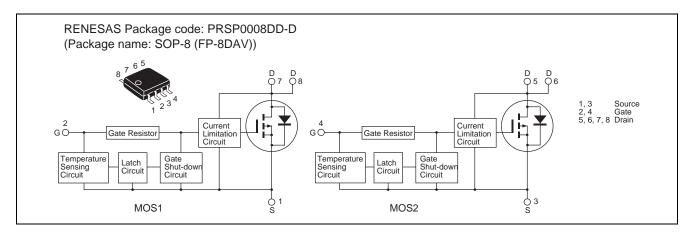
Description

This FET has the over temperature shut-down capability sensing to the junction temperature. This FET has the built-in over temperature shut-down circuit in the gate area. And this circuit operation to shut-down the gate voltage in case of high junction temperature like applying over power consumption, over current etc..

Features

- High endurance capability against to the short circuit.
- Built-in the over temperature shut-down circuit.
- Latch type shut down operation (need 0 voltage recovery).
- Built-in the current limitation circuit.
- Low on-resistance $R_{DS(on)}$: 140 m Ω Typ, 260 m Ω Max ($V_{GS} = -10 \text{ V}$)
- High density mounting

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	-60	V
Gate to source voltage	V_{GSS}	-16	V
Gate to source voltage	V_{GSS}	2.5	V
Drain current	I _D Note5	-1.5	Α
Body-drain diode reverse drain current	I_{DR}	-1.5	А
Avalanche current	I _{AP} Note 4	-1.5	Α
Avalanche energy	E _{AR} Note 4	9.6	mJ
Channel dissipation	Pch Note 2	2	W
Channel dissipation	Pch Note 3	1.5	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. Value at $Tc = 25^{\circ}C$

- 2. 1 Drive operation : When using the glass epoxy board (FR4 40 \times 40 \times 1.6 mm), PW \leq 10 s
- 3. 2 Drive operation : When using the glass epoxy board (FR4 $40 \times 40 \times 1.6$ mm), PW ≤ 10 s
- 4. Tch = 25°C, Rg \geq 50 Ω
- 5. It provides by the current limitation lower bound value.

Typical Operation Characteristics

 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Input voltage	V_{IH}	-3.5	_	_	V	
	V_{IL}	_	_	-1.2	V	
Input current	I _{IH1}	_	_	-100	μΑ	$Vi = -8 V, V_{DS} = 0$
(Gate non shut down)	I _{IH2}	_	_	-50	μΑ	$Vi = -3.5 \text{ V}, V_{DS} = 0$
	l _{IL}	_	_	-10	μΑ	$Vi = -1.2 \text{ V}, V_{DS} = 0$
Input current	I _{IH(sd)1}	_	-0.8	_	mA	$Vi = -8 V, V_{DS} = 0$
(Gate shut down)	I _{IH(sd)2}	_	-0.35	_	mA	$Vi = -3.5 \text{ V}, V_{DS} = 0$
Shut down temperature	Tsd	_	175	_	°C	Channel temperature (dv/dt V _{GS} ≥ 500 V/ms)
Gate operation voltage	Vop	-3.5	_	-12	V	(uv/ut vgs ≥ 300 v/ms)
Drain current (Current limitation value)	I _{D limt}	-1.5	_	_	Α	$V_{GS} = -12 \text{ V}, V_{DS} = -10 \text{ V}^{\text{Note 4}}$

Notes; 6. Pulse test

Electrical Characteristics

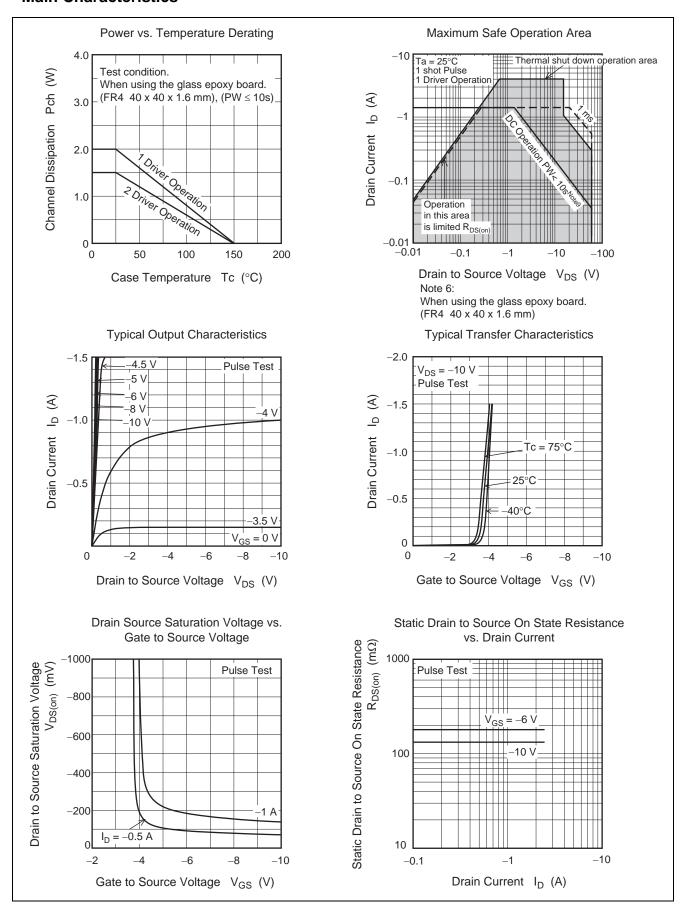
 $(Ta = 25^{\circ}C)$

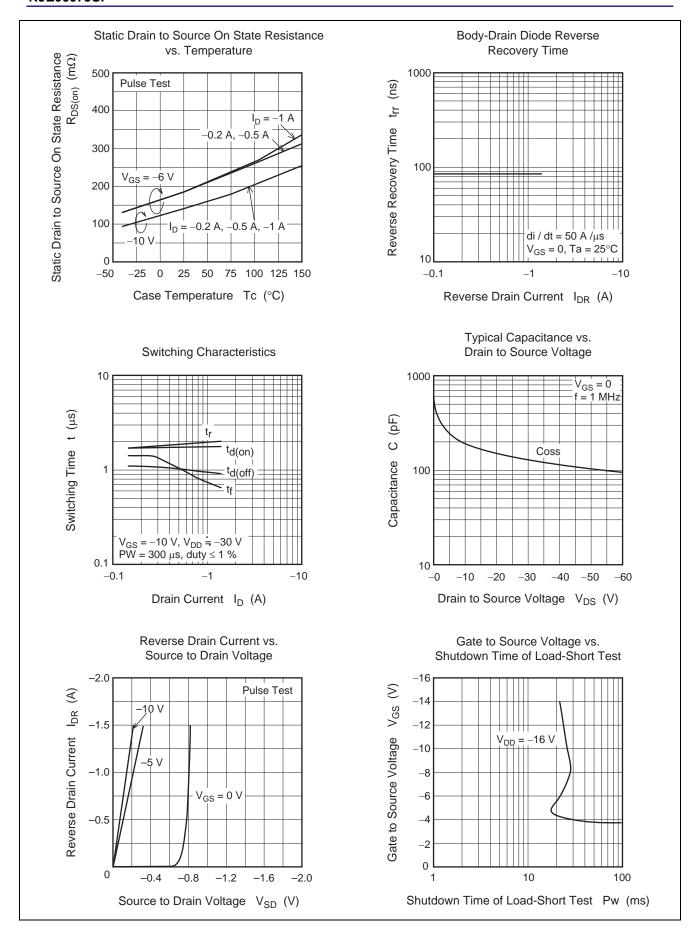
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain current	I _{D1}	-	_	-2	Α	$V_{GS} = -3.5 \text{ V}, V_{DS} = -10 \text{ V}$
	I _{D2}		_	-10	mA	$V_{GS} = -1.2 \text{ V}, V_{DS} = -10 \text{ V}$
	I _{D3}	-1.5	_	_	Α	$V_{GS} = -12 \text{ V}, V_{DS} = -10 \text{ V}^{\text{Note 7}}$
Drain to source breakdown voltage	$V_{(BR)DSS}$	-60	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown	V _{(BR)GSS}	-16	_	_	V	$I_G = -800 \mu A, V_{DS} = 0$
voltage	V _{(BR)GSS}	2.5	_	_	V	$I_G = 100 \mu A, V_{DS} = 0$
Gate to source leak current	I _{GSS1}	_	_	-100	μA	$V_{GS} = -8 \text{ V}, V_{DS} = 0$
Sale to source leak current	I _{GSS1}			-50	μΑ	$V_{GS} = -3.5 \text{ V}, V_{DS} = 0$
	I _{GSS2}			-10	μΑ	$V_{GS} = -1.2 \text{ V}, V_{DS} = 0$
	I _{GSS4}	_	_	100	μΑ	$V_{GS} = 1.2 \text{ V}, V_{DS} = 0$ $V_{GS} = 2.4 \text{ V}, V_{DS} = 0$
Input current (shut down)	1 -		-0.8		mΑ	$V_{GS} = -8 \text{ V}, V_{DS} = 0$
input current (shut down)	I _{GS(OP)1}		-0.35	_	mA	$V_{GS} = -3.5 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{GS(OP)2}		-0.55	-10	μА	$V_{DS} = -60 \text{ V}, V_{GS} = 0$
Zero gate voltage drain current	I _{DSS1}			-10 -10	μΑ	$V_{DS} = -48 \text{ V}, V_{GS} = 0$
	I _{DSS2}			-10	μΛ	Ta = 125°C
Gate to source cutoff voltage	V _{GS(off)}	-2.2	_	-3.4	V	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$
Static drain to source on state	R _{DS(on)}		185	380	mΩ	$I_D = -0.75 \text{ A}, V_{GS} = -6 \text{ V}^{\text{Note 7}}$
resistance	R _{DS(on)}	_	140	260	mΩ	$I_D = -0.75 \text{ A}, V_{GS} = -10 \text{ V}^{\text{Note 7}}$
Output capacitance	Coss	_	194	_	pF	V _{DS} = -10 V, V _{GS} = 0, f = 1MHz
Turn-on delay time	t _{d(on)}	_	1.82	_	μS	$V_{GS} = -10 \text{ V}, I_{D} = -0.75 \text{ A},$
Rise time	t _r	_	1.95	_	μS	R _L = 40 Ω
Turn-off delay time	t _{d(off)}	_	0.99	_	μS	_
Fall time	t _f	_	0.84	_	μS	
Body-drain diode forward voltage	V _{DF}	_	0.83	_	V	$I_F = -1.5 \text{ A}, V_{GS} = 0$
Body-drain diode reverse	t _{rr}	_	85	_	ns	$I_F = -1.5 \text{ A}, V_{GS} = 0$
recovery time						di _F /dt = 50 A/μs
Over load shut down operation time Note 8	t _{os1}	_	18.6	_	ms	$V_{GS} = -5 \text{ V}, V_{DD} = -16 \text{ V}$

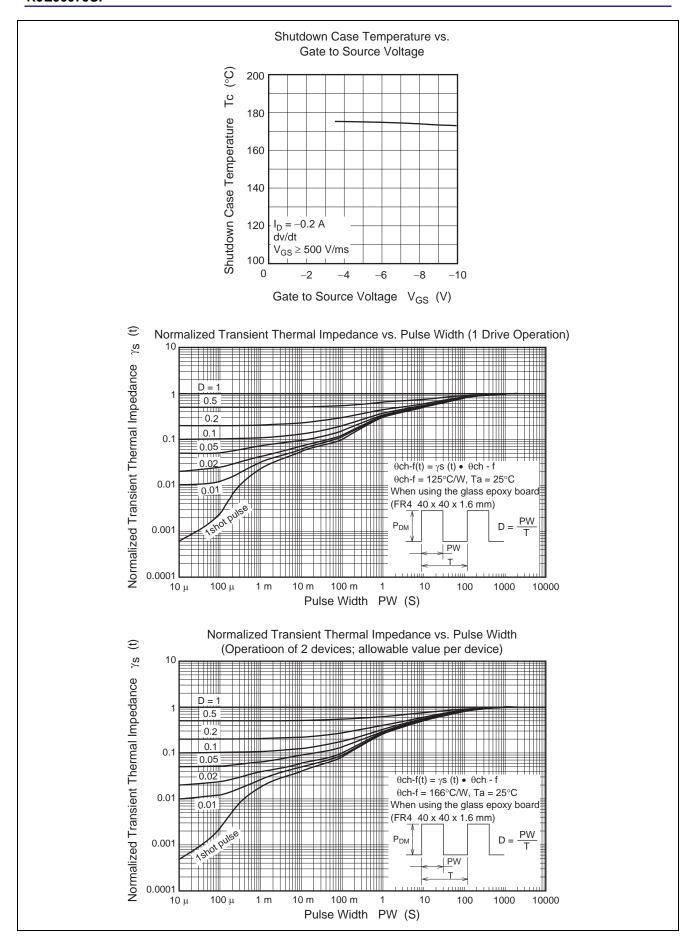
Notes: 7. Pulse test

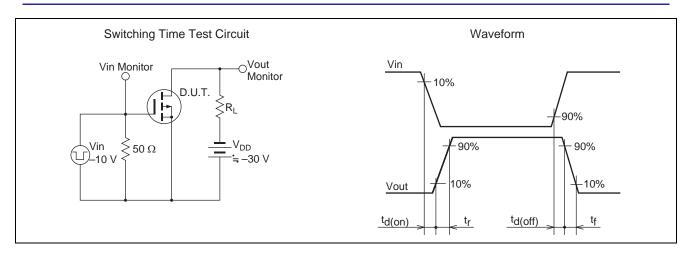
8. Including the junction temperature rise of the over loaded condition.

Main Characteristics

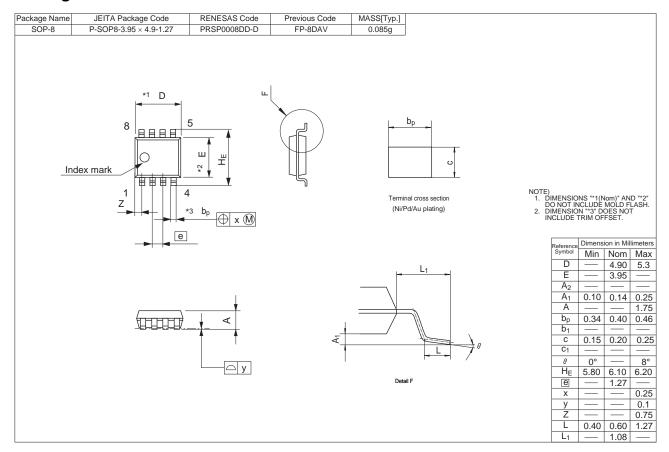








Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJE0607JSP-00-J0	2500 pcs/reel	Taping

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