

Description

This document details the custom configuration that is programmed into the one-time-programmable (OTP) memory of the 9FGV1006CQ505LTGI. Please refer to the device datasheet for further information about the device.

General Configuration

| Parameter | Value | Units |
|--|--------------|-------|
| Device I ² C Address | Primary 0xD0 | — |
| Default Configuration at I ² C Mode | 0 | — |

Frequency Overview

| Parameter | Configuration 0 | Configuration 1 | Configuration 2 | Configuration 3 | Units |
|-----------|-----------------|-----------------|-----------------|-----------------|-------|
| Input | 50 | 50 | 50 | 50 | MHz |
| REF 0 | — | — | — | — | — |
| Output 0 | 100 | 100 | 100 | 100 | MHz |
| Output 1 | 100 | 100 | 100 | 100 | MHz |

Configuration 0 Parameters: SEL[1:0] = 00

Input frequency: 50MHz; crystal load capacitance: 7.97pF

| Parameter | REF 0 | Output 0 | Output 1 | Units |
|----------------------------|---------|----------|----------|-------|
| Frequency | 50 | 100 | 100 | MHz |
| Default Output Status | Off | On | On | — |
| VDDO Voltage | Low | 3.3 | 3.3 | V |
| Output Type | LVC MOS | LP-HCSL | LP-HCSL | — |
| Spread Spectrum | — | — | — | % |
| Spread Spectrum Modulation | — | — | — | kHz |
| LP-HCSL Impedance | — | 100 | 100 | Ω |

Configuration 1 Parameters: SEL[1:0] = 01

Input frequency: 50MHz; crystal load capacitance: 7.97pF

| Parameter | REF 0 | Output 0 | Output 1 | Units |
|----------------------------|---------|----------|----------|-------|
| Frequency | 50 | 100 | 100 | MHz |
| Default Output Status | Off | On | On | — |
| VDDO Voltage | Low | 3.3 | 3.3 | V |
| Output Type | LVC MOS | LP-HCSL | LP-HCSL | — |
| Spread Spectrum | — | -0.1 | -0.1 | % |
| Spread Spectrum Modulation | — | 31.5 | 31.5 | kHz |
| LP-HCSL Impedance | — | 100 | 100 | Ω |

Configuration 2 Parameters: SEL[1:0] = 10

Input frequency: 50MHz; crystal load capacitance: 7.97pF

| Parameter | REF 0 | Output 0 | Output 1 | Units |
|----------------------------|---------|----------|----------|-------|
| Frequency | 50 | 100 | 100 | MHz |
| Default Output Status | Off | On | On | — |
| VDDO Voltage | Low | 3.3 | 3.3 | V |
| Output Type | LVC MOS | LP-HCSL | LP-HCSL | — |
| Spread Spectrum | — | -0.3 | -0.3 | % |
| Spread Spectrum Modulation | — | 31.5 | 31.5 | kHz |
| LP-HCSL Impedance | — | 100 | 100 | Ω |

Configuration 3 Parameters: SEL[1:0] = 11

Input frequency: 50MHz; crystal load capacitance: 7.97pF

| Parameter | REF 0 | Output 0 | Output 1 | Units |
|----------------------------|---------|----------|----------|-------|
| Frequency | 50 | 100 | 100 | MHz |
| Default Output Status | Off | On | On | — |
| VDDO Voltage | Low | 3.3 | 3.3 | V |
| Output Type | LVC MOS | LP-HCSL | LP-HCSL | — |
| Spread Spectrum | — | -0.5 | -0.5 | % |
| Spread Spectrum Modulation | — | 31.5 | 31.5 | kHz |
| LP-HCSL Impedance | — | 100 | 100 | Ω |

Package Outline Drawings and Marking Diagrams

Refer to the [datasheet](#) for detailed information pertaining to package outline drawings and marking diagrams.

Ordering Information

| Orderable Part Number | Carrier Type | Temperature | Crystal |
|-----------------------|---------------|---------------|----------------|
| 9FGV1006CQ505LTGI | Tray | -40° to +85°C | 50MHz Internal |
| 9FGV1006CQ505LTGI8 | Tape and Reel | -40° to +85°C | 50MHz Internal |

Revision History

| Revision Date | Description of Change |
|--------------------|-----------------------|
| September 17, 2020 | Initial release. |

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