

To our customers,

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## Old Company Name in Catalogs and Other Documents

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## 32185/32186 Group, 32192/32195/32196 Group

### SFR definition file

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#### 1. Overview

This documentation presents SFR definition file for 32185/32186 Group, 32192/32195/32196 Group.

#### 2. Introduction

The sample task described in this document uses the following microcomputers, under the respective conditions.

- Microcomputer: 32185 Group (M32185F4VFP)  
32186 Group (M32186F8VFP)  
32192 Group (M32192F8VFP, M32192F8UFP, M32192F8TFP)  
32195 Group (M32195F4VFP, M32195F4UFP, M32195F4TFP)  
32196 Group (M32196F8VFP, M32196F8UFP, M32196F8TFP)
- Operating frequency: 32185/32186 Group: 80 MHz  
32192/32195/32196 Group: 128 to 160 MHz

### 3. Explanation of SFR definition file

3 parts written below compose SFR definition file.

#### (1) #pragma ADDRESS definition part

It is declared that designated variable is arranged to designated address with using #pragma external function of M3T-CC32R (Compiler).

Defined variable name in register name of SFR definition in Hardware manual.

Description example)

```
#pragma ADDRESS P10DATA 0x0080070A
```

Explanation)

Arrange variable P10DATA to absolute address 0x0080070A.

#### (2) Variable size definition part

Size of variable is defined here.

Description example)

```
#define VUB volatile unsigned char
static VUB P10DATA
```

Explanation)

Arrange variable P10DATA as byte size.

Note: VUB model is re-defined bite size of volatile unsigned char model by typedef.

There are others half word size of VUH (volatile unsigned short) and word size of VUW (volatile unsigned long).

#### (3) Bit symbol definition part

Defined applicable bit in register as "1", others as "0".

Defined bit name as bit name in register table in Hardware manual.

Description example)

```
#define b7_8B (0x01u)
#define P107DT (b7_8B)
```

Explanation)

Define P107DT as 0x01.

Note: b7\_8B is 8 bit size data defined by #define in the same definition file, the data set only position of bit7as "1" (0x01). There are others bn\_8B (n=0 to 7 or 8 to 15) bn\_16B (n=0 to 15), bn\_32B (n=0 to 31). Each size is 8 bit, 16bit, 32 bit.

Note: The latest SFR definition file can be downloaded from Renesas Technology Corp. website.

#### 4. How to use SFR definition file

SFR definition file uses M3T-CC32R (compiler) of #include pre-processing, and is used with inserting file to C source program.

Example when use Bit symbol defined in SFR definition file in C source program.

Description example)

```
#include    "..\inc\sfr32176_pragma.h"
:
(Omitted)
:
P10DATA |= P107DT;          /* Set P107DT bit as "1"*/
```

Note: Bit symbol definition is defined by maximum size in register table in Hardware Manual. So bit symbol definition cannot be used in accessible register in different size except maximum size.

Example)

```
#define b0_32B    (0x80000000u)
#define IRB0      (b0_32B)
```

CAN0SLIMKW = IRB0; /\* Set IRB0 bit as "1" \*/

CAN0SLIMK = IRB0; /\* Because of size difference, use prohibited \*/

## 5. Reference of documents

- 32192/32196 Group Hardware Manual (Rev.1.01)
- 32186 Group Hardware Manual (Rev.1.00)
- 32185 Group Datasheet (Rev.1.00)
- 32195 Group Datasheet (Rev.1.00)
- M3T-CC32R Version 5.00 User's Manual (Compiler)
- M3T-AS32R Version 5.00 User's Manual (Assembler)
- M32R-FPU Software Manual (Rev.1.01)

(Please get the latest one from Renesas Technology website.)

## 6. Website and Support Center

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- Customer Support Center for all Products and Technical Support Center for M32R Family  
Customer Support Center: [csc@renesas.com](mailto:csc@renesas.com)

**Revision Record**

Rev.	Date	Description	
		Page	Summary
1.00	Feb.22.06	—	First edition issued.

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