

## RX671 Group

Renesas Starter Kit Sample Code (e<sup>2</sup> studio for CC-RX)

#### Introduction

Renesas Starter Kits (RSK+) are supplied as complete development systems for the selected microcontroller. The kit includes an evaluation board, portable On-Chip Debugger, and a set of peripheral sample code.

### **Target Device**

RX671 Group

#### **Development Environment**

IDE: e<sup>2</sup> studio 2021-07 Compiler: Renesas CC-RX v3.03.00 Hardware: Renesas Starter Kit+ for RX671

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#### 1. Installation

This section assumes that e<sup>2</sup> studio and the Renesas RXC toolchain are already installed. Create a new folder, for example 'C:\Renesas\Workspace\RSK\RSK+RSK671'. Copy the application note zip package 'r20an0640ej0100-rskrx671.zip' downloaded from the website to this folder.

#### 2. Creating the Project Workspace

Open e<sup>2</sup> studio by clicking the Windows Start button, select All Programs > Renesas Electronics e2studio > e2 studio.

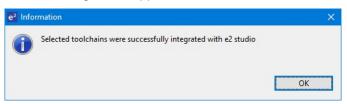
💽 e² studio Launcher	×
Select a directory as workspace	
e <sup>2</sup> studio uses the workspace directory to store its preferences and development artifacts.	
Workspace: C\Usersuser_name\e2_studio\workspace	
□ Use this as the default and do not ask again	
<u>R</u> ecent Workspaces	
Launch Cancel	

Select <Launch>.

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<b>oolchain Integration</b> <ol> <li>New toolchains available for integ</li> </ol>	ration
Toolchain Registry	
Toolchain Type ✓ ☑ Renesas CCRX	Installation Path
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<	>
Select all Deselect all	
Enable 'Toolchain integration' on	startup
	Register Cancel

In the 'Toolchain Registry' dialog select the 'Renesas CCRX' and 'Renesas CCRX – v3.03.00' checkboxes. Click <Register>.

The 'Information' dialog below appears. Click <OK>.





On the 'Welcome to e<sup>2</sup> studio' screen select the 'Hide' icon as shown below.

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*	Import existing projects Import existing e <sup>2</sup> studio projects from the filesystem or archive	P	Tutorials Go through tutorials	
# _	Import sample projects Download and import sample projects from Renesas website	2	Samples Try out the samples	
۰	Review IDE configuration settings Review the IDE's most fiercely contested preferences		What's New Find out what is new	
Ľ	Open an existing file Open a file from the filesystem	2	Quick Start Guides Quickly getting familiar with the tool	
				Always show Welcome at start

1. Click <Import projects...> in the Project Explorer window.

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There are no projects in your workspace. To add a project:							There is no active outline.	editor that pr	ovides an
Create a new Makefile project in a directory containing existing code									
Create a new C or C++ project									
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2. The 'Import - Select' dialog will now appear. Expand the "General" folder icon, and select "Existing Projects into Workspace", then click 'Next'.

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Select Create new projects from an archive file or directory.	Ľ	5
Select an import wizard:		
type filter text		
<ul> <li>✓ General</li> <li> <sup>®</sup> Archive File</li> <li><sup>®</sup> Existing Projects into Workspace</li> <li><sup>®</sup> File System</li> <li><sup>®</sup> Preferences</li> <li><sup>®</sup> Projects from Folder or Archive</li> <li><sup>®</sup> Rename &amp; Import Existing C/C++ Project into Workspace</li> <li><sup>®</sup> Renesas CCRX project conversion to Renesas GCC RX</li> <li><sup>®</sup> Renesas CS+ Project for CA78K0R/CA78K0</li> <li><sup>®</sup> Renesas CS+ Project for CC-RX and CC-RL</li> <li><sup>®</sup> Renesas GitHub FreeRTOS (with IoT libraries) Project</li> <li><sup>®</sup> Sample Projects on Renesas Website</li> <li>&gt; <sup>®</sup> C/C++</li> <li>&gt; <sup>®</sup> Code Generator</li> <li><sup>®</sup> Install</li> </ul>		~
(?) < <u>Back</u> <u>Next &gt;</u> <u>Finish</u>	Cance	ł

3. The 'Import - Import Projects' dialog will now appear. Select 'Select archive file', click the <Browse> button and locate the directory: 'C:\Renesas\Workspace\RSK\RSK+RX\r20an0640ej0100rskrx671.zip'. Click <Finish> to import the project.

Import						×	
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#### 3. Opening the Sample Code and Source Files

Once imported into e<sup>2</sup> studio select a project from the project list in the "Project Explorer". Click the arrow next to it to expand the folder contents.

description.txt in the 'doc' folder provides a functional description of the sample.

Clicking the arrow next to the 'src' folder will show the source files.

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> 😤 Timer PWM	36	- / -						Config_9				
> 12 Touch	37	<pre>#include "r_rsk_switch.</pre>						Config_9				
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_	42	#include "Config SCI10.h"							void) : uint16_t			
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> 🖻 ascii.c	44	<pre>#include "r_cg_userdefi</pre>	r_cg_userdefine.h"									
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> 🖻 main.c	47	Macro definitions.			+ <sup>S</sup> led_display_count(const uint8_t) :					uint8_t) : v		
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#### 4. Source Code Functionality

Each source code project is specifically written to run on the appropriate RSK. However, this source code can be useful as an example of peripheral initialization even without the RSK.

Each sample project will contain a C source file that includes "main" in the name, for example "main.c". This source file will include the C function main().



#### 5. Website and Support

Renesas Electronics Website

http://www.renesas.com/

Inquiries

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### **Revision History**

		Description	
Rev.	Date	Page	Summary
1.00	Aug.30.21	-	First edition issued



# General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

#### 1. Precaution against Electrostatic Discharge (ESD)

A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity. Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.

2. Processing at power-on

The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power is supplied until the power reaches the level at which resetting is specified.

3. Input of signal during power-off state

Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.

4. Handling of unused pins

Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.

5. Clock signals

After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.

6. Voltage application waveform at input pin

Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between  $V_{IL}$  (Max.) and  $V_{IH}$  (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between  $V_{IL}$  (Max.) and  $V_{IH}$  (Min.).

7. Prohibition of access to reserved addresses

Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.

8. Differences between products

Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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#### **Corporate Headquarters**

TOYOSU FORESIA, 3-2-24 Toyosu, Koto-ku, Tokyo 135-0061, Japan

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