
Replacement Guide for R8C Family to RL78 Family (CcnvNC30)

R01AN3508EC0100
Rev.1.00
Dec. 22, 2016

Introduction

This application note describes how to replace the programs for R8C with the programs for RL78.

Target Device

R8C Family

When using this application note with other Renesas MCUs, careful evaluation is recommended after making modifications to comply with the alternate MCU.

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1. How to Replace Programs from R8C Family to RL78 Family

This section explains how to replace the programs for R8C family with the programs for RL78 family.

First, use the C source converter CcnvNC30 to convert the extended functions for the C compiler NC30 to the extended functions for the C compiler CC-RL.

Next, use the integrated development environment CS+ or e2studio to create a project. Because the R8C family and the RL78 family have different peripheral functions, use the code generator for the RL78 family to generate programs for peripheral functions of the RL78 family instead of using the programs for peripheral functions of the R8C family.

Combine the programs converted with CcnvNC30 and the above programs for peripheral functions to replace programs.

2. Program Conversion Using CcnvNC30

2.1 About CcnvNC30

CcnvNC30 converts extended language specifications (such as macro names, reserved words, #pragma directives, and extended functions) in C source programs for NC30 into extended language specifications for CC-RL.

CcnvNC30 is the software that supports the porting of the programs for NC30 to the programs for CC-RL. Since we do not guarantee the correct operation of the programs converted by CcnvNC30, be sure to check the operation of the program after conversion.

In addition, the device-dependent codes such as location addresses, access to an SFR, and assembly-language codes cannot be converted. Convert these codes manually into the code for the RL78 family as required.

For details, see "CcnvNC30 C Source Converter User's Manual (R20UT3685E)".

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2.2 How to Use CcnvNC30

The method of converting a program with CcnvNC30 is shown below.

- (1) Place CcnvNC30 (CcnvNC30.exe) and a program for NC30 in the same folder of your choice.
- (2) Launch Command Prompt in Windows.
- (3) Change the current directory to the folder where CcnvNC30 is stored.

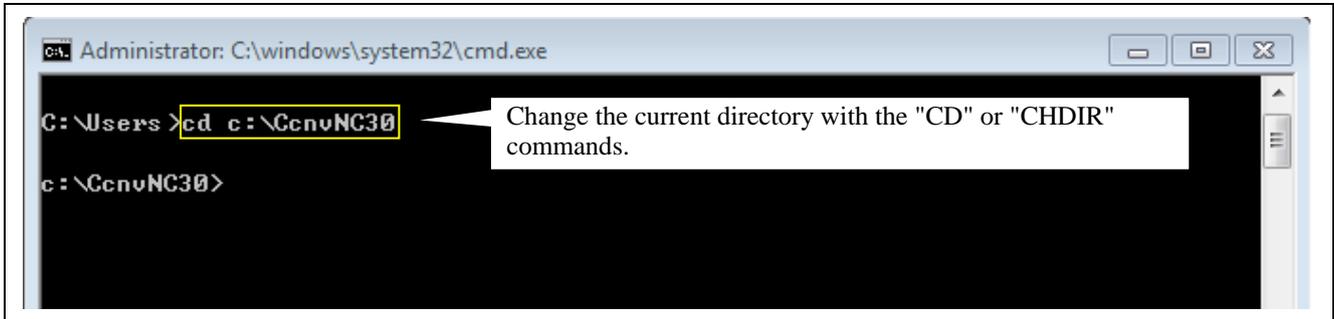


Figure 2.1 Command Prompt Window

- (4) Specify an output file name with the -o option before execution. After the execution, a program for CC-RL is output. In addition, when outputting messages in a specified file, use the -r option.

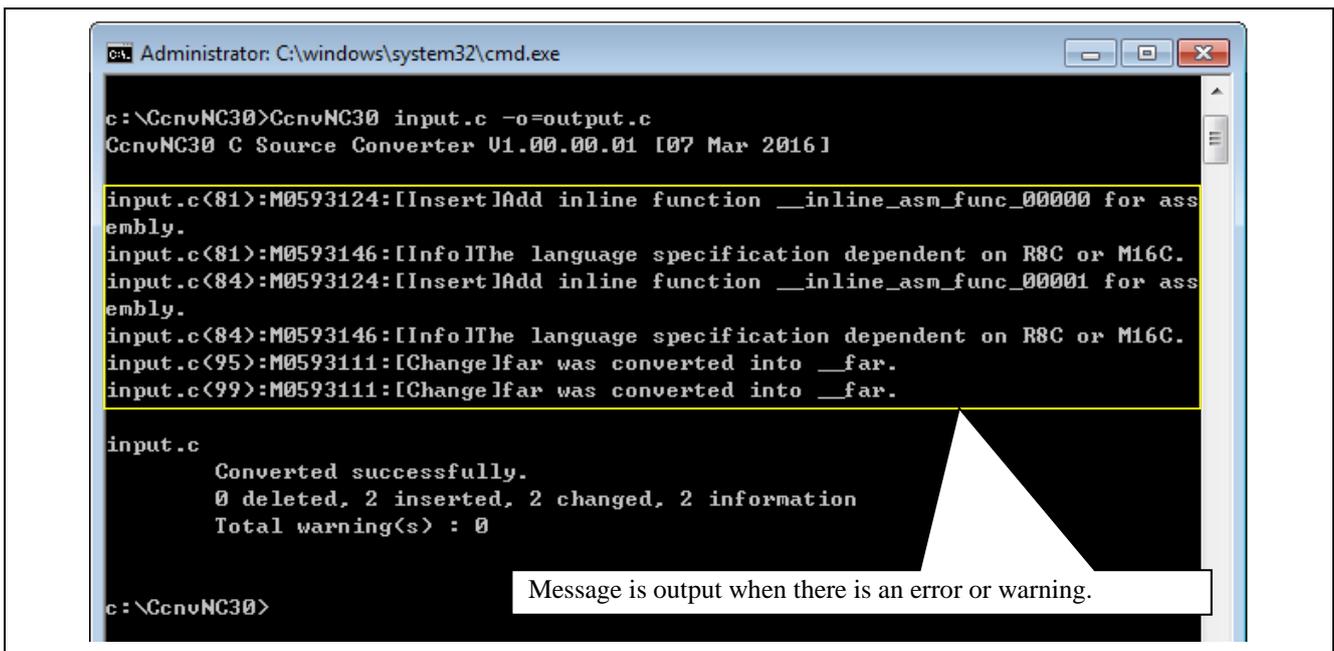


Figure 2.2 CcnvNC30 Execution Window

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- (5) When converting multiple files at the same time, create a list file and execute conversion with the -l option specified. After the execution, programs for CC-RL are output to the specified folder.

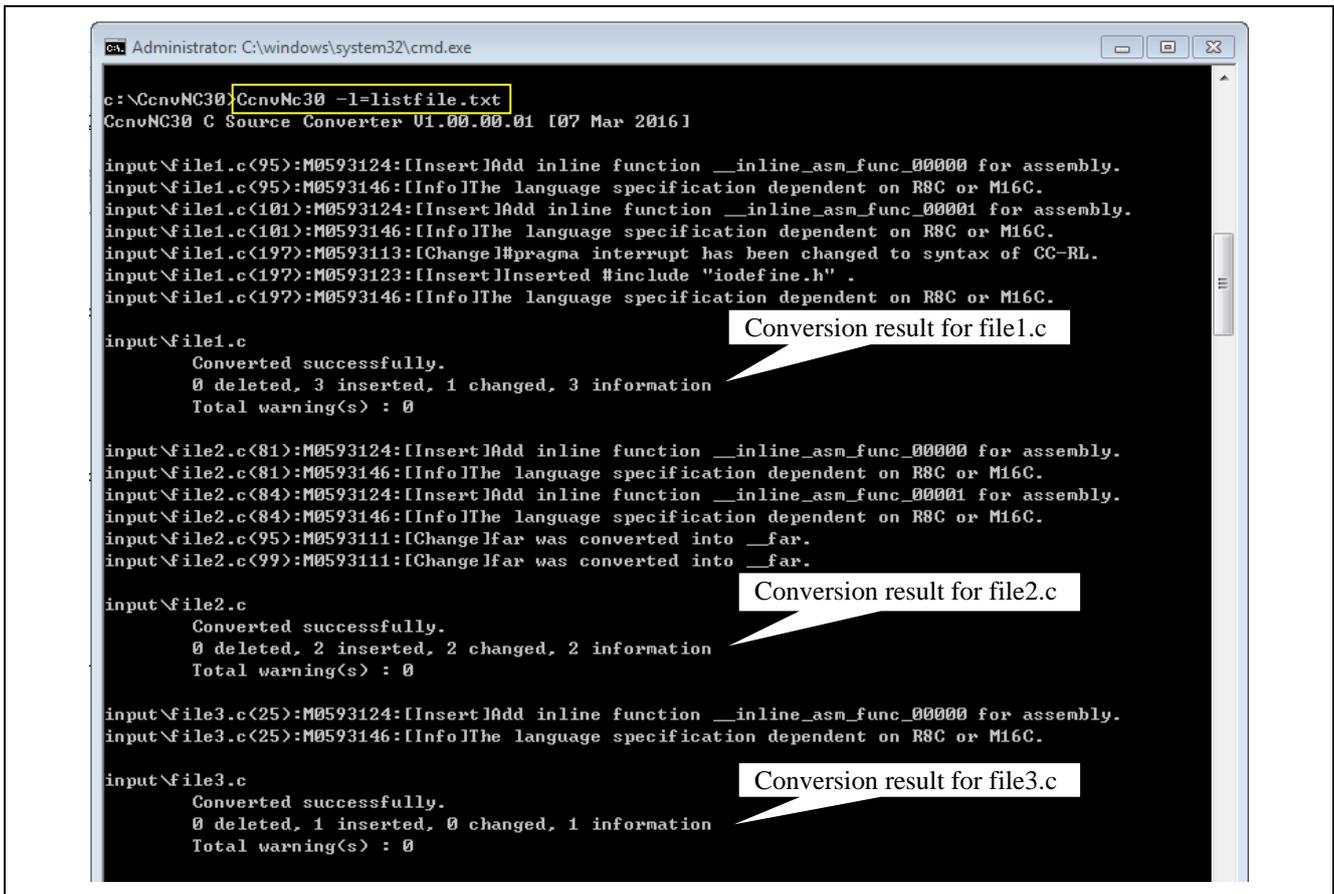


Figure 2.3 CcnvNC30 Execution Window (Multiple Files)

The example below shows the description in a list file.

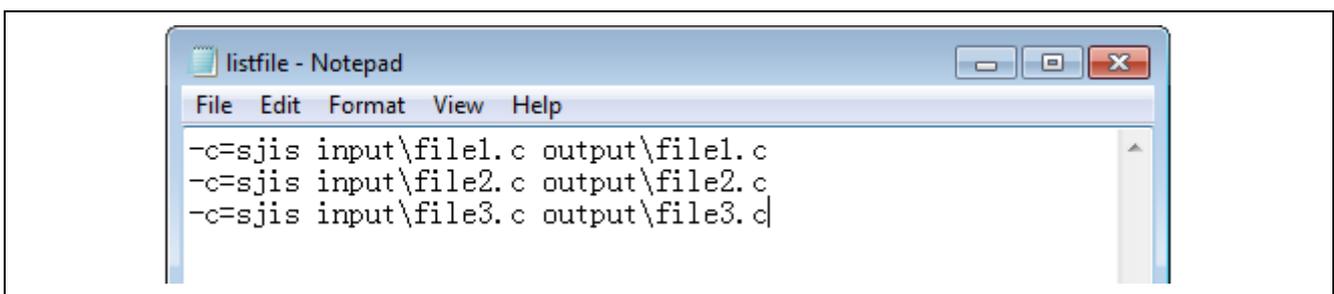


Figure 2.4 Example of Description in List File

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- (6) Correct the parts that are not converted by CcnvNC30. For the parts that require corrections, refer to "CONVERSION SPECIFICATIONS" of "CcnvNC30 C source converter User's Manual (R20UT3685E)".

2.3 When CcnvNC30 is Not Used

When CcnvNC30 is not used, extended functions of NC30 need to be converted manually into extended functions of CC-RL. For the extended language specifications supported by CC-RL, see "CC-RL Compiler User's Manual (R20UT3123E)".

3. Converting Programs for Peripheral Functions

3.1 Generating Programs Automatically

Programs are automatically generated for the RL78 family peripheral functions equivalent to the peripheral functions that were used by the R8C family by using the code generator for the RL78 family provided in the integrated development environment CS+ or e2studio. For how to use the code generator, see "CS+ Code Generator Integrated Development Environment User's Manual: Peripheral Function Operation [CS+ for CC][CS+ for CA,CX] (R20UT3104E)".

- (1) Under [Project Tree], click [Clock Generator] in [Code Generator (Design Tool)] and perform "pin assignment". When the pin assignment setting is decided once, it is not possible to be changed it later.

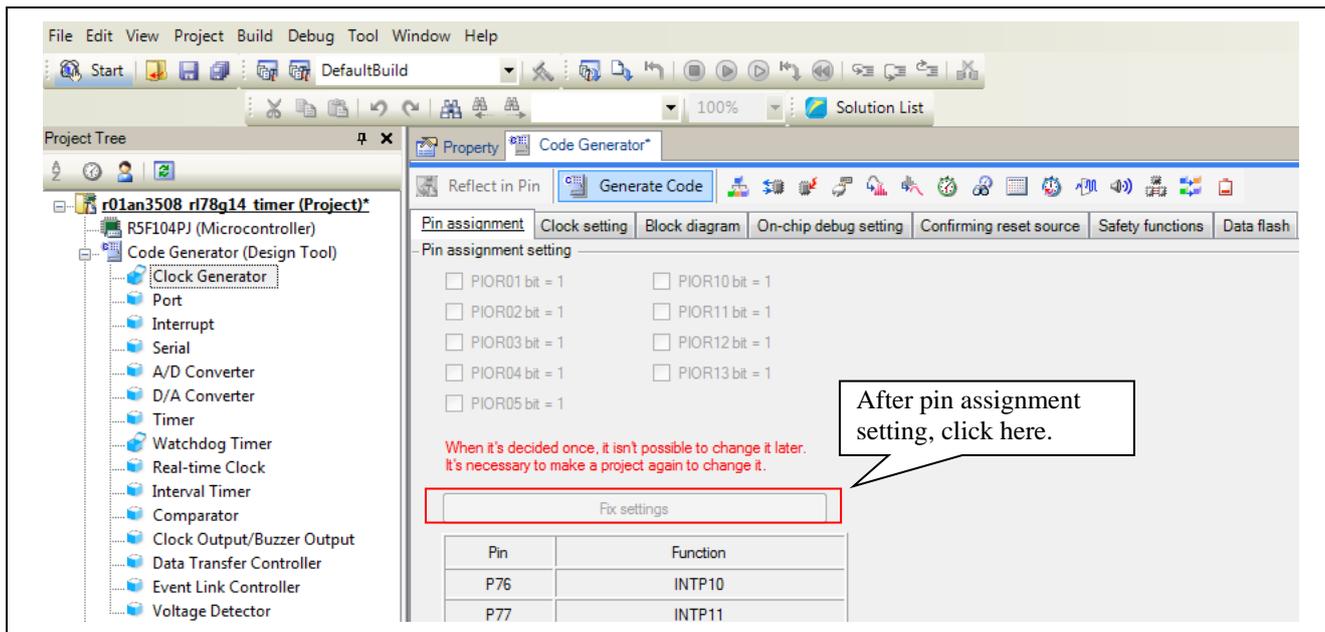


Figure 3.1 Code Generator Setting Window (1)

- (2) Refer to the program for the R8C family and set each function.

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- (3) On completion of all the peripheral function settings, click the [Generate Code] button at the top of the window to generate codes (automatic program generation). Use the automatically generated functions for peripheral functions to replace programs.

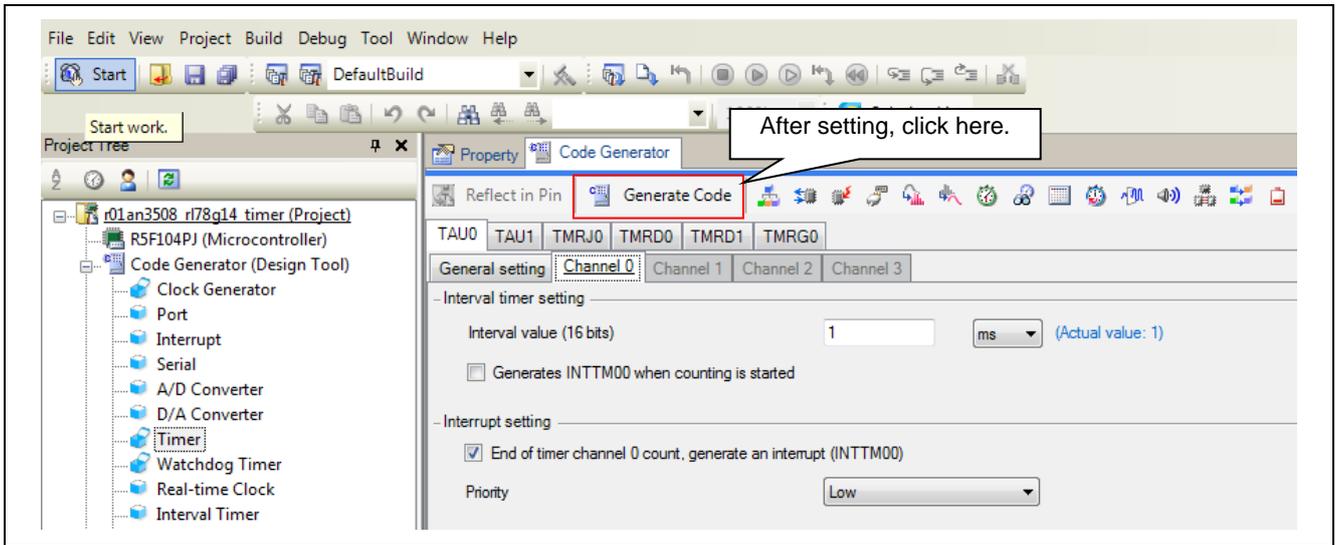


Figure 3.2 Code Generator Setting Window (2)

3.2 Adding Programs

Add the programs that cannot be automatically generated by the code generator (such as main function, interrupt function process, and variables).

Add a program between `/* Start user code for adding. Do not edit comment generated here */` and `/* End user code. Do not edit comment generated here */` in each file that was automatically generated. A program needs to be added manually. Note that any program added outside this range is automatically deleted during automatic generation of a program.

Be sure to confirm the operation of the system using the added programs.

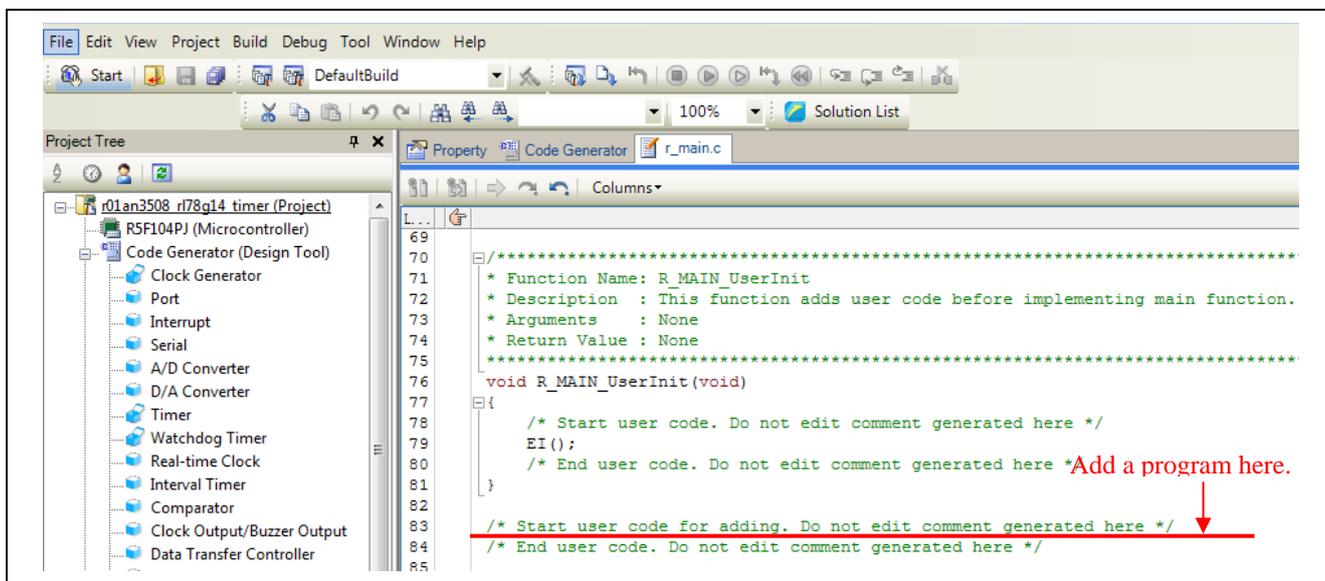


Figure 3.3 Adding Existing Program

3.3 When Code Generator is Not Used

When the code generator is not used, you need to create a new project first with the integrated development environment CS+ or e2studio and then manually create a program for a peripheral function. For details of peripheral functions, see the user's manual for the RL78 family.

4. Replacement Examples

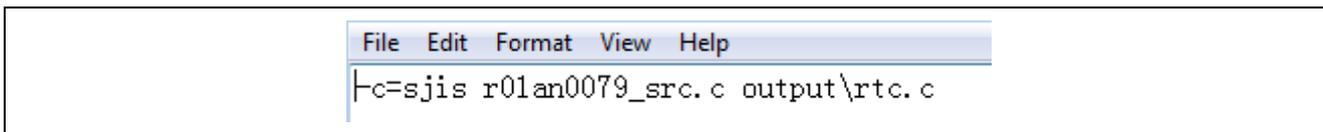
4.1 R8C Sample Program (Clock Operation Using RTC)

The program of "R8C/35A Group Clock Operation Using RTC (R01AN0079E)" is replaced with the program for RL78/G14. The project file after replacement is "r01an3508_rl78g14_rtc".

This program uses timer RE (Real-Time Clock Mode) to operate a clock. Use the 20 MHz XIN clock for the CPU clock. Use fC4 (XCIN clock (32.768 kHz) divided by 4) for the timer RE count source.

4.1.1 Porting Source to CC-RL with CcnvNC30

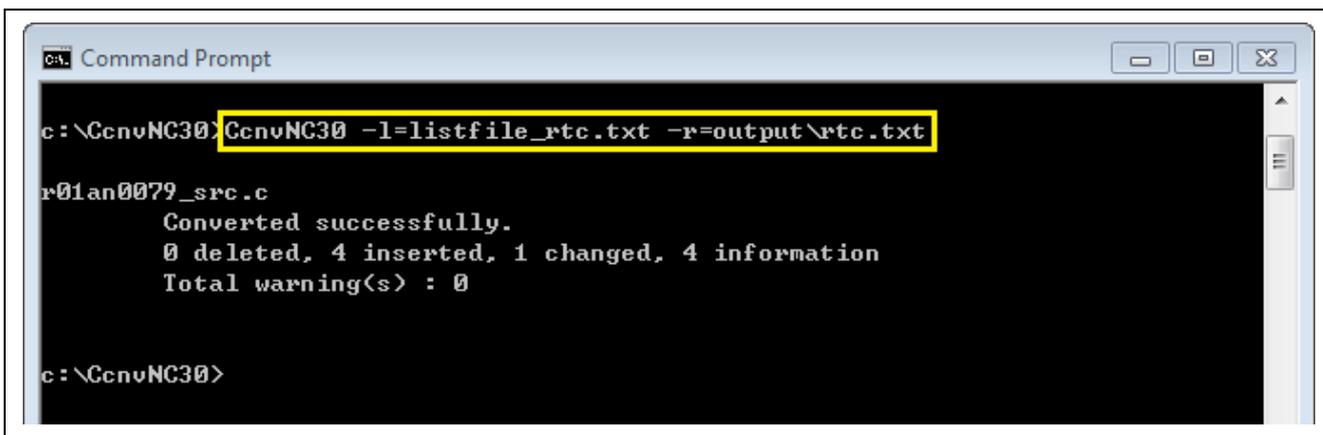
- (1) Create a list file to specify a C source file to be converted.



```
File Edit Format View Help
|c=sjis r01an0079_src.c output\rtc.c
```

Figure 4.1 Example of Description in List File (Clock Operation Using RTC)

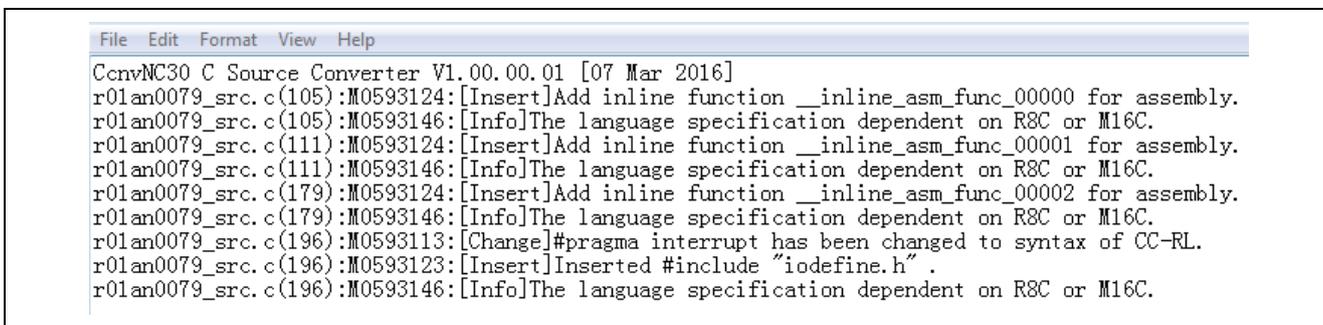
- (2) Launch Command Prompt to convert the C source file specified with the list file. In addition, the output conversion result file indicates changes.



```
CA: Command Prompt
c:\CcnvNC30>CcnvNC30 -l=listfile_rtc.txt -r=output\rtc.txt
r01an0079_src.c
Converted successfully.
0 deleted, 4 inserted, 1 changed, 4 information
Total warning(s) : 0
c:\CcnvNC30>
```

Figure 4.2 CcnvNC30 Execution Window (Clock Operation Using RTC)

The conversion result file indicates the conversion result as shown below. For details of the conversion result, see "CcnvNC30 C source converter User's Manual (R20UT3685E)".



```
File Edit Format View Help
CcnvNC30 C Source Converter V1.00.00.01 [07 Mar 2016]
r01an0079_src.c(105):M0593124:[Insert]Add inline function __inline_asm_func_00000 for assembly.
r01an0079_src.c(105):M0593146:[Info]The language specification dependent on R8C or M16C.
r01an0079_src.c(111):M0593124:[Insert]Add inline function __inline_asm_func_00001 for assembly.
r01an0079_src.c(111):M0593146:[Info]The language specification dependent on R8C or M16C.
r01an0079_src.c(179):M0593124:[Insert]Add inline function __inline_asm_func_00002 for assembly.
r01an0079_src.c(179):M0593146:[Info]The language specification dependent on R8C or M16C.
r01an0079_src.c(196):M0593113:[Change]#pragma interrupt has been changed to syntax of CC-RL.
r01an0079_src.c(196):M0593123:[Insert]Inserted #include "iodefine.h".
r01an0079_src.c(196):M0593146:[Info]The language specification dependent on R8C or M16C.
```

Figure 4.3 Details of Conversion Result (Clock Operation Using RTC)

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(3) Correct the converted C source file.

There may be redundant interrupt function declarations. As CC-RL produces an error in this case, delete the converted #pragma directive.

```
240 //[[CcnvNC30] #pragma interrupt/B _timer_re(vect=10)
241 #pragma interrupt _timer_re(vect=10, bank=RB1) ← Delete the #pragma directive.
242 void _timer_re(void)
243 {
244     sec = trespsec & 0x7f; /* Set second to RAM */
245     min = trespmin & 0x7f; /* Set minute to RAM */
246     hr = trespshr & 0x3f; /* Set hour to RAM */
247     wk_old = wk; /* Set last-time value */
248     wk = trespwk & 0x07; /* Set day to RAM */
249     up_flg = UPDATE; /* Set update flag */
250 }
```

Figure 4.4 Changing Interrupt Function Declaration

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4.1.2 Generating Programs Automatically

- (1) Create a new project with the integrated development environment CS+ or e2studio.
- (2) Set each function with the code generator.

Set the CPU clock to 16MHz high-speed OCO clock.

Set RTC and interval timer operation clock is 32.768kHz.

The screenshot shows the 'Code Generator' settings window for clock configuration. The window is divided into several sections, each with a title and a set of options and input fields. The following table summarizes the settings shown in the image:

Section	Setting	Value	Unit
Main system clock (fMAIN) setting	High-speed OCO (fIH)	<input checked="" type="radio"/>	
	High-speed system clock (fMX)	<input type="radio"/>	
High-speed OCO clock setting	Operation	<input checked="" type="checkbox"/>	
	Frequency	16 (fHOCO=16, fIH=16)	(MHz)
High-speed system clock setting	Operation	<input type="checkbox"/>	
	X1 oscillation (fX)	<input checked="" type="radio"/>	
	External clock input (fEX)	<input type="radio"/>	
	Frequency	20	(MHz)
Stable time	13107.2 (2 ¹⁸ /fX)	(µs)	
Subsystem clock (fSUB) setting	Operation	<input checked="" type="checkbox"/>	
	XT1 oscillation (fXT)	<input checked="" type="radio"/>	
	External subclock input (fEXS)	<input type="radio"/>	
	Frequency	32.768	(kHz)
XT1 oscillator oscillation mode setting	Low power consumption		
Subsystem clock in STOP, HALT mode setting	Enables supply		
Internal low-speed oscillation clock (fIL) setting	Frequency	15	(kHz)
RTC and interval timer operation clock setting	RTC and interval timer operation clock	32.768 (fSUB)	(kHz)
CPU and peripheral clock setting	CPU and peripheral clock (fCLK)	16000 (fIH)	(kHz)

Figure 4.5 Setting Window in Code Generator (Clock)

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Set Real-time Clock (RTC) which is the equivalent function to Timer RE of the R8C family.

The screenshot shows the 'Setting Window in Code Generator (Real-time Clock)'. The interface includes a toolbar at the top with icons for 'Reflect in Pin' and 'Generate Code'. The main settings are organized into four sections:

- Real-time clock operation setting:** Radio buttons for 'Unused' and 'Used'. The 'Used' option is selected and highlighted with a red box.
- Real-time clock setting:**
 - 'Hour-system selection' is set to '24-hour' (highlighted with a red box).
 - 'Set real-time clock initial value' is checked (highlighted with a red box), with the value '09-01-01 00:00:00 (Thursday)' displayed.
 - 'Enable output of RTC1HZ pin (1 Hz)' is unchecked.
- Alarm detection function setting:**
 - 'Use alarm detection function' is unchecked.
 - 'Set alarm initial value' is unchecked.
 - 'Week day' selection includes checkboxes for Sunday, Monday, Tuesday, Wednesday, Thursday, Friday, and Saturday.
 - 'Hour:Minute' is set to '00:00'.
- Interrupt setting:**
 - 'Used as constant-period interrupt function (INTRTC)' is checked (highlighted with a red box), with a period of 'Once per 1 s' (highlighted with a red box).
 - 'Used as alarm interrupt function (INTRTC)' is checked.
 - 'Priority' is set to 'Level 1' (highlighted with a red box).

Figure 4.6 Setting Window in Code Generator (Real-time Clock)

- (3) Set ports, watchdog timer, voltage detection circuit, etc. based on your environment.
- (4) Click [Generate Code] to generate a file.

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4.1.3 Adding Programs

Add the processes of symbol definition and the main function to the program with generated code. Use the programs with generated code for other programs (such as clock setting and RTC function setting).

- Symbol definition

Add symbol definition to r_cg_rtc.h.

Program for R8C

```
94  /*****  
95  /* DEFINE  
96  /*****  
97  #define NO_UPDATE      0          /* No update */  
98  #define UPDATE        1          /* Update */  
99  #define COMM          0          /* Common year */  
100 #define LEAP          1          /* Leap year */  
101 #define DEC           0x12       /* December */  
102 #define WEEK          0x04       /* A day of the week(thursday) */
```

Add contents corresponding to
the red box manually.

r_cg_rtc.h file for RL78/G14

```
146 /* Start user code for function. Do not edit comment generated here */  
147 #define NO_UPDATE      0          /* No update */  
148 #define UPDATE        1          /* Update */  
149 #define COMM          0          /* Common year */  
150 #define LEAP          1          /* Leap year */  
151 #define DEC           0x12       /* December */
```

Figure 4.7 Replacement of Symbol Definition

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- main function

When the code generator for RL78/G14 is used, the R_Systeminit function is executed before the main function is executed. The R_Systeminit function performs the initial setting of the clock and RTC. Thus, only the process indicated in the red box is added manually. The R_RTC_Start function starts the operation of RTC.

In R8C/35A, customers can use Timer RE for real-time clock. In RL78/G14, customer can use RTC for real-time clock. In the main function of "R8C/35A Group Clock Operation Using RTC (R01AN0079E)", the initialization function and the start command of Timer RE are executed. So in main function of "r01an3508_rl78g14_rtc", the initialization function (RTC generate the interrupt per one second) and the start function of RTC are executed.

Program for R8C

```
103 void main(void)
104 {
105     asm("FCLR I"); /* Interrupt disabled */
106
107     mcu_init(); /* MCU initialize */
108
109     timer_re_init(); /* TimerRE initialize */
110
111     asm("FSET I"); /* Interrupt enabled */
112
113     leap_flg = leap_chk(); /* Leap year check */
114
115     tstart_trecr1 = 1; /* TRE1 count start */
116     while(tcstf_trecr1== 0); /* If TRE1 count start? else wait */
117
118     while(1){
119         if(up_flg == UPDATE){
120             update(); /* Update processing */
121         }
122     }
123 }
```

Add contents corresponding to the red box manually.

r_main.c file for RL78/G14

```
61 void main(void)
62 {
63     R_MAIN_UserInit();
64     /* Start user code. Do not edit comment generated here */
65     leap_flg = leap_chk(); /* Leap year check */
66
67     R_RTC_Set_ConstPeriodInterruptOn(SEC); /* Interrupt turned on */
68     R_RTC_Start(); /* Enabled */
69
70     while (1U)
71     {
72         if(up_flg == UPDATE){
73             update(); /* Update processing */
74         }
75     }
76     /* End user code. Do not edit comment generated here */
77 }
```

Figure 4.8 Replacement of main Function

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Additionally, "update" and "leap_chk" functions, which are used by "main" function, should be added into r_main.c file. And the variables that are used by these two functions are also needs to be added in r_main.c file.

Variables for R8C

```
63  /******  
64  /* RAM  
65  /******  
66  unsigned short year = 0x2009;          /* Year */  
67  unsigned char month = 0x01;           /* Month */  
68  unsigned char day = 0x01;             /* Day */  
69  unsigned char wk = WEEK;              /* Sun,Mon,Tue,Wed,Thu,Fri,Sat */  
70  unsigned char hr = 0x00;              /* Hour */  
71  unsigned char min = 0x00;             /* Minute */  
72  unsigned char sec = 0x00;             /* Second */  
73  
74  /* Work area */  
75  unsigned char wk_old = 0x00;          /* Last-time value of 'wk' */  
76  
77  /* Flags */  
78  unsigned char up_flg = NO_UPDATE;     /* Update flag */  
79  unsigned char leap_flg = COMM;        /* Leap flag */  
80  
81  /******  
82  /* ROM  
83  /******  
84  unsigned char const MONTH_DAYS[13] = {          /* This table contains the number of days in different months */  
85  0x00,0x31,0x28,0x31,0x30,0x31,0x30,0x31,0x31,0x30,0x31,0x30,0x31  
86  };  
87  
88  unsigned char const MONTH_DAYS_L[13] = {        /* This table contains the number of days in different months (leap year) */  
89  0x00,0x31,0x29,0x31,0x30,0x31,0x30,0x31,0x31,0x30,0x31,0x30,0x31  
90  };
```

Add contents corresponding to the red box manually.

r_main.c file for RL78/G14

```
46  /******  
47  Global variables and functions  
48  /******  
49  /* Start user code for global. Do not edit comment generated here */  
50  unsigned short year = 0x2009;          /* Year */  
51  unsigned char month = 0x01;           /* Month */  
52  unsigned char day = 0x01;             /* Day */  
53  unsigned char wk = 0x04;              /* Sun,Mon,Tue,Wed,Thu,Fri,Sat */  
54  unsigned char hr = 0x00;              /* Hour */  
55  unsigned char min = 0x00;             /* Minute */  
56  unsigned char sec = 0x00;             /* Second */  
57  
58  unsigned char wk_old = 0x00;          /* Last-time value of 'wk' */  
59  
60  unsigned char up_flg = NO_UPDATE;     /* Update flag */  
61  unsigned char leap_flg = COMM;        /* Leap flag */  
62  
63  unsigned char const MONTH_DAYS[13] = {          /* This table contains the number of days in different months */  
64  0x00,0x31,0x28,0x31,0x30,0x31,0x30,0x31,0x31,0x30,0x31,0x30,0x31  
65  };  
66  
67  unsigned char const MONTH_DAYS_L[13] = {        /* This table contains the number of days in different months (leap year) */  
68  0x00,0x31,0x29,0x31,0x30,0x31,0x30,0x31,0x31,0x30,0x31,0x30,0x31  
69  };
```

Figure 4.9 Add Variables

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Program for R8C

```
207  /*"FUNC COMMENT"*****
208  * Outline           : Update processing
209  * Declaration      : void update(void)
210  * Description      : Update check
211  * Argument         : none
212  * Return Value     : none
213  /*"FUNC COMMENT END"*****/
214  void update(void)
215  {
216      unsigned char  num = 0;
217
218      up_flg = NO_UPDATE;           /* Update flag clear */
219
220      if(wk != wk_old){           /* Change day ? */
258  }
259
260  /*"FUNC COMMENT"*****
261  * Outline           : Leap year check processing
262  * Declaration      : unsigned char leap_chk(void)
263  * Description      : Leap year check
264  * Argument         : none
265  * Return Value     : none
266  /*"FUNC COMMENT END"*****/
267  unsigned char leap_chk(void)
268  {
269      unsigned char  chk = 0;           /* Result of check */
270      unsigned short dec = 0;
271      unsigned short work = 0;
272
273      work = year;                 /* Copy year */
274
275      dec = (((work & 0xF000) >> 12) * 1000); /* Conversion to decimal */
276      dec += (((work & 0x0F00) >> 8) * 100);
277      dec += (((work & 0x00F0) >> 4) * 10);
278      dec += (((work & 0x000F) >> 0) * 1);
279
280      chk = COMM;                 /* Set Common year */
281
282      if((dec % 4) == 0){
288          return(chk);
289  }
```

Add contents corresponding to the red box manually.

r_main.c file for RL78/G14

```
113  /* Start user code for adding. Do not edit comment generated here */
114  /******
115  * Function Name: update
116  * Description  : Update check
117  * Arguments   : None
118  * Return Value: None
119  /******/
120  void update(void)
121  {
165  }
166  /******
167  * Function Name: leap_chk
168  * Description  : Leap year check
169  * Arguments   : None
170  * Return Value: None
171  /******/
172  unsigned char leap_chk(void)
173  {
195  /* End user code. Do not edit comment generated here */
```

Figure 4.10 Add Functions

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4.1.4 Other Items to be Corrected

If RTC is set with the code generator, interrupt processes are automatically generated. Add the user program to interrupt routine.

Program for R8C

```
189  /*"FUNC COMMENT"*****
190  * Outline           : Timer RE interrupt processing
191  * Declaration      : void _timer_re(void)
192  * Description      : Timer RE interrupt
193  * Argument         : none
194  * Return Value     : none
195  /*"FUNC COMMENT END"*****/
196  #pragma interrupt/B _timer_re(vect=10)
197  void _timer_re(void)
198  {
199      sec = trespsec & 0x7f;           /* Set second to RAM */
200      min = tremin & 0x7f;           /* Set minute to RAM */
201      hr = trehr & 0x3f;             /* Set hour to RAM */
202      wk_old = wk;                  /* Set last-time value */
203      wk = trewk & 0x07;            /* Set day to RAM */
204      up_flg = UPDATE;              /* Set update flag */
205  }
```

Add contents corresponding to the red box manually.

r_cg_rtc_user.c file for RL78/G14

```
62  /*"*****
63  * Function Name: r_rtc_interrupt
64  * Description  : This function is INTRTC interrupt service routine.
65  * Arguments   : None
66  * Return Value : None
67  /*"*****/
68  static void __near r_rtc_interrupt(void)
69  {
70      if (1U == RIFG)
71      {
72          RTCC1 &= (uint8_t)~_08_RTC_INTG_GENERATE_FLAG; /* clear RIFG */
73          r_rtc_callback_constperiod();
74      }
75  }
76
77  /*"*****
78  * Function Name: r_rtc_callback_constperiod
79  * Description   : This function is real-time clock constant-period interrupt service handler.
80  * Arguments    : None
81  * Return Value : None
82  /*"*****/
83  static void r_rtc_callback_constperiod(void)
84  {
85      /* Start user code. Do not edit comment generated here */
86      sec = SEC & 0x7F;           /* Set second to RAM */
87      min = MIN & 0x7F;           /* Set minute to RAM */
88      hr = HOUR & 0x3F;          /* Set hour to RAM */
89      wk_old = wk;               /* Set last-time value */
90      wk = WEEK & 0x07;          /* Set day to RAM */
91      up_flg = UPDATE;           /* Set update flag */
92      /* End user code. Do not edit comment generated here */
93  }
```

Figure 4.11 Add Interrupt Program

Additionally, these variables need external declaration in r_cg_rtc_user.c file.

```
48  /* Start user code for global. Do not edit comment generated here */
49  extern unsigned short year;      /* Year */
50  extern unsigned char month;     /* Month */
51  extern unsigned char day;       /* Day */
52  extern unsigned char wk;        /* Sun,Mon,Tue,Wed,Thu,Fri,Sat */
53  extern unsigned char hr;        /* Hour */
54  extern unsigned char min;       /* Minute */
55  extern unsigned char sec;       /* Second */
56
57  extern unsigned char wk_old;     /* Last-time value of 'wk' */
58
59  extern unsigned char up_flg;
60  /* End user code. Do not edit comment generated here */
```

Figure 4.12 Add External Declaration

4.1.5 Sample Code After Replacement

Obtain the sample code "an-r01an3508ec0100-rl78-migrate.zip" from the Renesas Electronics Website. "rl78g14_migrate_rtc" in the "workspace" folder is the sample code that replaces the program of "R8C/35A Group Clock Operation Using RTC (R01AN0079E)".

Replacement Guide for R8C Family to RL78 Family (CcnvNC30)

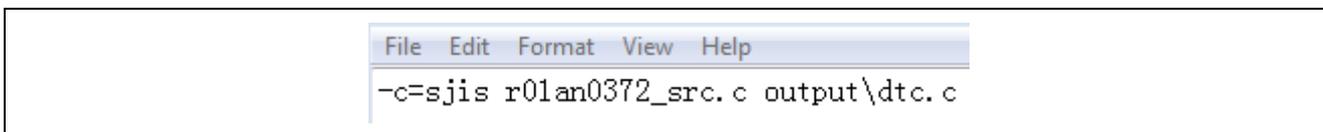
4.2 R8C Sample Program (DTC Operation)

The program of "R8C/35C Group DTC Operation in Chain Transfers (R01AN0372E)" is replaced with the program for RL78/G14. The project file after replacement is "r01an3508_rl78g14_dtc".

This program uses DTC operation in chain transfers.

4.2.1 Porting Source to CC-RL with CcnvNC30

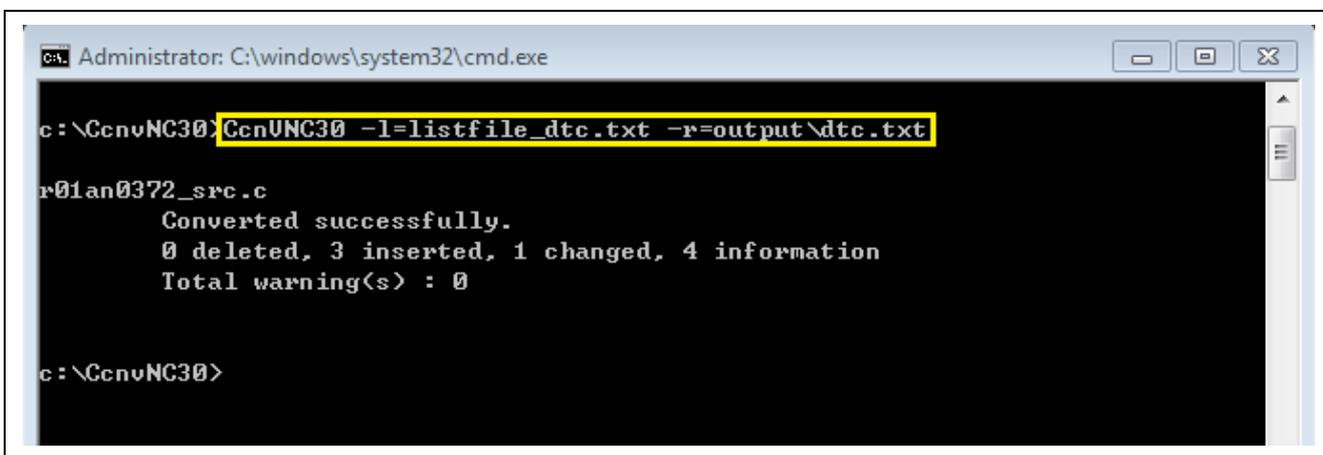
- (1) Create a list file to specify a C source file to be converted.



```
File Edit Format View Help
-c=sjis r01an0372_src.c output\dtc.c
```

Figure 4.13 Example of Description in List File (DTC Operation in Chain Transfers)

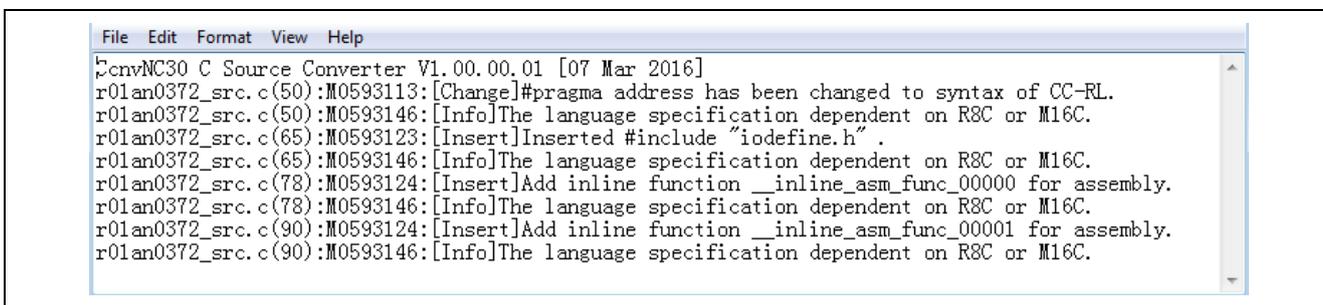
- (2) Launch Command Prompt to convert the C source file specified with the list file. In addition, the output conversion result file indicates changes.



```
Administrator: C:\windows\system32\cmd.exe
c:\CcnvNC30>CcnvNC30 -l=listfile_dtc.txt -r=output\dtc.txt
r01an0372_src.c
Converted successfully.
0 deleted, 3 inserted, 1 changed, 4 information
Total warning(s) : 0
c:\CcnvNC30>
```

Figure 4.14 CcnvNC30 Execution Window (DTC Operation in Chain Transfers)

The conversion result file indicates the conversion result as shown below. For details of the conversion result, see "CcnvNC30 C source converter User's Manual (R20UT3685E)".



```
File Edit Format View Help
CcnvNC30 C Source Converter V1.00.00.01 [07 Mar 2016]
r01an0372_src.c(50):M0593113:[Change]#pragma address has been changed to syntax of CC-RL.
r01an0372_src.c(50):M0593146:[Info]The language specification dependent on R8C or M16C.
r01an0372_src.c(65):M0593123:[Insert]Inserted #include "iodefine.h".
r01an0372_src.c(65):M0593146:[Info]The language specification dependent on R8C or M16C.
r01an0372_src.c(78):M0593124:[Insert]Add inline function __inline_asm_func_00000 for assembly.
r01an0372_src.c(78):M0593146:[Info]The language specification dependent on R8C or M16C.
r01an0372_src.c(90):M0593124:[Insert]Add inline function __inline_asm_func_00001 for assembly.
r01an0372_src.c(90):M0593146:[Info]The language specification dependent on R8C or M16C.
```

Figure 4.15 Details of Conversion Result (DTC Operation in Chain Transfers)

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(3) Correct the converted C source file.

There may be redundant interrupt function declarations. As CC-RL produces an error in this case, delete the converted #pragma directive.

```
86 // [CcnvNC30] #pragma ADDRESS ad value 00600H /* Destination address of A/D conversion data */  
87 #pragma address ad value=0x00600 ← Delete the #pragma directive.
```

Figure 4.16 Changing Interrupt Function Declaration

4.2.2 Generating Programs Automatically

- (1) Create a new project with the integrated development environment CS+ or e2studio.
- (2) Set each function with the code generator.
Set the CPU clock to 16MHz high-speed OCO clock.

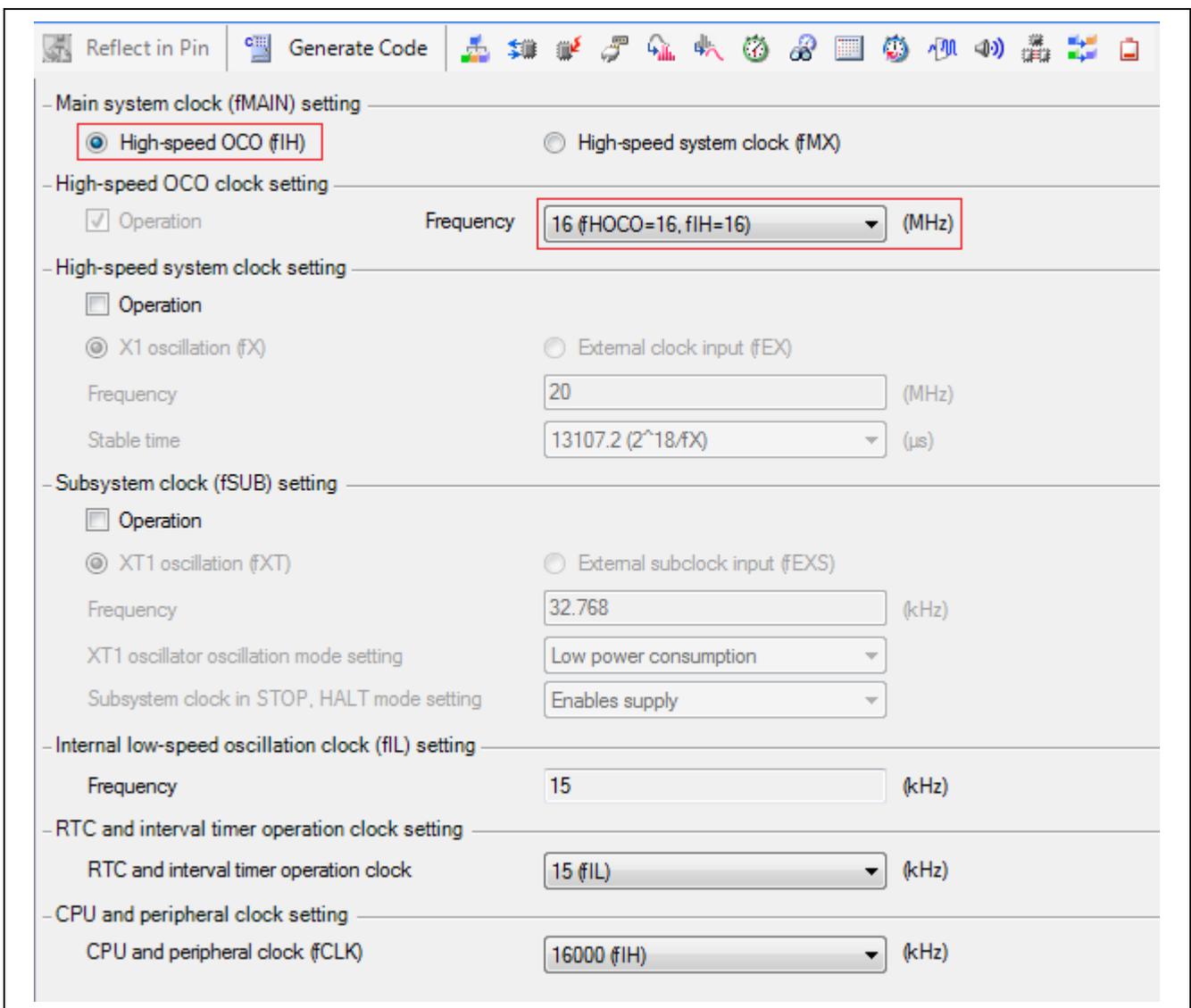


Figure 4.17 Setting Window in Code Generator (Clock)

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Set A/D Converter (ADC) which is the equivalent function to ADC of the R8C family.
Use A/D converter in software trigger mode, scan mode, and one-shot conversion mode.

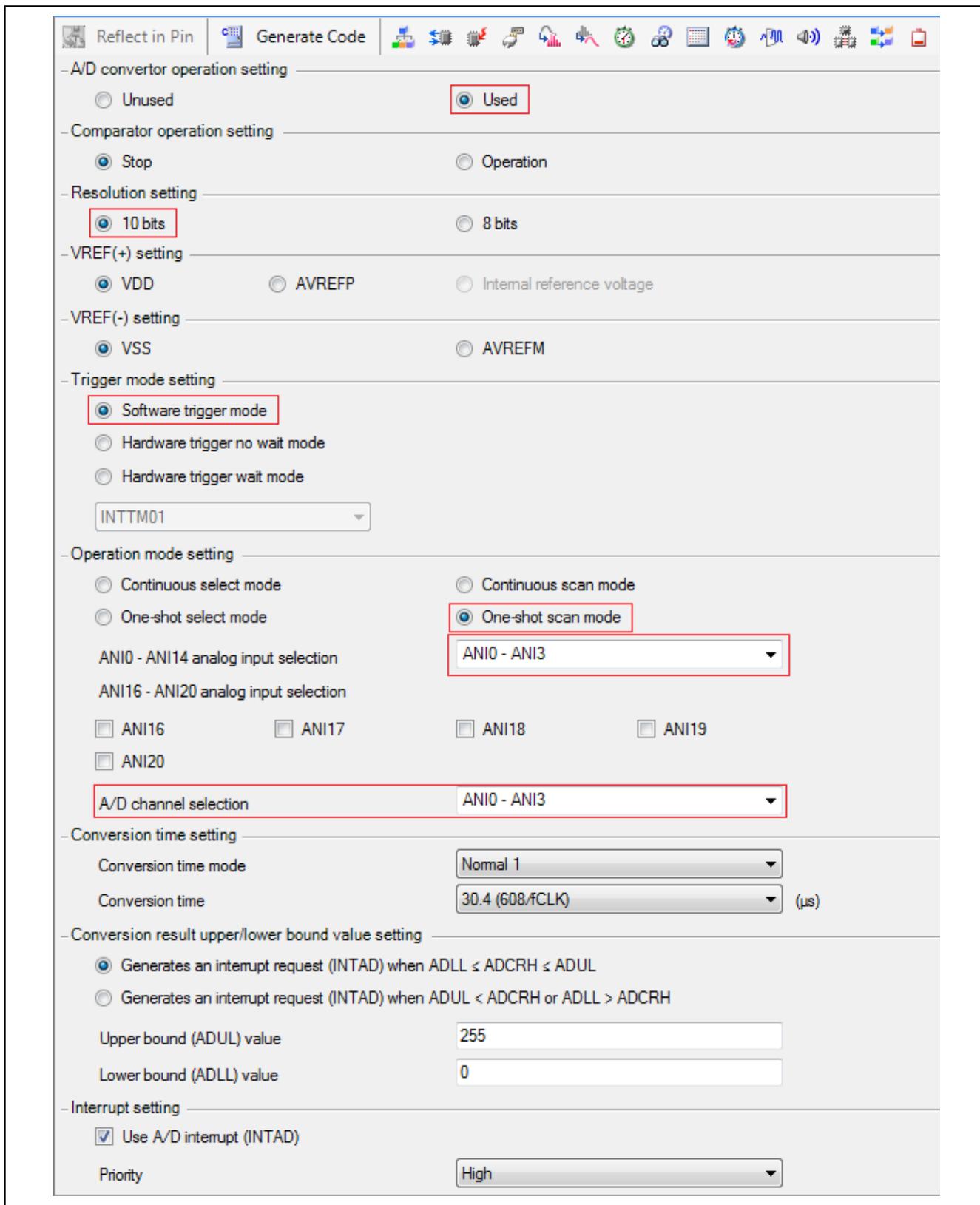


Figure 4.18 Setting Window in Code Generator (ADC)

Replacement Guide for R8C Family to RL78 Family (CcnvNC30)

Set Data Transfer Controller (DTC) which is the equivalent function to DTC of the R8C family.

In the sample program of "R8C/35C Group DTC Operation in Chain Transfers (R01AN0372E)", A/D converted values that have been stored to A/D registers (AD0 to AD3) are transferred to the internal RAM using the DTC chain transfer. But, in RL78/G14 Group, there is only one A/D conversion result register, so use DTC (normal mode, not use chain transfer). Perform A/D conversion on analog input voltage input to pins ANI0 to ANI3 while in scan mode, and use DTC transfer to store the A/D converted value to the RAM. Perform A/D conversion for individual pins successively. Every time A/D conversion for a pin is completed, store the converted result to the 10-bit A/D conversion result register (ADCR), activate the DTC, and transfer the A/D converted result from the ADCR register to the RAM. When A/D conversion and DTC transfer for all of the above pins are completed, an A/D conversion end interrupt request is generated.

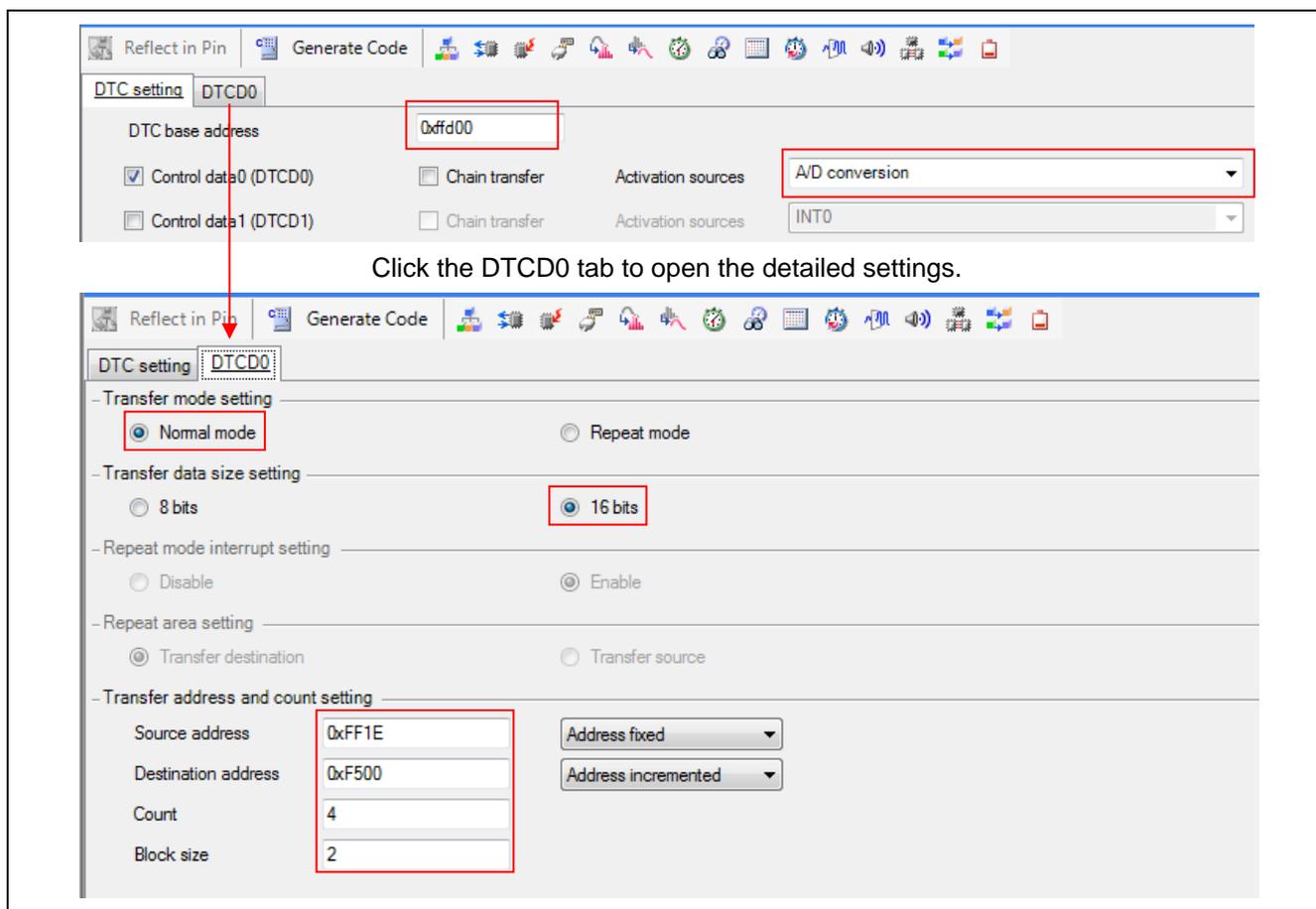


Figure 4.19 Setting Window in Code Generator (DTC)

- (3) Set ports, watchdog timer, voltage detection circuit, etc. based on your environment.
- (4) Click [Generate Code] to generate a file.

Replacement Guide for R8C Family to RL78 Family (CcnvNC30)

4.2.3 Adding Programs

Add the processes of symbol definition and the main function to the program with generated code. Use the programs with generated code for other programs (such as clock setting, ADC function and DTC function setting).

- main function

When the code generator for RL78/G14 is used, the R_Systeminit function is executed before the main function is executed. The R_Systeminit function performs the initial setting of the clock, ADC and DTC. Thus, only the process indicated in the red box is added manually. The R_DTCD0_Start function starts the operation of DTC. The R_ADC_Start function starts the operation of ADC.

Program for R8C

```
75 void main(void)
76 {
77     /* ==== Interrupt disabled ==== */
78     asm("FCLR I");
79
80     /* ==== Set High-speed on-chip oscillator clock to System clock ==== */
81     mcu_init();
82
83     /* ==== A/D converter initialize ==== */
84     ad_converter_enable();
85
86     /* ==== DTC initialize ==== */
87     dtc_enable();
88
89     /* ==== Interrupt enabled ==== */
90     asm("FSET I");
91
92     dtcen16 = 1; /* Activation of A/D interruption enable */
93
94     adst = 1; /* A/D conversion starts */
95
96     /* ==== Main loop ==== */
97     while(1){
98     }
99 }
```

Add contents corresponding to the red box manually.

r_main.c file for RL78/G14

```
59 void main(void)
60 {
61     R_MAIN_UserInit();
62     /* Start user code. Do not edit comment generated here */
63     R_DTCD0_Start(); /* DTC enabled */
64     R_ADC_Set_OperationOn();
65     R_ADC_Start(); /* A/D converter enabled */
66
67     while (1U)
68     {
69         NOP();
70     }
71     /* End user code. Do not edit comment generated here */
72 }
```

Figure 4.20 Replacement of main Function

Even if a DTC transfer request is generated, DTC transfer is held pending immediately when there are no commands in the instruction "while (1U)", so add the command "NOP ()" into the "while (1U)".

Replacement Guide for R8C Family to RL78 Family (CcnvNC30)

4.2.4 Other Modifications

If ADC is set with the code generator, interrupt processes are automatically generated. Add the user program to interrupt routine.

Variables for R8C

```
49  /* **** Global Variables **** */
50  #pragma ADDRESS ad_value    00600H    /* Destination address of A/D conversion data */
51  unsigned short ad_value[4];          /* A/D data from AN8 to AN11 addressed from */
52                                     /* 0x0600 to 0x0607 */
53
54  unsigned short an8_value;            /* AN8 value */
55  unsigned short an9_value;            /* AN9 value */
56  unsigned short an10_value;           /* AN10 value */
57  unsigned short an11_value;           /* AN11 value */
```

Add contents corresponding to
the red box manually.

r_cg_adc_user.c file for RL78/G14

```
37  #pragma address ad_value=0xFF500
38  unsigned short ad_value[4];          /* A/D data from AN10 to AN13 addressed from */
39                                     /* 0xFF500 to 0xFF507 */
40
41  unsigned short an0_value;            /* AN10 value */
42  unsigned short an1_value;            /* AN11 value */
43  unsigned short an2_value;            /* AN12 value */
44  unsigned short an3_value;            /* AN13 value */
```

Figure 4.21 Add Variables for Interrupt Program

Replacement Guide for R8C Family to RL78 Family (CcnvNC30)

Program for R8C

```
301 void ad_converter_int(void)
302 {
303     /* ==== Setting A/D conversion data ==== */
304     an8_value = (ad_value[0]&0x03FF); /* Setting AN8 value */
305     an9_value = (ad_value[1]&0x03FF); /* Setting AN9 value */
306     an10_value = (ad_value[2]&0x03FF); /* Setting AN10 value */
307     an11_value = (ad_value[3]&0x03FF); /* Setting AN11 value */
308
309     /* ==== A/D conversion starts ==== */
310     dtcct0 = 1; /* One-time is set to transfer */
311     dtcct1 = 1; /* One-time is set to transfer */
312     dtcen16 = 1; /* Activation of A/D interruption enable */
313     adst = 1; /* A/D conversion starts */
314 }
```

Add contents corresponding to the red box manually.

r_cg_adc_user.c file for RL78/G14

```
66 static void __near r_adc_interrupt(void)
67 {
68     /* Start user code. Do not edit comment generated here */
69     an0_value = (ad_value[0U] & 0xFFCOU) >> 6U;
70     an1_value = (ad_value[1U] & 0xFFCOU) >> 6U;
71     an2_value = (ad_value[2U] & 0xFFCOU) >> 6U;
72     an3_value = (ad_value[3U] & 0xFFCOU) >> 6U;
73     /* ==== A/D conversion starts ==== */
74     R_DTC_Create();
75     R_DTCDO_Start(); /* DTC enabled */
76     R_ADC_Start(); /* A/D converter enabled */
77     /* End user code. Do not edit comment generated here */
78 }
```

Figure 4.22 Add Interrupt Program

4.2.5 Sample Code After Replacement

Obtain the sample code "an-r01an3508ec0100-rl78-migrate.zip" from the Renesas Electronics Website. "rl78g14_migrate_dtc" in the "workspace" folder is the sample code that replaces the program of "R8C/35C Group DTC Operation in Chain Transfers (R01AN0372E)".

Replacement Guide for R8C Family to RL78 Family (CcnvNC30)

4.3 Conditions for Confirming Operations of Sample Programs

The operations of the sample codes after replacement are confirmed under the following conditions.

Table 4.1 Conditions for Confirming Operations

Item	Description
Microcontroller used	RL78/G14 (R5F104PJ)
Integrated development environment (CS+)	CS+ for CC V4.01.00 from Renesas Electronics Corp.
C compiler (CS+)	CC-RL V1.03.00 from Renesas Electronics Corp.
Integrated development environment (e2 studio)	e2 studio V5.2.0.020 from Renesas Electronics Corp.
C compiler (e2 studio)	CC-RL V1.03.00 from Renesas Electronics Corp.
Board used	Renesas original

5. Sample Code

The sample code is available on the Renesas Electronics website.

6. Reference Documents

User's Manual

RL78 Family User's Manual: Software (R01US0015E)
CC-RL Compiler User's Manual (R20UT3123E)
CS+ Code Generator Tool Integrated Development Environment User's Manual: Peripheral Function
Operation[CS+ for CC][CS+ for CA,CX] (R20UT3104E)
CcnvNC30 C Source Converter User's Manual (R20UT3685E)

Application Note

R8C/35A Group Clock Operation Using RTC (R01AN0079E)
R8C/35C Group DTC Operation in Chain Transfers (R01AN0372E)

(The latest versions can be downloaded from the Renesas Electronics website.)

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Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Dec. 22, 2016	-	First edition issued

General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

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1. Handling of Unused Pins

Handle unused pins in accordance with the directions given under Handling of Unused Pins in the manual.

- The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

2. Processing at Power-on

The state of the product is undefined at the moment when power is supplied.

- The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.
In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.

3. Prohibition of Access to Reserved Addresses

Access to reserved addresses is prohibited.

- The reserved addresses are provided for the possible future expansion of functions. Do not access these addresses; the correct operation of LSI is not guaranteed if they are accessed.

4. Clock Signals

After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.

- When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

5. Differences between Products

Before changing from one product to another, i.e. to a product with a different part number, confirm that the change will not lead to problems.

- The characteristics of Microprocessing unit or Microcontroller unit products in the same group but having a different part number may differ in terms of the internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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