

FAQs: RS-485 Transceivers

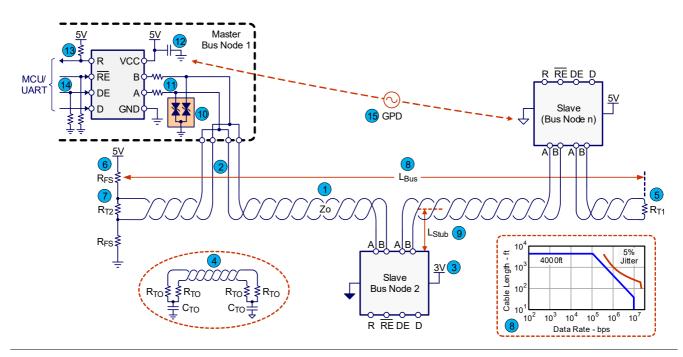
This application note addresses some frequently asked questions about the RS-485 Transceivers.

Contents

1.	RS-485 Design Check List	. 2
2.	Can I interface a 5V transceiver with a 3V controller?	. 3
3.	Can I mix 3.3V and 5V transceivers on the same bus?	. 3
4.	Can I use a 5Mbps transceiver in a product operating at <200kbps?	. 4
5.	What is the maximum stub length between backbone cable and transceiver?	. 5
6.	How do I wire a differential receiver Input to accept single-ended levels?	. 5
7.	How do I route a bus cable to avoid longs stubs?	. 6
8.	When do I need to terminate a Bus?	. 8
9.	What are the benefits of the ISL315xE transceivers?	. 9
10.	Why do RS-485 transceivers require high EFT immunity?	10
11.	How can I protect my RS-485 transceiver against surge transients?	10
12.	Do you have more RS-485 application collateral?	11
13.	Revision History	11



1. RS-485 Design Check List



- 1) Use twisted pair cable with: $Z_0 = 120\Omega \text{ or } 100\Omega$
- 2) Connect bus nodes using: Daisy-Chain
- 3) You can mix 3V and 5V transceivers on a bus
- 4) Terminate unused conductors with: $R_{T0} = Z_0/2 \text{ and } C_{T0} = 1 \mu F$
- 5) Terminate one cable end with: $R_{T1} = Z_0$
- 6) Apply fail-safe biasing to the other end with: $R_{FS} = K \times (1 + V_{CC(mim)}/V_{FS})$ $K = 27.8\Omega$ for $Z_0 = 120\Omega$ $K = 23.4\Omega$ for $Z_0 = 100\Omega$
- 7) Terminate this end with:

$$R_{T2} = \frac{2R_{FS} \times Z}{2R_{FS} - Z}$$

 ^NT2⁻ 2R_{FS}-Z₀
8) Determine maximum cable length with: Chart in bottom right corner 9) Make stub length no longer than:

- $L_{Stub} = 3 \times 10^4 \times t_r \times v$
- L_{Stub} = Stub length (m)
- t_r = Driver rise time (ns)
- v = Cable velocity (%)
- 10) For transient protection (ESD, EFT, Surge) use: SM712 or read app notes AN1976 to AN1979.
- 11) Limit transceiver input current during TVS clamping with 20Ω carbon-composite or MELF resistors.
- 12) Apply a capacitor of at least 0.1µF close to supply to ensure sufficient supply current during transceiver switching
- 13) Pull RO up with a $1k\Omega$ to $10k\Omega$ resistor to ensure RO is high when the receiver is disabled
- 14) Pull enable pins down with $1k\Omega$ to $10k\Omega$ resistors to ensure receiver is enabled and the driver is disabled during power-up.
- 15) For ±7V GPDs use ISL315xE or RAA78815x For ±25V GPDs use ISL3245xE or ISL3249xE For higher GPDs isolate ISL315xE with optocouplers



2. Can I interface a 5V transceiver with a 3V controller?

Yes. Because the output voltage levels of 3V MCUs are sufficiently high to properly drive the TTL input levels of 5V transceivers, controller outputs can be directly connected to transceiver inputs.

In the opposite direction however, the receiver output level at RO exceeds the MCU maximum input rating and level shifting is required. Figure 1 shows a simple solution using a small signal Schottky diode (D_1) and a pull-up resistor (R_{PU}) connected to the 3.3V controller supply. The other 10k Ω pull-up connected to the 5V transceiver supply is always needed to ensure RO is high when the receiver is disabled.

Then, when RO is high or high-impedance, the diode is reverse-biased and blocks current flow. In this case, the RxD input of the controller receives a logic high of 3.3V via R_{PU} . If RO is low, the diode is forward-biased and conducts, thus pulling RxD low.

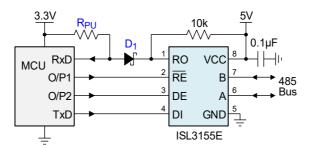


Figure 1. Level Shifting with Schottky Diode and Pull-Up Resistor

For a low RxD input capacitance of 5pF, the combination of a BAS70 Schottky diode (D₁) and a 10k Ω pull-up resistor (R_{PU}) provides crisp level-shifting up to about 1Mbps.

3. Can I mix 3.3V and 5V transceivers on the same bus?

Yes. The differential driver of an RS-485 transceiver produces a differential output voltage (V_{OD}) that swings around a common-mode level of $V_{CC}/2$. The differential receiver of an RS-485 transceiver, however, rejects any common-mode voltage and only detects differential signals. Therefore, eliminating the impact of the supply dependent common-mode level at the driver output.

It is therefore possible to have a 5V driver communicating with a 3V receiver, or a 3V driver communicating with a 5V receiver (Figure 2).

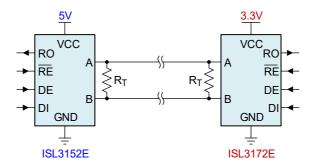


Figure 2. Interfacing 3V and 5V Transceivers on the Same Bus



4. Can I use a 5Mbps transceiver in a product operating at <200kbps?

It depends on the EMI requirements of the application the product will be used for.

The outputs of RS-485 transceivers with data rates below 1Mbsp are slew-rate limited to minimize signal overshoot and therefore, outgoing electromagnetic interference (EMI) to the environment.

High-speed transceivers are not slew-rate limited, which can increase EMI significantly, especially in long cable runs because of the higher cable inductance.

A good tool for deciding what transceiver data rate to apply for a given cable length, is the cable-length-versus data-rate characteristics provided in the Telecommunications Systems Bulletin, TSB-89-A (Figure 1).

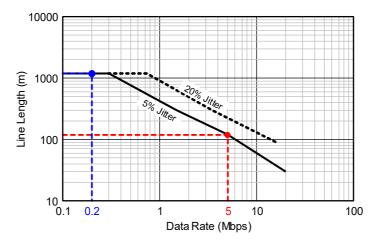


Figure 3. Maximum Line Length vs Data Rate

A simple measurement of the signal rise time out of a length of cable yields an estimation for the maximum length at a specific data-signaling rate using Equation 1 where $t_{r,f}$ is the 10%-to-90% transition (rise or fall) time of the signal at the end of the interconnection and T_{Bit} is the minimum bit width.

(EQ. 1)
$$t_{r,f} \le \frac{T_{Bit}}{2}$$

Obviously, a 200kbps transceiver, such as ISL81487L, easily clears a 1200m distance, while a 5Mbps transceiver, such as ISL81487E, only supports 1/10th of that distance, 120m.

Certainly, a 5Mbps transceiver can be operated at 200kbps, but a product manufacturer would have to specify its limitation in cable length.



5. What is the maximum stub length between backbone cable and transceiver?

A stub is the distance between the transceiver interconnection on a bus node circuit board and the main cable trunk. As stubs must not be terminated to avoid overloading the bus, a stub must be kept short to avoid turning into a transmission line. The stub length is calculated using Equation 2:

$$(\textbf{EQ. 2}) \qquad \textbf{L}_{stub} \leq \frac{t_r}{4} \times \textbf{v} \times \textbf{c}$$

with

- L_{Stub} = maximum stub length (m)
- t_r = driver rise time (ns)
- v = signal velocity of the cable as a factor of c
- c = Speed of light (3×10⁸m/s)

Table 1 lists the stub length for a variety of RS-485 Transceivers and their corresponding driver rise times.

Part	Data Rate (Mbps)	Rise Time (ns)	Stub Length	
Number			(m)	(ft)
ISL3152E	0.115	700	31.5	103
ISL8487E	0.25	250	11.25	37
ISL3155E	1	150	6.75	22
ISL81487E	5	3	0.14	0.4
ISL3158E	20			
ISL3159E	40	2	0.09	0.3
ISL3259E	100			

Table 1. Stub Length vs Driver Rise Times

6. How do I wire a differential receiver Input to accept single-ended levels?

Interfacing a logic gate to a differential receiver is simply accomplished by feeding the non-inverting receiver input with the gate output, while biasing the inverting input with a low-impedance reference voltage, V_{REF} . Do not rely on the receiver internal, high-impedance biasing, which might be sensitive to noise coupling, therefore, making it easy to shift. V_{REF} should lie between the lowest V_{OH} MIN and the highest V_{OL} MAX level:

(EQ. 3)
$$V_{\text{REF}} = \frac{V_{\text{OH}} M N + V_{\text{OL}} M A X}{2}$$

For a modern 1/8 unit-load receiver, you can produce V_{REF} with a voltage divider, or a low-cost linear voltage regulator.

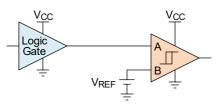


Figure 4. Wiring a Differential Input to Accept Single-Ended Levels

7. How do I route a bus cable to avoid longs stubs?

The most reliable method of routing an RS-485 network is by daisy-chaining its bus transceivers. Daisy-chaining means, running the bus cable as close as possible to an equipment (bus node), tapping off a short connection to the actual transceiver (stub), and continuing the cable run to the next bus node (Figure 5). This method requires the first and last bus node in the network to be terminated with a termination resistor (T), whose value matches the characteristic impedance of the RS-485 cable. Daisy-chaining ensures the shortest stubs, while allowing for long cable runs of several thousands of feet.

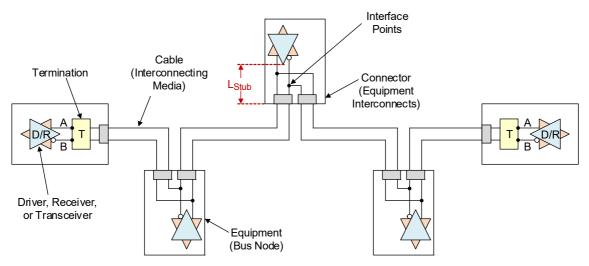


Figure 5. RS-485 Daisy-Chain Configuration Aiming for Shortest Possible Stubs

Other network configurations are acceptable, such as tapping off transceiver connections from a backbone cable by using junction boxes (Figure 6). This configuration method, however, requires the entire length from the backbone to the transceiver to be included in the stub length calculation.

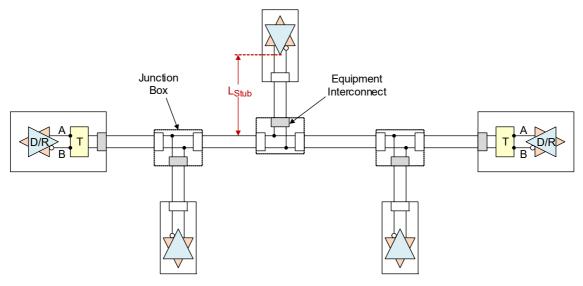


Figure 6. RS-485 Bus with Stub Cables Off a Main Backbone Cable

Configuring transceivers in a star configuration (Figure 7) is not recommended, as it requires the termination of each branch. Multiple terminations will electrically overload the output of a standard transceiver. However, Renesas' ISL315xE transceivers have been specifically designed for this type of application and can drive up to eight termination loads.

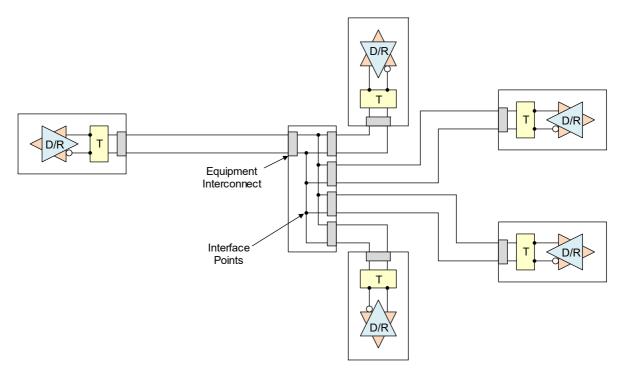


Figure 7. Star Configuration with L_{Stub} >> L_{Stub(max)} (Not Recommended for Standard Transceiver, but Possible with ISL315xE Transceivers, which can Drive Up to 8 Terminations)



8. When do I need to terminate a Bus?

A general guideline is to terminate transmission lines to minimize signal reflections on the line. A cable turns becomes a transmission line when its two-way propagation delay approaches the transition time of the signal source (driver rise/fall time). Practical measurements, however, lead to a rule-of-thumb, stating that termination should be applied if the two-way propagation time equals 50% of the driver rise time, which is equivalent to the single-way propagation being equal to 25% or ¼ of the driver rise time.

Note: This is the same equation used to calculate the maximum stub length, with the exception that the equation sign changes from \leq to >. In other words, terminate the line if its electrical length is:

(EQ. 4)
$$L_{\text{Line}} > \frac{t_r}{4} \times v \times c$$

with

- L_{Line} = Line length (m)
- t_r = Driver rise time (ns)
- v = Signal velocity of the cable as a factor of c
- c = speed of light $(3 \times 10^8 \text{ m/s})$

Another question arising from the above discussion could be: When would I not need termination?

If your application can tolerate a reflections for a limited time and you drive the bus at low data rate, so that the bit width is about ten times the two-way prop-delay, you can operate a bus without termination. In this case, line reflections would only occur in the first half of the bit width and settling for the remaining duration of a bit.



Figure 8. Line Reflections during the First Half of a Bit



9. What are the benefits of the ISL315xE transceivers?

The ISL315xE transceivers have high output drives, about 60% higher than that of standard transceivers, 3.1V versus 1.9V across a differential load of 54Ω (Figure 1).

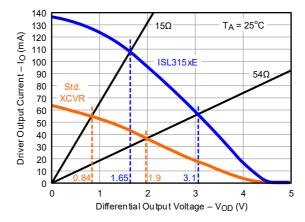


Figure 9. Output Drive Comparison: ISL315xE vs Standard Transceivers

This high driver capability allows them to drive

- up to 2600m (twice the maximum cable length suggested in TSB-89-A
- or twice the number of 1/8 unit-load transceivers (512 versus 256)
- or even be configured in a star configuration (eight versus two terminations, see Figure 10)

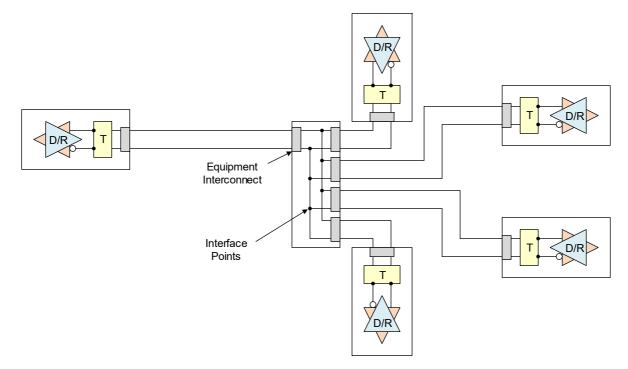


Figure 10. Star Configuration with L_{Stub} >> L_{Stub(max)} (Not Recommended for Standard Transceiver, but Possible with ISL315xE Transceivers, which can Drive up to 8 Terminations)

For more detailed information, consult the *ISL315xE datasheet*.

10. Why do RS-485 transceivers require high EFT immunity?

Industrial RS-485 networks often are subject to electrical fast transient (EFT) bombardment on a daily base. These transients originate when inductive loads, such as relays, switch contactors, or heavy-duty motors, are de-energized. They can corrupt the data traffic bus nodes and even damage transceivers, resulting in network downtime.

To withstand these transient without the need for external transient suppressors, the RAA78815x family of RS-485 transceivers was developed.

The RAA78815x transceivers were exposed to 5kV EFT transients and subsequently tested for parametric performance. None of the devices showed any parametric shift, thus making them the transceiver family with the industry's highest EFT immunity.

For detailed information consult application note R15AN0006EU0100 EFT, *Immunity of the RAA78815x Family of 5V RS-485/RS-422 Transceivers*.

11. How can I protect my RS-485 transceiver against surge transients?

Surge transients result from lightning strikes (direct strike or induced voltages and currents due to an indirect strike), and the switching of power systems, including load changes and short circuits switching.

Their energy content is about 100 times that of an EFT pulse train and more than 8-million times that of an ESD pulse, which makes them the most lethal form of overvoltage transients.

To protect against lethal surge transients, external transient suppressor devices (TVS) are recommended. For standard transceivers, whose internal ESD structures have lower trigger voltages in the range of 10V to 15V, series resistors with values of 10Ω to 50Ω are required to prevent interaction between the internal ESD diodes and the external TVS devices (Figure 11).

Overvoltage protected transceivers with ESD trigger levels of more than 60V, do not require these resistors. Instead, they can operate with more powerful TVS devices to achieve protection levels of up to several thousands of volts (Figure 12).

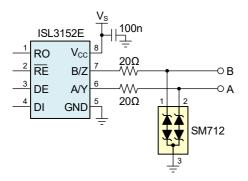


Figure 11. 500V Surge Protection for a Standard Transceiver

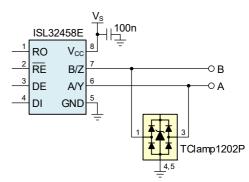


Figure 12. 3kV Surge Protection for an Overvoltage Protected Transceiver

For more in-depth information on the various transient immunity tests, types of transient suppressors, and protection schemes for standard and overvoltage protected transceivers, consult the following application notes:

- AN1976: Important Transient Immunity Tests for RS-485 Networks
- AN1977: Transient Voltage Suppressors: Operation and Features
- AN1978: Surge Protection for Renesas' Standard RS-485 Transceivers
- AN1979: Surge Protection simplified with Renesas' Overvoltage Protected (OVP) Transceivers

12. Do you have more RS-485 application collateral?

Table 2. List of Application Notes

File Number	Title		
AN1593	Add a Loss-of-Signal (LOS) Indicator to your RS-485 Transceiver		
AN1976	Important Transient Immunity Tests for RS-485 Networks		
AN1977	Transient Voltage Suppressors: Operation and Features		
AN1978	Surge Protection for Renesas' Standard RS-485 Transceivers		
AN1979	Surge Protection simplified with Renesas' Overvoltage Protected (OVP) Transceivers		
AN1980	Ground Potential Differences: Origin and Remedies		
AN1986	External Failsafe Biasing of RS-485 Networks		
AN1988	Isolated RS-485 Repeaters, the Backbone of Industrial Long Haul Networks		
AN1991	Isolating RS-485 Interfaces with High-Speed Digital Optocouplers		
R15AN0002 RS-485 Transient Protection in Industrial DC-Supply Buses			

Table 3. List of Technical Briefs

File Number	Title	
TB505	Understanding the Difference between HBM and IEC61000-4-2 ESD Immunity	
TB506	Functional Principles of RS-485 Drivers and Receivers	
TB507	Dual-Protocol Transceivers simplify Interface Designs	
TB508	Dual-Protocol Applications: Programmable versus Standard Transceivers	
TB509	Detecting Bus Signals correctly with Failsafe biased RS-485 Receivers	
TB510	Transmitting Full-Duplex Data over Single Twisted-Pair cable	
TB511	The Unit Load Concept	

Table 4. List of White Papers

How to simplify Industrial PC Interfaces	
Dual Protocol Transceivers ease the Design of Industrial Interfaces	
Overvoltage Protection for RS-485 Bus Nodes	
Design Considerations for High-Speed RS-485 Data Links	
RS-485 Transceiver Tutorial	
How to simplify Industrial PC Interfaces	

13. Revision History

Revision	Date	Description	
1.00	May 17, 2022	Initial release.	



IMPORTANT NOTICE AND DISCLAIMER

RENESAS ELECTRONICS CORPORATION AND ITS SUBSIDIARIES ("RENESAS") PROVIDES TECHNICAL SPECIFICATIONS AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS OR IMPLIED, INCLUDING, WITHOUT LIMITATION, ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for developers skilled in the art designing with Renesas products. You are solely responsible for (1) selecting the appropriate products for your application, (2) designing, validating, and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. Renesas grants you permission to use these resources only for development of an application that uses Renesas products. Other reproduction or use of these resources is strictly prohibited. No license is granted to any other Renesas intellectual property or to any third party intellectual property. Renesas disclaims responsibility for, and you will fully indemnify Renesas and its representatives against, any claims, damages, costs, losses, or liabilities arising out of your use of these resources. Renesas' products are provided only subject to Renesas' Terms and Conditions of Sale or other applicable terms agreed to in writing. No use of any Renesas resources expands or otherwise alters any applicable warranties or warranty disclaimers for these products.

(Rev.1.0 Mar 2020)

Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu, Koto-ku, Tokyo 135-0061, Japan www.renesas.com

Trademarks

Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners.

Contact Information

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit: www.renesas.com/contact/