

RZ/A1H Group

RZ/A1H Software Package for GR-PEACH Quick Start Guide

1. Introduction

This is the Quick Start Guide for the RZ/A1H Software Package which runs on the GR-PEACH, and the operation of Renesas e² studio.

The sample code in this software package can be used as the starting point for developing your own applications.

Table 1. All Samples of this package

Name	Description
SDK for Camera Sample	Displays the image from the attached camera and allows several image adjustments (for example brightness and contrast).
Touch Panel Sample	Detects a touch event and draws a cursor using JPEG decode.
Web Server Sample	The target board operates as a web server. You can control the LED and view the directory listing of any USB Mass Storage devices via a web browser on your PC.
USB Host Sample	Implements Mass Storage Class access via a file system accessed using commands in a console. Also implements HID class (mouse / keyboard).
USB Function Sample	HID class (mouse / keyboard) and CDC class are available.
Sound Sample (play / record)	'Play' demonstrates playing a fixed sound that is hard-coded in the sample code. 'Record' plays the sound present on the microphone input in real time.

List of Abbreviations and Acronyms

Table 2. List of Abbreviations and Acronyms

Abbreviation	Full Form
ADC	Analogue to Digital Converter
API	Application Programming Interface
ARM	Advanced RISC Machines
CDC	Communications Device Class
COM	COMmunications
DHCP	Dynamic Host Configuration Protocol
DTR	Data Terminal Ready
EEPROM	Electrically Erasable Programmable Read-Only Memory
FAT	File Allocation Table
GCC	GNU Compiler Collection
GSE	Guiliani Stream Editor
GUI	Graphical User Interface
HID	Human Interface Device
HMI	Human Machine Interface
H/W	HardWare
IAR	IAR Systems (https://www.iar.com)
I/O	Input/Output
IP	Internet Protocol
JTAG	Joint Test Action Group
LCD	Liquid Crystal Display
LED	Light Emitting Diode
MAC	Media Access Control
PC	Personal Computer
QSG	Quick Start Guide
QSPI	Quad Serial Peripheral Interface
RAM	Random Access Memory
RTOS	Real Time Operating System
RTS	Request To Send
SDK	Software Development Kit
TES	TES Electronic Solutions
TFT	Thin Film Transistor
URL	Uniform Resource Locator
USB	Universal Serial Bus
WDT	WatchDog Timer
WYSIWYG	What You See Is What You get

2. Preparation

2.1 Hardware

To run the sample applications included in this software package requires following hardware environment. User can choose the camera input either GR-PEACH Audio Camera Shield or GR-PEACH Wireless Camera Shield. Please refer the Table 3 and choose suitable hardware for user usecase.

- GR-PEACH
- GR-PEACH Audio Camera Shield
- CF5642-V2 Camera Module (OmniVision OC5642 sensor)
- GR-PEACH 4.3-inch LCD Shield
- GR-PEACH Wireless Camera Shield (Includes cmos camera module.)

User can purchase GR-PEACH and related optional boards from the page below.

<https://www.renesas.com/products/gadget-renesas/boards/gr-peach.html>

User can purchase the CF5642-V2 Camera Module from the page below.

https://www.alibaba.com/product-detail/CF5642C-V2-camera-module-OV5642_60431731038.html?fullFirstScreen=true

The Table 3 shows required hardware by each sample application.

Table 3. Required hardware for each sample application

Sample Application	Required Hardware
SDK for Camera Sample	• GR-PEACH • GR-PEACH 4.3-inch LCD Shield • GR-PEACH Audio Camera Shield + CF5642-V2 Camera Module • or GR-PEACH Wireless Camera Shield
Touch Panel Sample	• GR-PEACH • GR-PEACH 4.3-inch LCD Shield
Web Server Sample	• GR-PEACH • GR-PEACH Audio Camera Shield
USB Host Sample	• GR-PEACH • GR-PEACH Audio Camera Shield
USB Function Sample	• GR-PEACH
Sound Sample (play / record)	• GR-PEACH • GR-PEACH Audio Camera Shield

2.2 Tool

The RZ/A1H Software Package can be used in the following environment. Please check your environment before continuing.

Tools:

e² studio (free of charge)

IDE: e² studio v2023-10 or later

Tool Chain: GNU ARM Embedded Toolchain 9-2020-q2-update

Available from <https://developer.arm.com/open-source/gnu-toolchain/gnu-rm>

Target Board:

Product code: GR-PEACH

ICE (In-circuit emulator):

On GR-PEACH environment, user do not need to prepare an ICE for programming and debugging.

GR-PEACH supports Arm Mbed DAPlink feature for rapid prototyping. DAPlink provides features below.

- drag-and-drop programming (MSC)
- a virtual serial port (CDC)
- CMSIS-DAP based debugging (HID)

For more detail of DAPlink specification, please refer following page.

<https://os.mbed.com/handbook/DAPLink>

Bootloader:

This package includes a bootloader as a binary table data in source code. If user need to customize the boot loader, download the source code from the following URL:

Japan

<https://www.renesas.com/software/D6001196.html>

America, Brazil, Europ/Middle East/Africa

<https://www.renesas.com/software/D6000656.html>

Asia

<https://www.renesas.com/software/D6001197.html>

Please refer section 6 of above sample's application note (R01AN3093) for boot loader customization.

2.3 Virtual Serial Port Connection

Connect a USB cable between Serial(USB) on the GR-PEACH and a Windows™ PC, this provides a USB virtual serial port.



When the GR-PEACH is first connected the PC will look for a suitable driver. This driver is installed during the installation process, and your PC should automatically find and install it. It will report that it is installing a driver and then that the driver has been installed successfully. The COMx port number allocated to the virtual serial port can be found in Windows™ Device Manager.

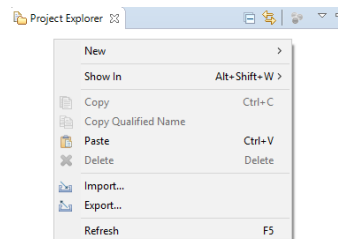
2.4 Serial Terminal

Start a serial terminal program (such as PuTTY, HyperTerminal or Tera Term) using the configuration below:

- Baud Rate: 115,200
- Data Bits: 8
- Parity: None
- Stop Bits: 1
- Flow Control: None
- COM Port: As shown in Windows™ Device Manager

2.5 Importing the Software Package into the IDE

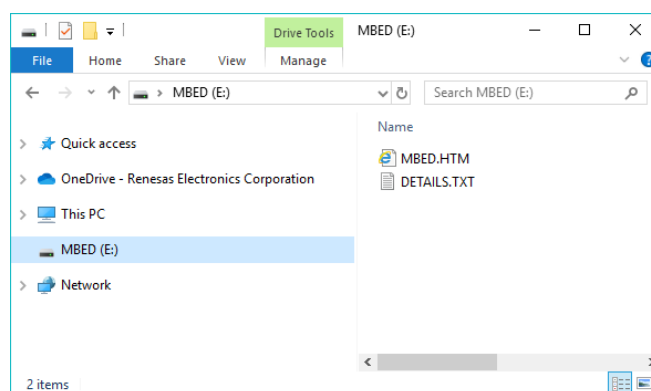
- The software package is distributed as an archive file. The build project for this software package can be imported into e² studio from the **Project Import** menu.
- Launch e² studio from the start menu.
- Set the 'RZA1_SoftwarePackage' directory which has 'RZ/A1H_Sample' sub-directory for the workspace directory.
- In the e² studio welcome screen, click **workbench**.
- Right-click in the **Project Explorer** window and select **Import**.
- Under **Import Source** select **General** → **Existing Projects into Workspace** and click **Next**.
- Select **Browse** to the right of **Select root directory**, and the **Browse For Folder** dialog box will appear.
- Press **OK**.
- Ensure the 'RZ/A1H_Sample' project is selected, then click **Finish**.



2.6 Build and Download to Target Board

When the GR-PEACH is first connected the PC detects the GR-PEACH as MSC class device.

User can download the program by drag-and-drop the program binary file to MBED drive.



- Select the 'RZ/A1H_Sample' project with a left click, then click the arrow next to the (build) button and select **HardwareDebug** from the drop-down menu.
- e² studio will now build the project (see note).
- Once this is complete, the 'RZA1H_Sample.bin' file is created in 'RZA1H_Sample\HardwareDebug'.
- Drag-and-drop the 'RZA1H_Sample.bin' to MBED drive to download the program.

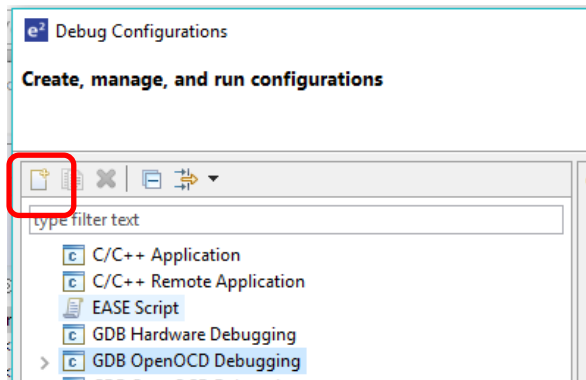


Note: Please chose a workspace path that is not too long. Long paths can cause build errors.

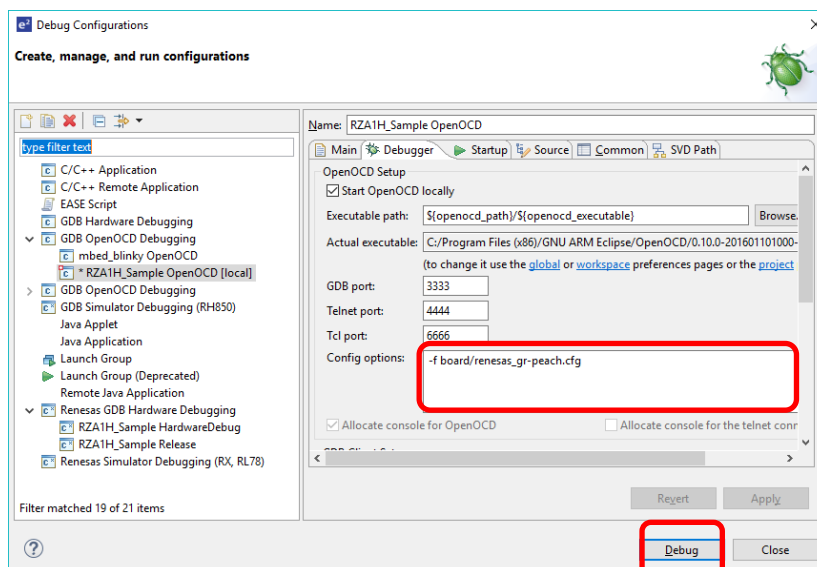
2.7 CMSIS-DAP based debugging

User can debug without ICE by using the CMSIS-DAP based debugging feature on GR-PEACH.

This section shows how to launch the debug environment and debugging.

- Refer the page below and follow the procedure from step 1 to step 8 to create debug environment.
<https://os.mbed.com/teams/Renesas/wiki/Exporting-to-e2studio-with-CMSIS-DAP-DBG>
- Reconnect USB cable.
- Select the 'RZ/A1H_Sample' project and select 'Run' -> 'Debug Configurations...'.

- Select the 'GDB OpenOCD Debugging' and click 'New launch configuration' button.

- Specify 'HardwareDebug/RZA1H_Sample.elf' to 'C/C++ Application:' of Main tab.
- Move to Debugger tab and specify the '-f board/renesas_gr-peach.cfg' to 'Config options', and click 'Debug'



3. Running Sample Application

You can set enable or disable for each sample application by editing the definitions in the configuration file `application_cfg.h`. This file is located in `RZA1H_Sample\src\renesas\configuration`. Note that after making changes to `application_cfg.h`, you will need to rebuild and then download the code to reflect your chosen applications. Please bear in mind that not every application can be concurrently enabled due to target board specification and memory size limitations. If a sample application is enabled, its operation can be controlled via a terminal console as shown in section 4. All available commands are shown in the following tables.

Table 4. Console Commands (1 / 3)

Command Type	Command	Description
General Commands	<code>date DD/MM/YYYY <CR></code>	Set the date
	<code>time hh:mm:ss <CR></code>	Set the time
	<code>mem a l <CR></code>	Reads memory from address H'a length H'l
	<code>drivers <CR></code>	List driver table. Note that drivers may not be loaded
	<code>handles <CR></code>	List opened driver information
	<code>ver <CR></code>	Show the application version
	<code>mperf <CR></code>	Check the speed of writing to RAM
Platform Commands	<code>led a s <CR></code>	Sets LED a state to on (s = 1), off (s = 0), toggle (s = ^)
	<code>restart <CR></code>	Restart the system with a WDT reset
	<code>logout <CR></code>	Exit a login shell
	<code>sys <CR></code>	Shows the system resource usage information
	<code>task <CR></code>	List tasks
	<code>help <CR></code>	Show the help screen
	<code>tsdemo <CR></code>	Run the touch screen demo
Camera SDK Commands (refer to section 3.5)	<code>sdk <CR></code>	Run the SDK for camera demo (requires screen and camera modules)
USB HID class Mouse Commands (refer to section 3.4)	<code>hidmouse <CR></code>	Provides HID mouse implementation, follow on screen instructions
USB CDC class (refer to section 3.4)	<code>cdcconsole <CR></code>	Run CDC console, requires a terminal program
Sound Application Commands (refer to section 5.8)	<code>play <CR></code>	Run the play sound application
	<code>record <CR></code>	Run the record sound application

Table 5. Console Commands (2 / 3)

Command Type	Command	Description
USB Commands	usbm <CR>	Monitors the data from a USB mouse
	usbk <CR>	Invokes a USB console reading data from a USB keyboard
USB Mass Storage Commands (*) (refer to section 3.3)	vol <CR>	Volume information for the working drive
	type f <CR>	Write text file f to the console
	copy s d <CR>	Copy file 's' to destination 'd'
	view f <CR>	View contents of file 'f'
	dir <CR>	List the working directory
	pwd <CR>	Print the working directory
	cd d <CR>	Change working directory to 'd'
	del f <CR>	Delete file 'f'
	md n <CR>	Make 'n' in the working directory
	rd d <CR>	Delete directory 'd'
	ren s d <CR>	Rename / move file 's' to 'd'
	disk <CR>	List the available disk drives
	eject d <CR>	Eject disk 'd'
	dismount <CR>	Dismount all mounted drives
	mount <CR>	Mount all Mass Storage devices
USB MS Test Commands	rperf f <CR>	Read file performance test
	wrperf f <CR>	Write file performance test
USB CDC Serial Port Commands	sopen <CR>	Opens a CDC device
	sclose <CR>	Closes an open CDC device
	sctlst <CR>	Perform control API tests for the CDC driver
	sttx n <CR>	Transmit n kbytes of data through the CDC device
	sloop <CR>	Loops-back received characters through the CDC device
	sbaud n <CR>	Set the baud rate to n
	scontrol n <CR>	Assert / Deassert RTS / DTR control signals n = 1 or 0
	sparity p <CR>	Sets the parity to N = none, E = Even, O = Odd
	sstop s <CR>	Sets the number of stop bits 1, 1.5 or 2
	sline <CR>	Returns the line status
	sbreak <CR>	Sets / clears the break signal
	stest s <CR>	Test all CDC driver functions with a loop-back connector
	sloopall <CR>	Loop-back test on maximum of 4 CDC devices (default baud rate)

*Note: After connecting USB MSC devices, enter a drive letter such as 'A:[Enter]' before using any of the USB mass storage commands. Please refer to section 3.3.

Table 6. Console Commands (3 / 3)

Command Type	Command	Description
Ethernet Platform Commands (refer to section 3.2)	ipconfig -o <CR>	Where o is one of the or more following options: -r = Reset to default settings -i:xxx.xxx.xxx.xxx = Set IP address -m:xxx.xxx.xxx.xxx = Set IP address mask -g:xxx.xxx.xxx.xxx = Set DHCP server gateway address -on = Enable DHCP -off = Disable DHCP -all = Display current settings (no option) = Command list display
	ifconfig -o <CR>	Where o is one of the above options

3.1 General Functionality

- The serial terminal will show the welcome banner.
- Type '?'<CR> on the serial terminal to show a list of all available commands.
- All commands are invoked once <CR> is pressed after entering the command. This is not shown again in this document.

3.2 Web Server Sample

In `application_cfg.h` the two definitions 'R_SELF_LOAD_MIDDLEWARE_ETHERNET_MODULES' and 'R_SELF_LOAD_MIDDLEWARE_USB_HOST_CONTROLLER' should both be set to R_OPTION_ENABLE when using this sample.

Connect GR-PEACH and GR-PEACH Audio Camera Shield before running this sample. And ensure that the JP1 mounted on GR-PEACH Audio Camera Shield MUST be shorted to use USB connection.

Please step through the following procedure to use this application.

3.2.1 Setting MAC Address

The MAC address can be specified in `application_cfg.h`.

When the 'R_SELF_LOAD_MIDDLEWARE_ETHERNET_MODULES' is set R_OPTION_ENABLE, the configMAC_ADDR0 to configMAC_ADDR5 are used as MAC address.

3.2.2 Ethernet Functionality

- IP address configuration depends on whether a static IP address is to be used, or if it's going to be assigned by a DHCP server:

Table 7. DHCP and Static IP Configuration

DHCP Configured	Static IP Configuration
a) Type 'ipconfig -on -s' to enable DHCP and save settings to EEPROM.	a) Type the following on a single line: ipconfig -i:ip-address -m:subnet-mask -g:gateway-address -off -s Where addresses and masks are in the format xxx.xxx.xxx.xxx for example: 192.168.172.123.
b) Connect the Ethernet cable to CN4. The serial console will display the allocated ip-address.	b) Connect the Ethernet cable to CN4.

- On the remote PC launch a web browser and enter the following in to the address bar:
http://ip-address
Where ip-address is the address used or allocated in the step above.
- The web browser will display web pages for the demonstration loaded from the target board.

3.2.3 Web Pages

- The landing web page shows an image of the target board and information about the demonstration. On the left there is a check box which controls the target board user LED.
- The second tab (**Mass Storage**) shows the directory listing of any USB Mass Storage devices inserted into the target board. Clicking on a file name will open it in the PC web browser.
- On the left is a USB read / write speed test facility. The drive and test file size are selected from the drop down and the test started by clicking **Test**.

Note: Due to limitations of some USB Mass Storage Class Devices, very large test sizes may not work as expected.

- The final tab (**Administration**) demonstrates a simple secure webpage implemented on the target board. The sample is not intended to be a truly secure solution, only an example. The default login credentials are: Username: 'admin', Password: 'password'. These can both be changed on the **Administration** page or via the serial terminal.
- The Software Package web server can support many simultaneous connections over Ethernet.

3.3 USB Host Sample

The definition 'R_SELF_LOAD_MIDDLEWARE_USB_HOST_CONTROLLER' in the `application_cfg.h` configuration file should be set to `R_OPTION_ENABLE` in order to use this sample.

Ensure that 'R_SELF_INSERT_APP_HID_MOUSE' and 'R_SELF_INSERT_APP_CDC_SERIAL_PORT' are both set to `R_OPTION_DISABLE`.

Connect GR-PEACH and GR-PEACH Audio Camera Shield before running this sample. And ensure that the JP1 mounted on GR-PEACH Audio Camera Shield MUST be shorted to use USB connection.

Multiple USB devices may be connected to the GR-PEACH Audio Camera Shield using a USB hub with four ports or less (single tier). To provide sufficient power for all devices the hub must be externally powered. For this QSG only one device will be used at a time and inserted directly in to GR-PEACH Audio Camera Shield.

Please step through the following procedure to use this application:

- Insert a FAT formatted Mass Storage Device (USB memory stick, hard drive, etc.) into CN20 of GR-PEACH Audio Camera Shield.
- Type 'drivers' - details of the USB device will be shown.
- Type 'a.' - the prompt changes to confirm the drive change.
- Type 'dir' - a directory listing of the drive will be shown.
- Type '?' to see the available commands for file and folder manipulation.
- Remove the Mass Storage Device from CN20 ready for the next demonstration.

3.4 USB Function Sample

The definitions 'R_SELF_INSERT_APP_CDC_SERIAL_PORT' and 'R_SELF_INSERT_APP_HID_MOUSE' in the configuration file `application_cfg.h` should be set to enable in order to use this application.

Ensure that the define 'R_SELF_LOAD_MIDDLEWARE_USB_HOST_CONTROLLER' is disabled by setting it to `R_OPTION_DISABLE`.

Please follow the steps below to use this application:

3.4.1 USB CDC Functionality

- Connect a USB cable from the PC to the red portion USB connector on the GR-PEACH.



- Use the 'cdcconsole' command to bring up the CDC functionality.
- Follow the instructions on the debug terminal.
- Using the host PC's Device Manager, confirm that a new COM port is added when the 'cdcconsole' command is running.
- Ensure that another serial terminal program is configured and press any key on the first serial terminal program.
- Connect your second terminal to the virtual COM port.
- The 'cdcconsole' application will echo what is typed on the virtual console.

3.4.2 USB HID Mouse Functionality

- Use the 'hidmouse' command to bring up the mouse functionality.
- Again the '?' command shows the available commands for this functionality.
- To move the PC mouse, use the command 'movexy' followed by a desired x and y coordinate.
- Use 'button1' and 'button2' for the left and right click, respectively.
- To end the session, use the 'logout' command.

3.5 SDK for Camera Sample

The definition 'R_SELF_INSERT_APP_SDK_CAMERA' in the configuration file `application_cfg.h` should be set to `R_OPTION_ENABLE` in order to use this application.

Connect GR-PEACH, GR-PEACH Audio Camera Shield, CF5642-V2 Camera Module, and GR-PEACH 4.3-inch LCD Shield before running this sample.

Please follow the steps below to use this application:

- Type 'sdk' <CR> on the serial terminal to view available commands.
- The SDK for Camera application will be launched.
- Several image adjustments can be made. Please refer to the SDK for Camera Application Note (R01AN5094).

3.6 Touch Panel Sample

The definition 'R_SELF_INSERT_APP_TOUCH_SCREEN' in the configuration file `application_cfg.h` should be set to `R_OPTION_ENABLE` in order to use this application.

Connect GR-PEACH, and GR-PEACH 4.3-inch LCD Shield before running this sample.

Please follow the steps below to use this application:

- Type 'tsdemo' <CR> on the serial terminal to view available commands.
- The Touch Panel application will be launched.
- The application will detect a touch event and draw a small green rectangle at the coordinates of the event. Please refer to the Touch Panel Utility Application Note (R01AN5096).

3.7 Sound Sample

The definition 'R_SELF_INSERT_APP_SOUND' in the configuration file `application_cfg.h` should be set to `R_OPTION_ENABLE` in order to use this application.

Connect GR-PEACH and GR-PEACH Audio Camera Shield before running this sample.

Please follow the steps below to use this application:

- Plug in a headphone to connector CN17 of GR-PEACH Audio Camera Shield, and line-in jack to connector CN16 of GR-PEACH Audio Camera Shield.
- Type 'play' <CR> on the serial terminal to execute the sound functionality.
- Sound will play in the left and right channels.
- Type 'record' to listen to the line-in input.

4. Tips

4.1 Using un-cached internal RAM

4.1.1 Allocating a variable to un-cached internal RAM

To allocate a variable to un-cached internal RAM, add the following string after the declaration of the variable:

```
int foo __attribute__((section(".VRAM_SECTION0")));
```

4.1.2 Changing the size of un-cached internal RAM

To increase the size of un-cached internal RAM, open `src\renesas\compiler\linker_settings.ld` and increase the value of `VRAM_LENGTH`. And reduce the value of `RAM_LENGTH` by the same value you increased for the value of `VRAM_LENGTH`.

From ld File

```
VRAM_END      = 0x20A00000;
VRAM_LENGTH   = 0x200000;
VRAM_START    = VRAM_END - VRAM_LENGTH;
```

4.1.3 Using Mirror memory

RZ/A1H has the mirror memory. If you modified `src\renesas\compiler\linker_settings.ld` to use the mirror memory. If desired, modify `src\renesas\compiler\asm\ttb_init.S` to specify the attribute of the memory.

In ld file simply setting should swap from non-Mirrored to Mirrored RAM select only 1

```
#VRAM_END      = 0x20A00000;                # Normal RAM
VRAM_END       = 0x60A00000;                # Mirrored RAM
VRAM_LENGTH    = 0x200000;
VRAM_START     = VRAM_END - VRAM_LENGTH;
```

In `ttb_init.S` where

```
/* ---- Parameter setting to level1 descriptor (bits 19:0) ---- */
/* Strongly-ordered memory
   B-000000000000000000000000110111100010 */
.equ TTB_PARAM_STRGLY, 0x0DE2
/* Outer and inner not cache normal memory
   B-000000000000000000000000110111100010 */
.equ TTB_PARAM_NORMAL_NOT_CACHE, 0x1DE2
/* Outer and inner write back, write allocate normal memory (Cacheable)
   B-000000000000000000000000110111101110 */
.equ TTB_PARAM_NORMAL_CACHE, 0x1DEE

.equ M_SIZE_RAM_M, 10      /* [Area11] Internal RAM (mirror) */
```

Modify the attribute

```
setting_area11:
    LDR r3, =M_SIZE_RAM_M
    LDR r4, =TTB_PARAM_NORMAL_NOT_CACHE
    BAL init_counter
```

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Apr.17. 2020	-	First Edition issued
1.10	Oct.11. 2021	-	Toolchain updated, template applied
1.20	Jul.20. 2022	16	Change library related build options Added section 4 Tips
1.31	Nov.24.2023	4	Updated the version of e2 studio

General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Precaution against Electrostatic Discharge (ESD)

A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity.

Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.

2. Processing at power-on

The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power reaches the level at which resetting is specified.

3. Input of signal during power-off state

Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.

4. Handling of unused pins

Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.

5. Clock signals

After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.

6. Voltage application waveform at input pin

Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between V_{IL} (Max.) and V_{IH} (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between V_{IL} (Max.) and V_{IH} (Min.).

7. Prohibition of access to reserved addresses

Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.

8. Differences between products

Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

Notice

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Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,
Koto-ku, Tokyo 135-0061, Japan
www.renesas.com

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